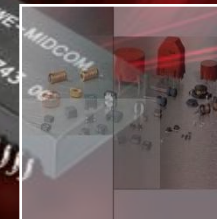


2011

WÜRTH ELEKTRONIK EISOS GMBH & CO. KG

EMC SEMINAR 2011



Agenda

- EMC
- Magnetic fields
- Filtering & Signals
- Insertion loss calculation
- Filter topologies



EMC



EMC - Definition



- Electro-Magnetic Compatibility
- The ability of electronic equipment and or systems to operate in its environment without causing unacceptable interferences.



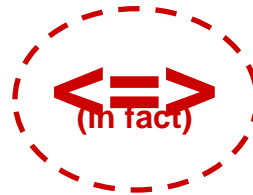
EMC - Definition

Transmitter/Receiver

- devices which operate with other devices in one electro magnetic environment

Source / Transmitter

- mobile base station
- electro engine
- high power electronic
- mobile device (Laptop, PDA, Mobile phones etc.)
- discharge of static capacity
 - ESD (Electro Static Discharge – “Person”)
 - LEMP (Lighting Electro Magnetic Pulse)



Load / Receiver

- receivers (TV, Radio, ...)
- white & brown goods
- IT systems
- measurment and control tech. (e.g. sensors)
- medical electronics (e.g. pace maker)

EMC - Requirement

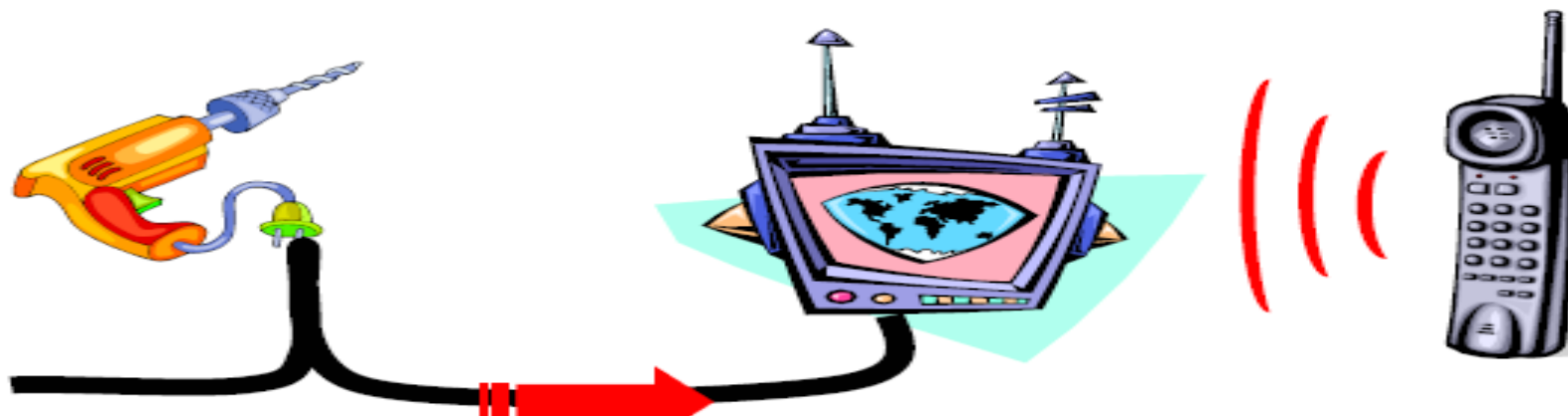
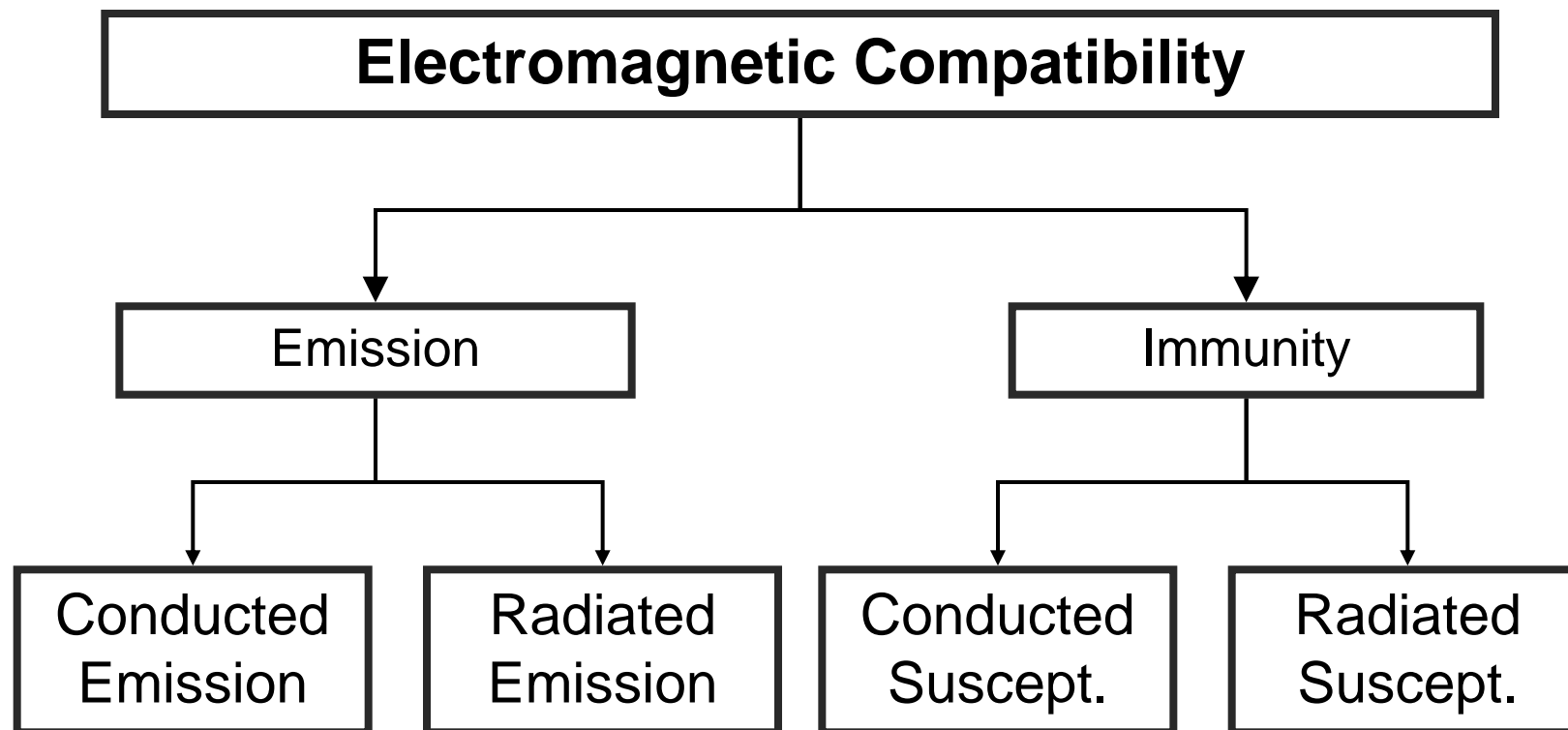
Beginning from definition EMC

- Basic requirement to devices:

- 1) decreasing of
 - 2) prevention of
- } emission
- 3) existence of
- noise immunity

effective protection **TO AND AGAINST** other electronic devices

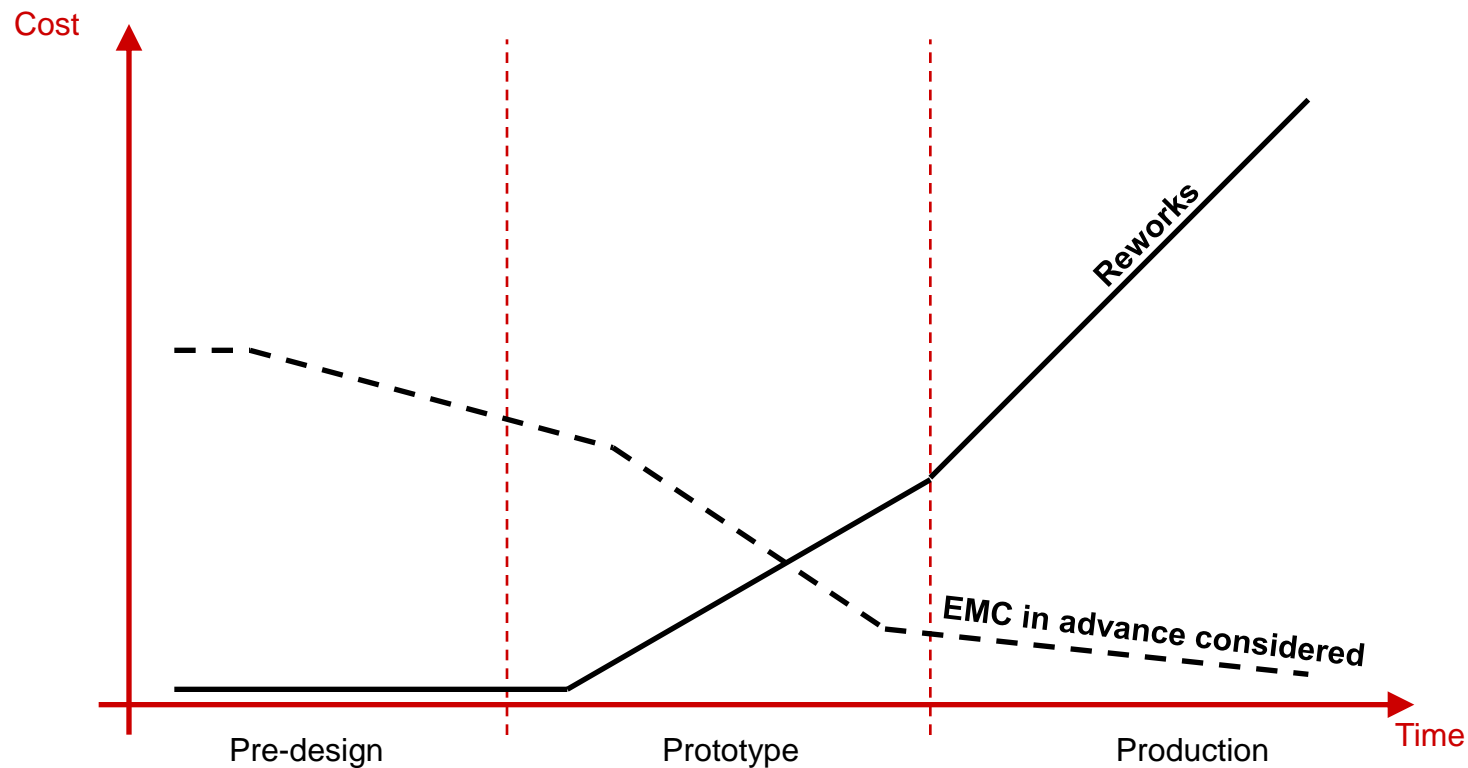
EMC - Requirement



EMC - Effect

Economical point of view:

- Depends when you will start to design EMC conform



EMC - Norms



Since 1996 it is a must, that in EU all electronic devices are CE conform according to 2004/108/EC

World wide:

IEC 61000-1	-	Introduction, terms and conditions
IEC 61000-2	-	Classification of electromagnetic environments
IEC 61000-3	-	Limits and disturbance levels
IEC 61000-4	-	Testing and measurement techniques
IEC 61000-5	-	Installation and mitigation guidelines
IEC 61000-6	-	Generic standards

European norms

	<u>Emission</u>	<u>Immunity</u>
Information technology equipment	EN 55022 (P)	EN 55024 (P)
Industrial plant	EN 50081-2 (FG)	EN 50082-2 (FG)
Industrial, scientific and medical equipment RF equipment	EN 55011 (P)	EN 50082-2 (FG)
Signalling on low-voltage electrical installations	EN 50065 (P)	EN 50082-2 (FG)
Sound and television broadcast receivers	EN 55013 (P)	EN 55020 (P)
Requirements for household appliances, electric tools etc.	EN 55014-1 (P)	EN 55104-2 (P)

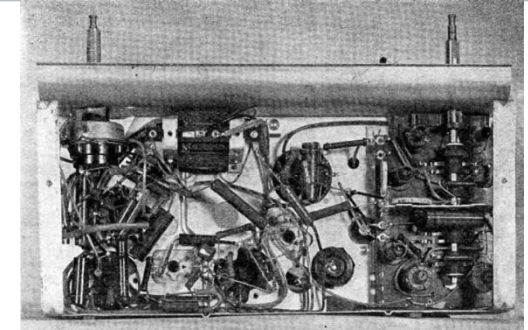
EMC – General solutions



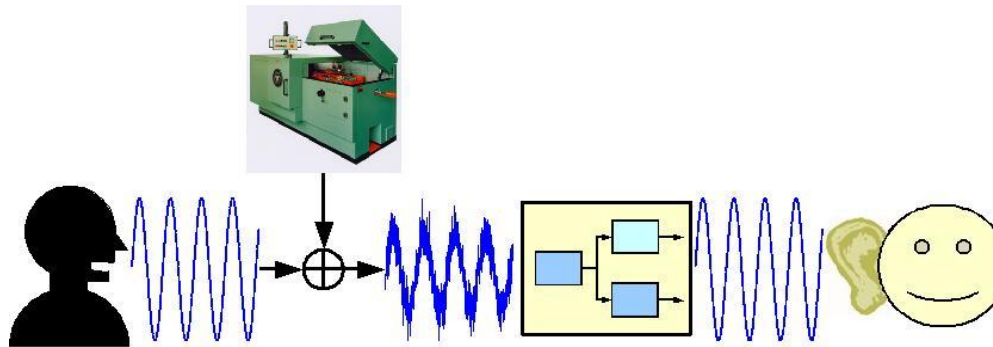
1) Optimization of the layout:

→ situation:

one Problem but to many “solutions”/opinions



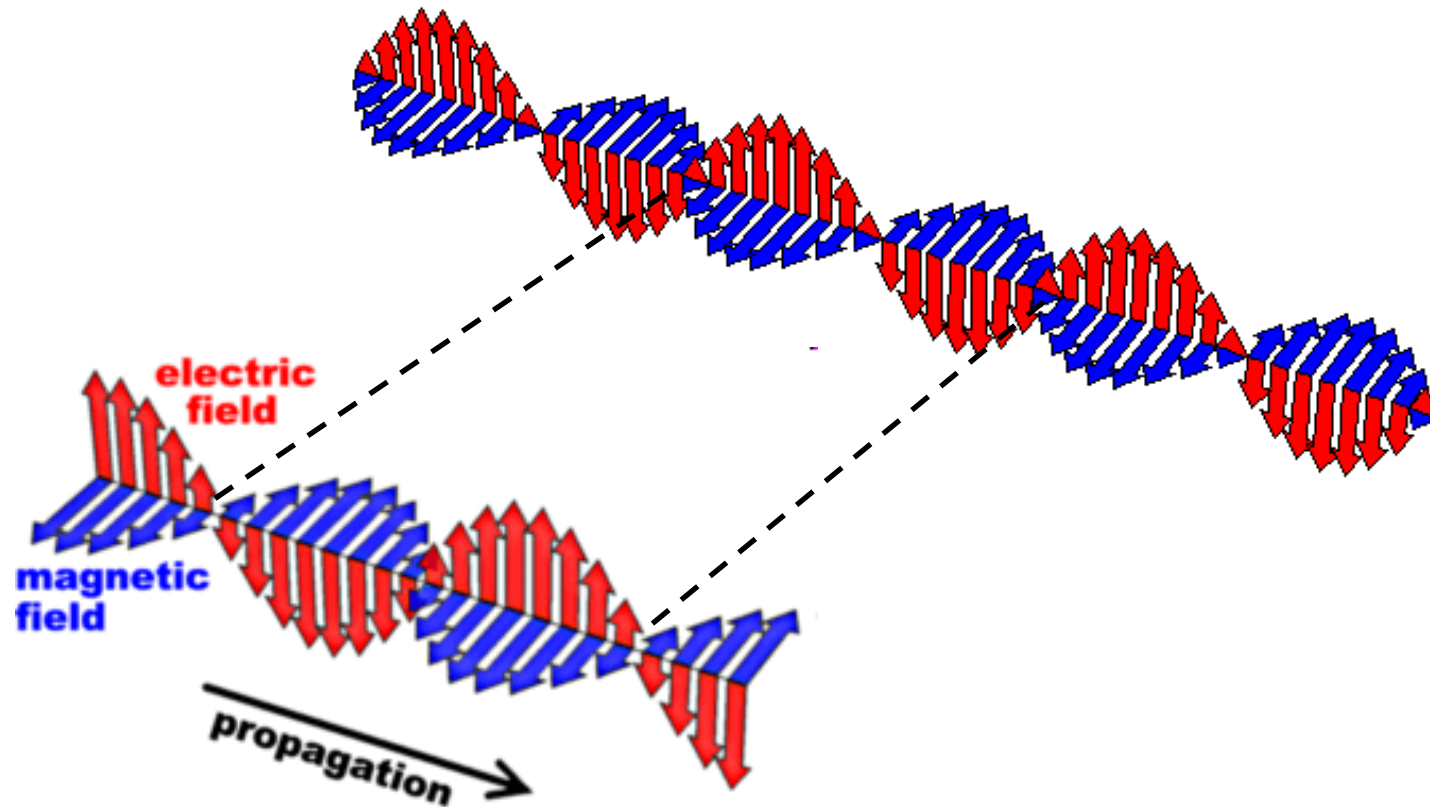
2) Filtering



3) Shielding



EMC – Electromagnetic Wave



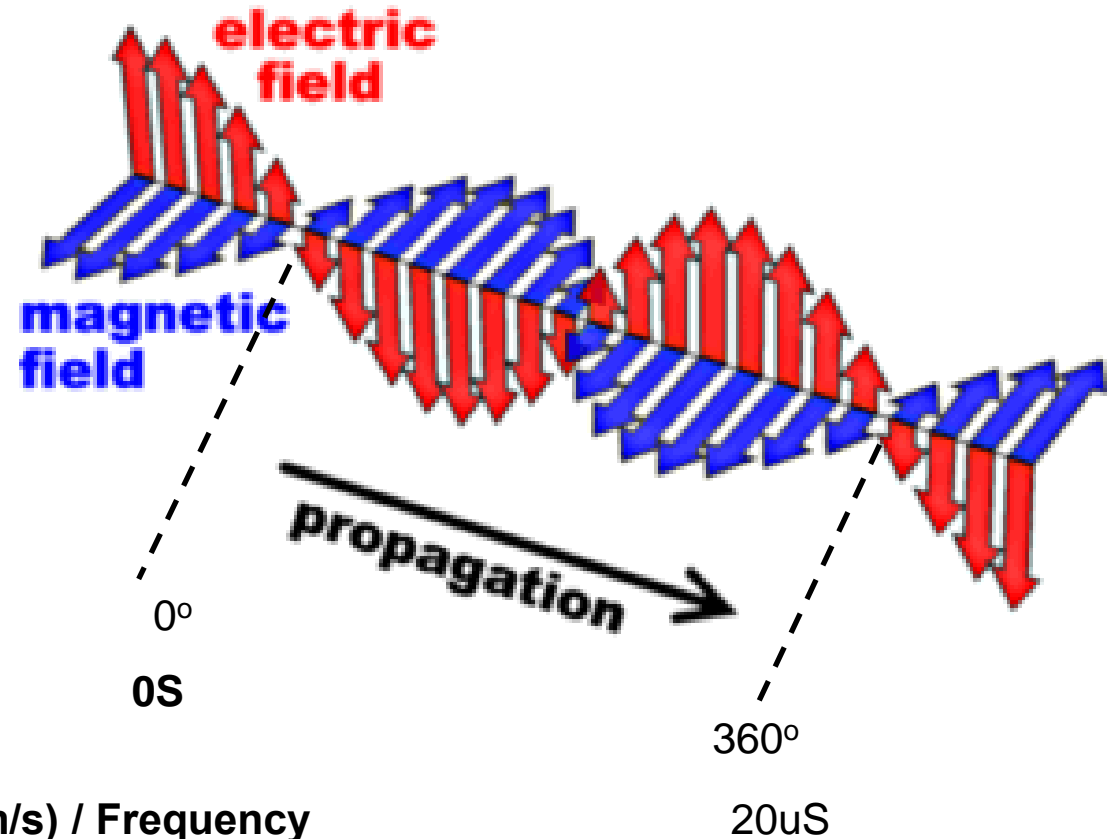
EMC – Electromagnetic Wave

1 cycle = 0° to 360°

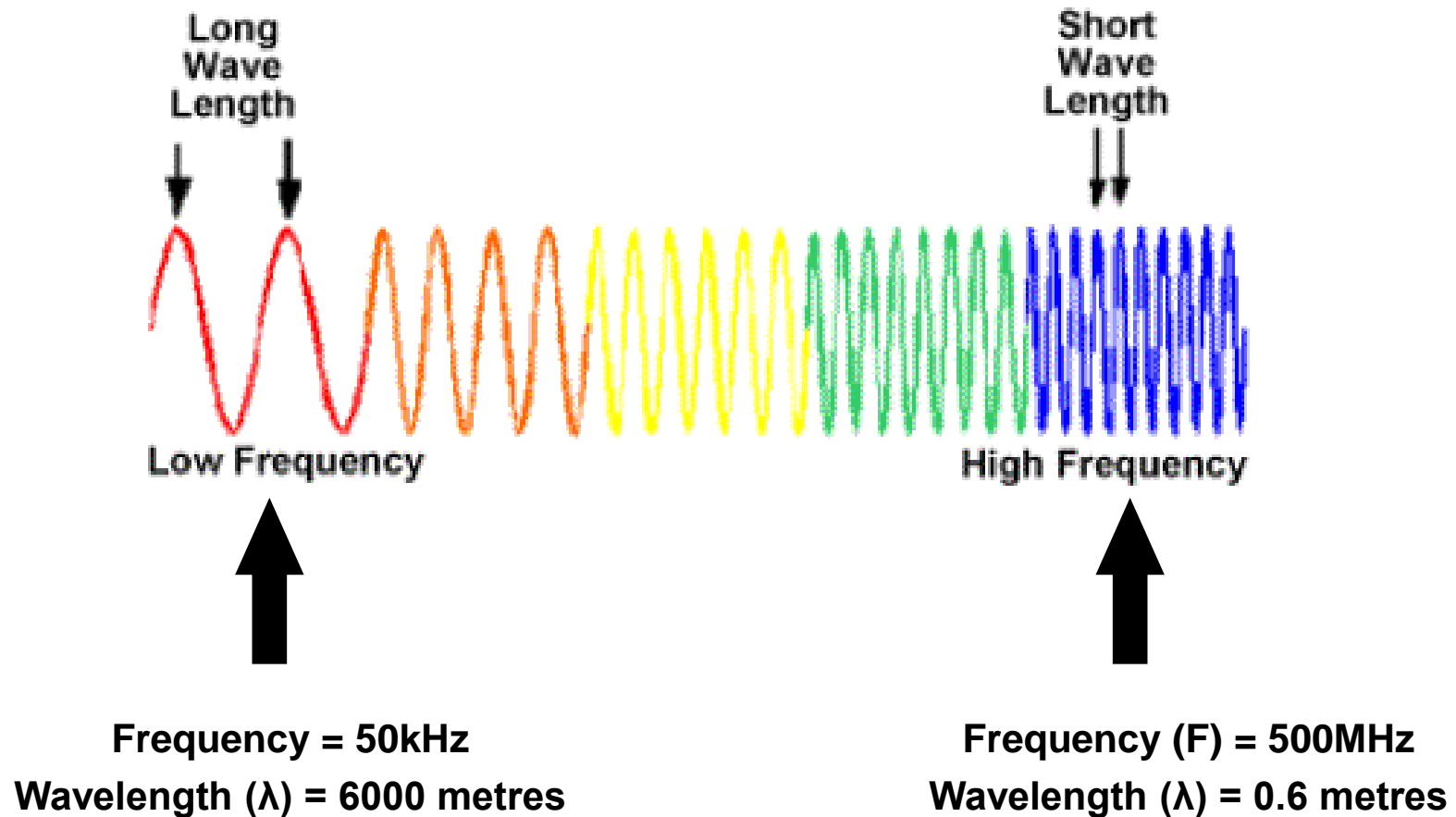
Period (S) = 0 seconds to 20uS

Frequency (F) = 1 / Period
= 1 / 20uS
= 50 kHz

Wavelength (λ) = Speed of Light (m/s) / Frequency
= $300 \times 10^6 / 50000$
= 6000 metres



EMC – Electromagnetic Wave





EMC – Coupling Paths

1) Conductive

- Coupling path between source and victim is formed by direct contact.

2) Capacitive

- Electric field coupling

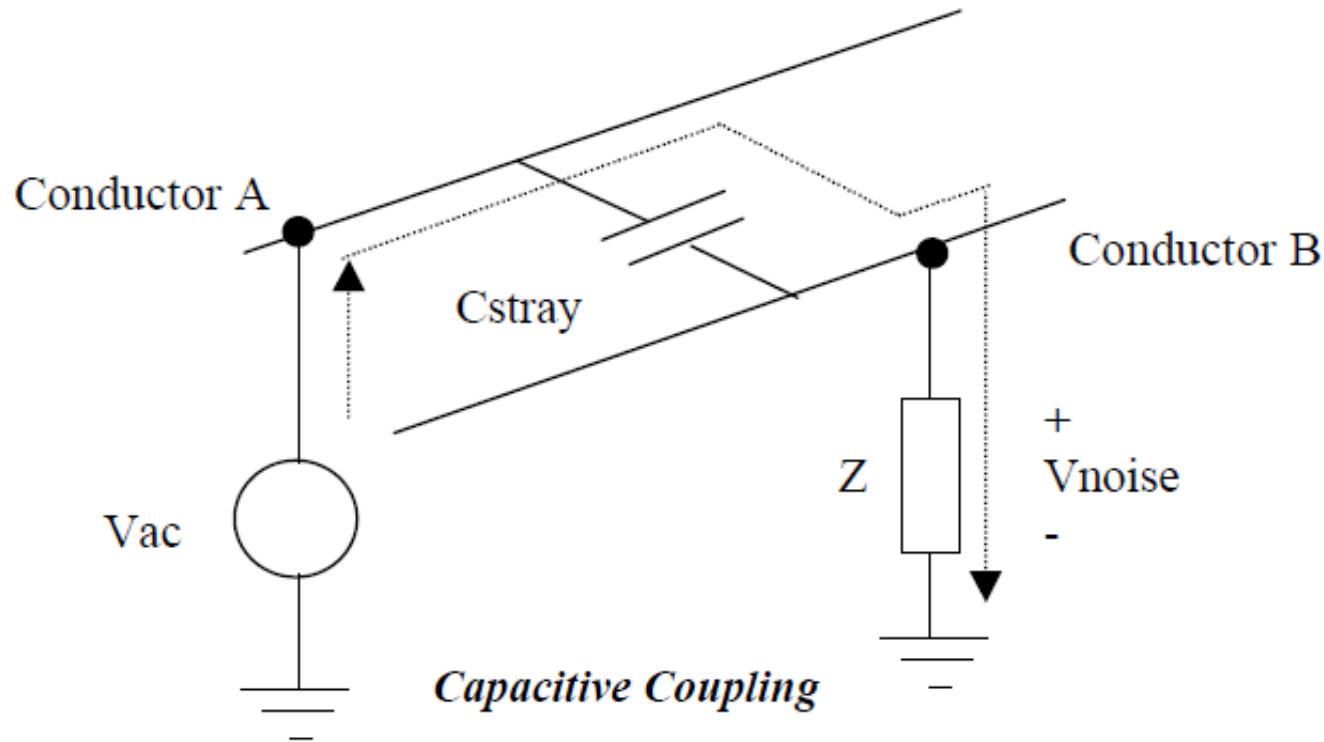
3) Inductive

- Magnetic field coupling

4) Radiative

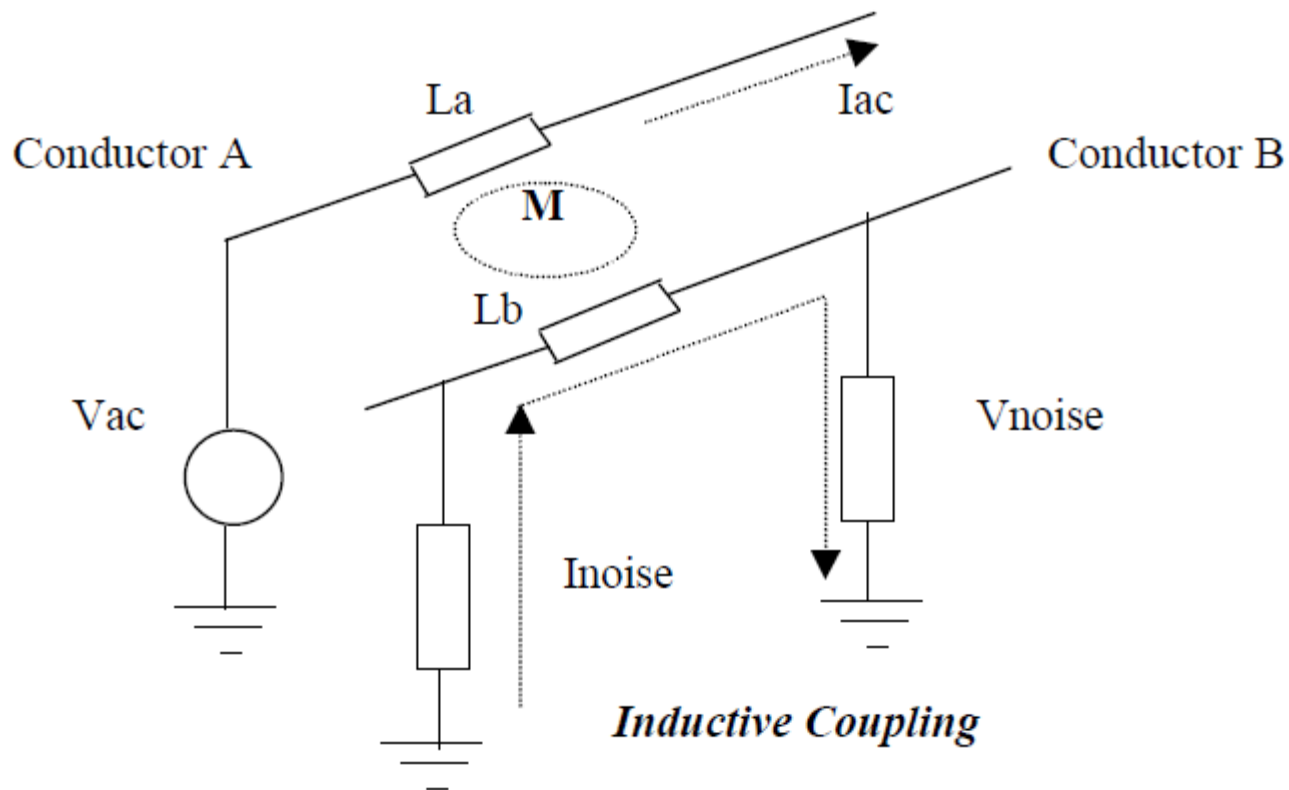
- Source is the “transmitter” and victim is the “receiver”

EMC – Coupling Paths (Capacitive Coupling)



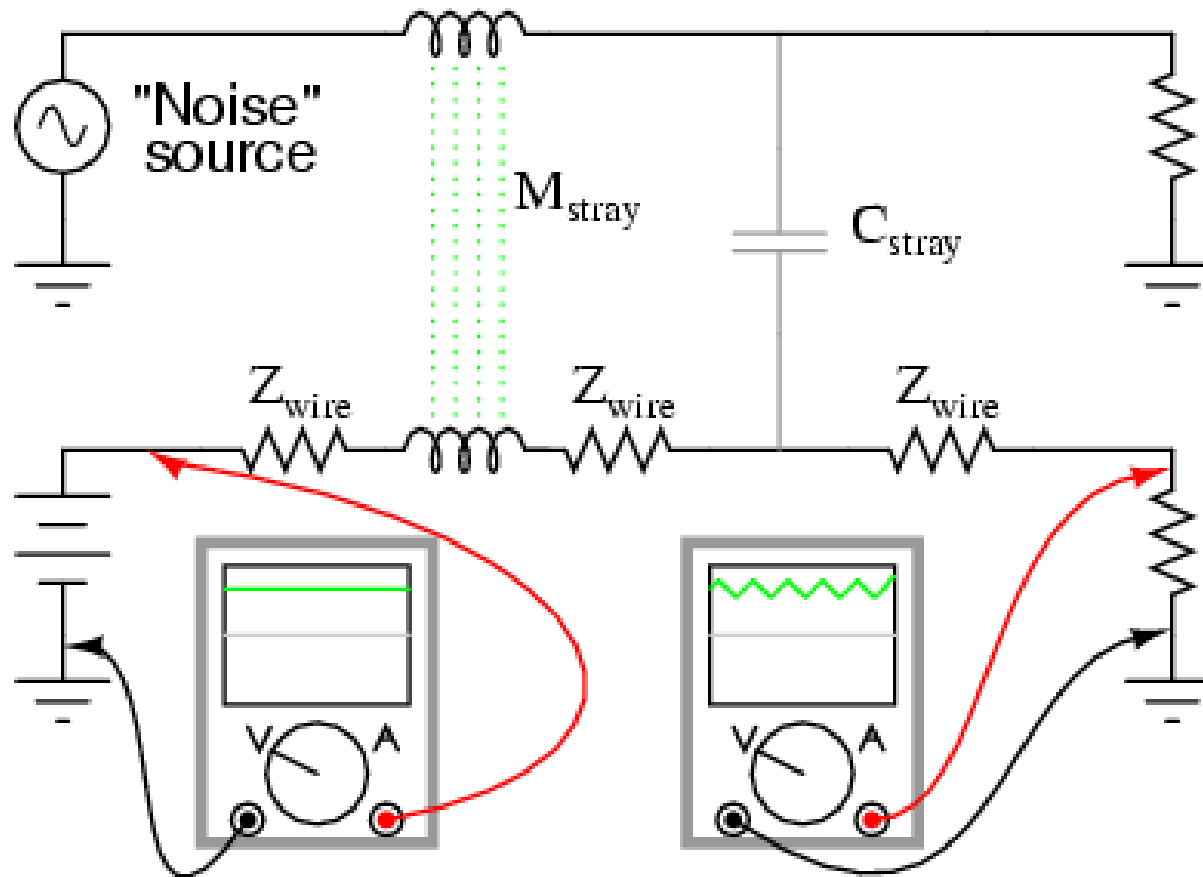
- Capacitive coupling between conductors cause parasitic currents
- Noise voltage increases with frequency. Higher frequency means more high frequency harmonics flow through the capacitor.
- Two wires with 2 mm diameter and spaced by 1 cm shows about 0.1pF of parasitic capacitance.

EMC – Coupling Paths (Magnetic Coupling)



- Magnetic coupling between conductors causes parasitic induced voltages.
- Noise current increases with frequency.
- Two wires with 2mm diameter and spaced by 1cm, shows about 10nH/cm of parasitic inductance.

EMC – Coupling Paths



"Clean" DC voltage

DC voltage + AC "noise"

AC "noise" source may "couple" to a DC circuit through mutual inductance (M_{stray}) and capacitance (C_{stray}) along the length of the conductors

Magnetic Fields





Magnetic Fields - What does frequency mean?

- lat. frequentia = frequency, commonness
- ...describes some events within a dedicated space of time
- Mostly we talk about recurrent events - periodic
- All waveforms are based on a basic wave (sin or cos)
 - fourier-series expansion
- Unwished superposition of these signals results in disturbance signals
 - e.g. noise (a random signal/waveform with a constant amplitude)
- One target of EMC: suppressing / filtering these interferences

Magnetic Fields - What is an Inductor ? What is a coil?



...technical aspect:

→ a piece of wire wound on something

What is the difference between Coil and Inductor?

Coil = **Inductor**
(many shapes) (just inductance)

As a function:

- A filter
- An energy-storage-part (short-time)



Magnetic Fields - What is an EMC ferrite?



.....technical aspect:

→ sintered ferrite material applied to a wire

As a function

- RF-Absorber
- frequency dependant filter

Shapes:

Split ferrite

Toroid / sleeve ferrite

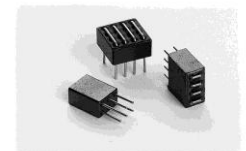
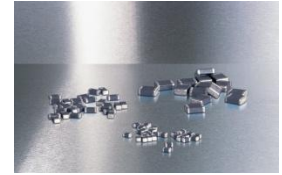
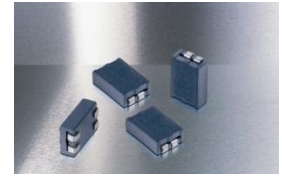
flat cores

ferrite plates

chip bead ferrite

multi hole ferrite

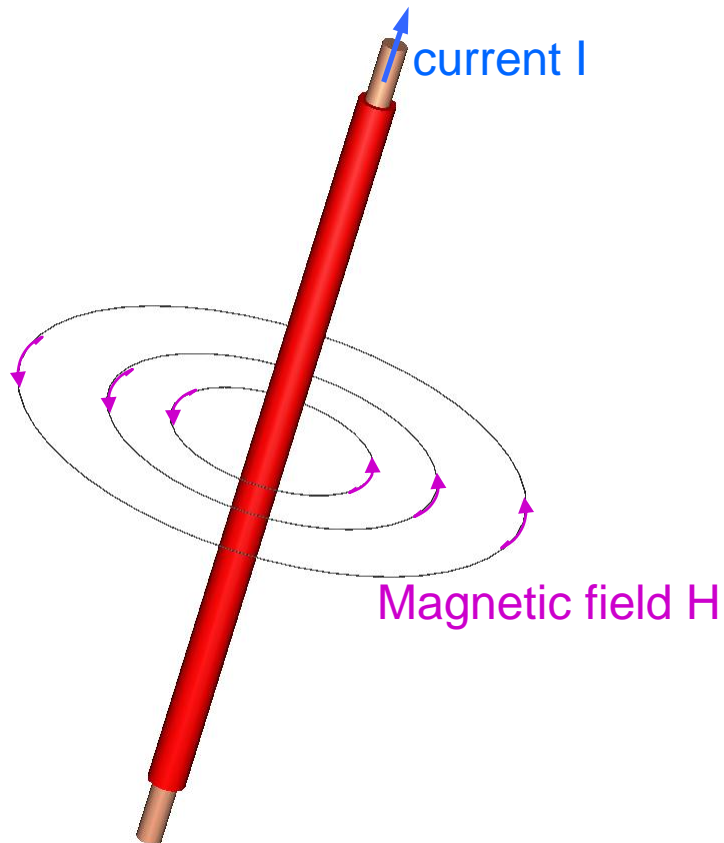
ferrite beads



Magnetic Fields - The magnetic field

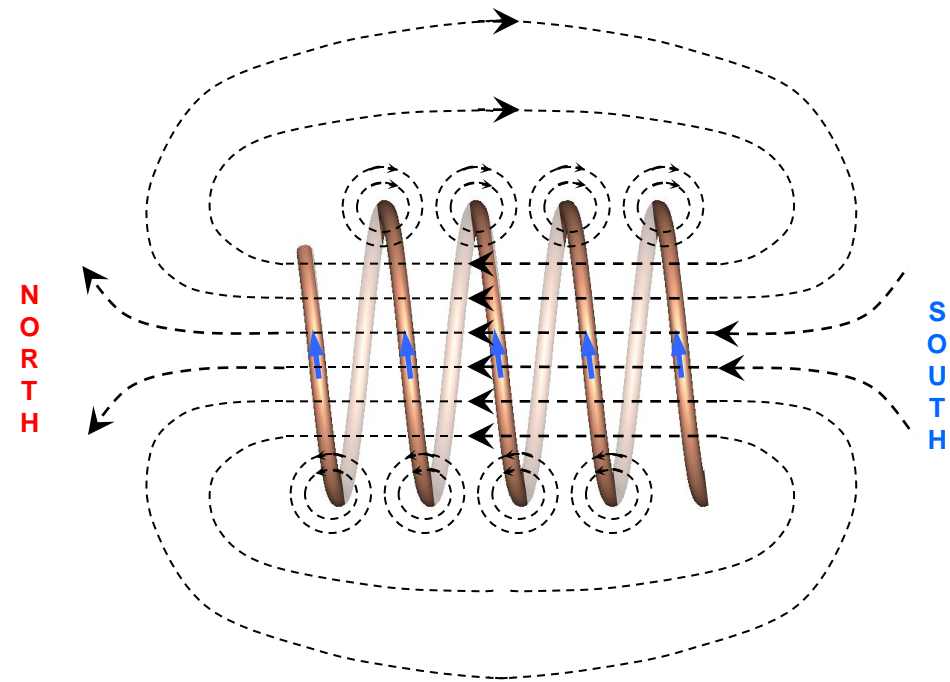
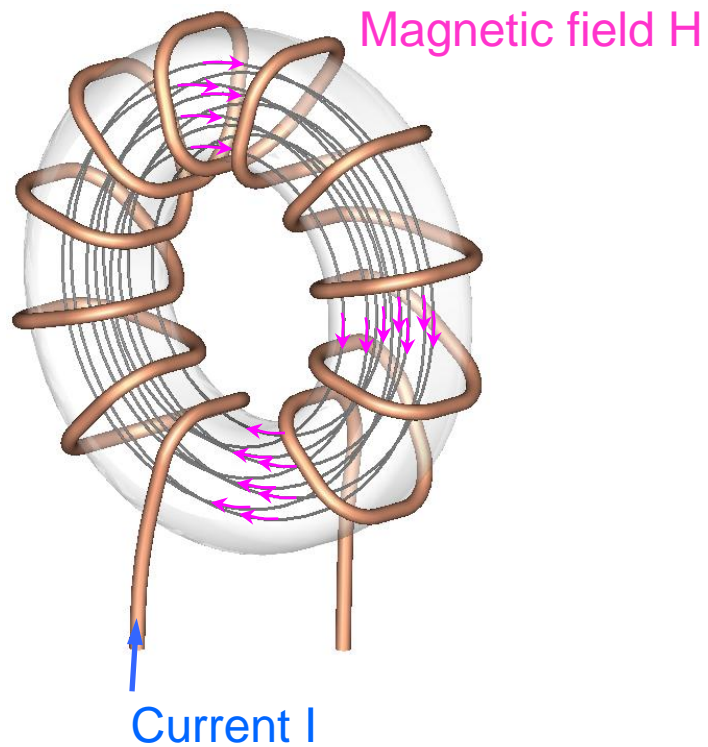
Each electric powered wire generates a magnetic field

Field model



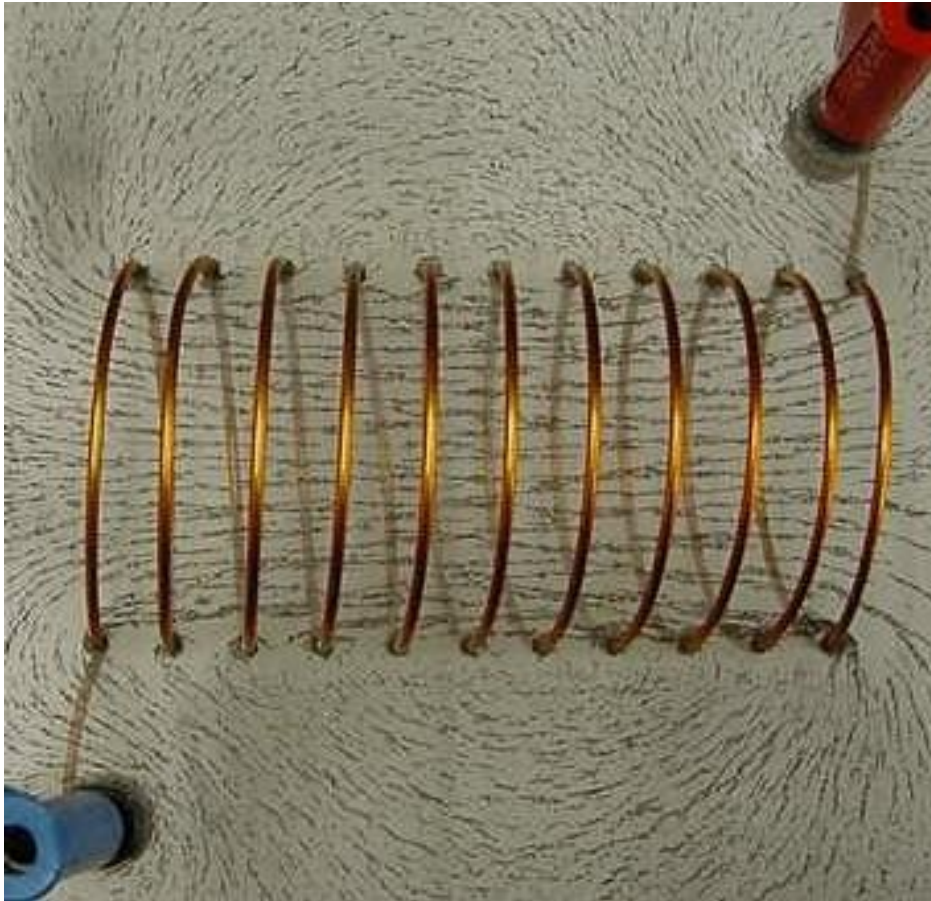
Magnetic Fields - The magnetic field

Field model

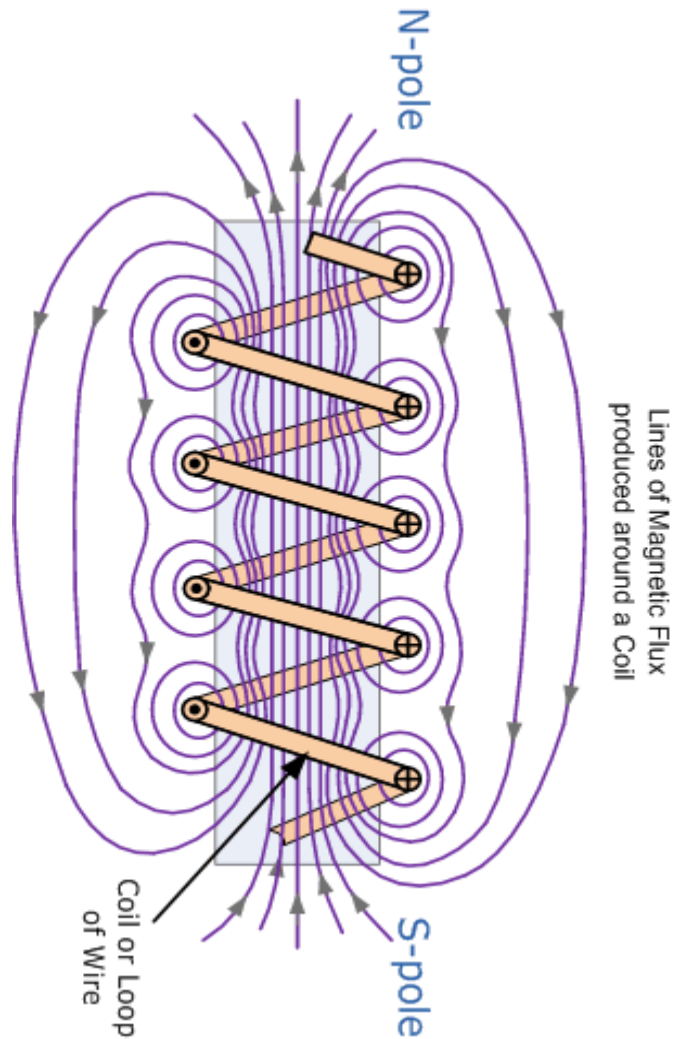


Magnetic Fields - The magnetic field

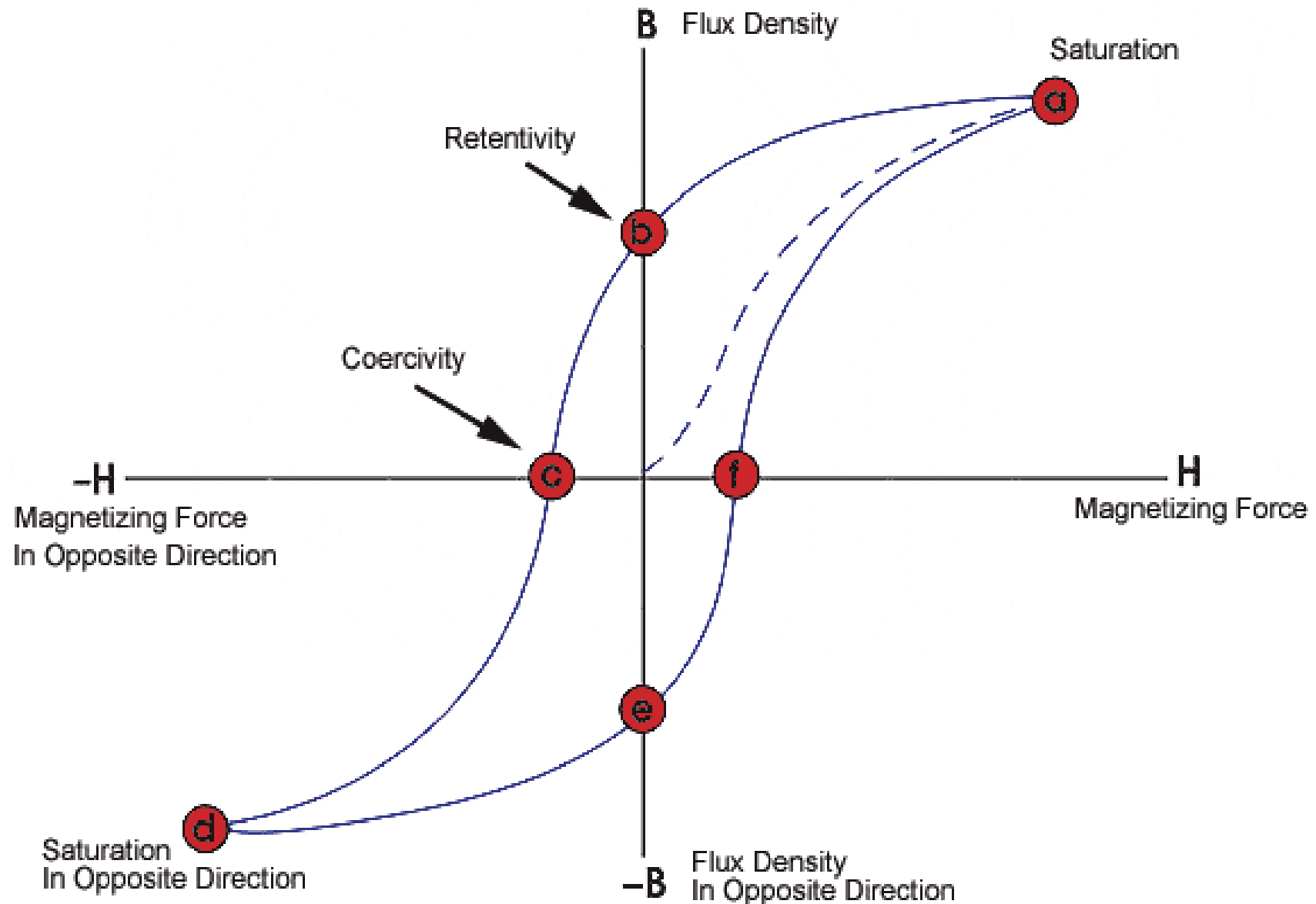
Field model



Magnetic Fields - The magnetic field

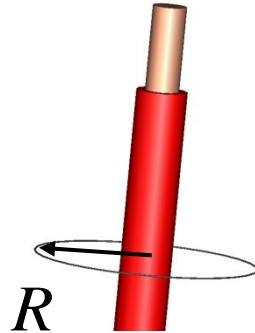


Magnetic Fields – Permeability (Core material parameter)



Magnetic Fields - The magnetic field

Straight wire

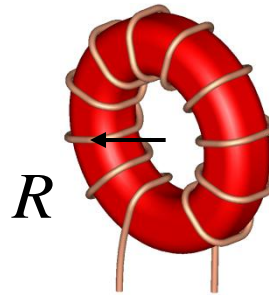


$$H = \frac{I}{2 \cdot \pi \cdot R}$$

The magnetic field strength depends on:

- Dimensions
- No. of turns
- Current

Toroidal

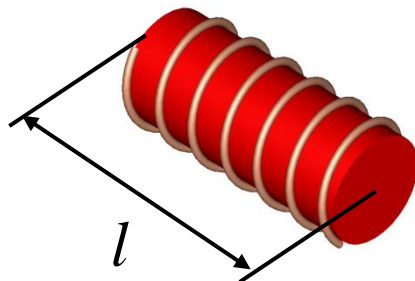


$$H = \frac{N \cdot I}{2 \cdot \pi \cdot R}$$

but

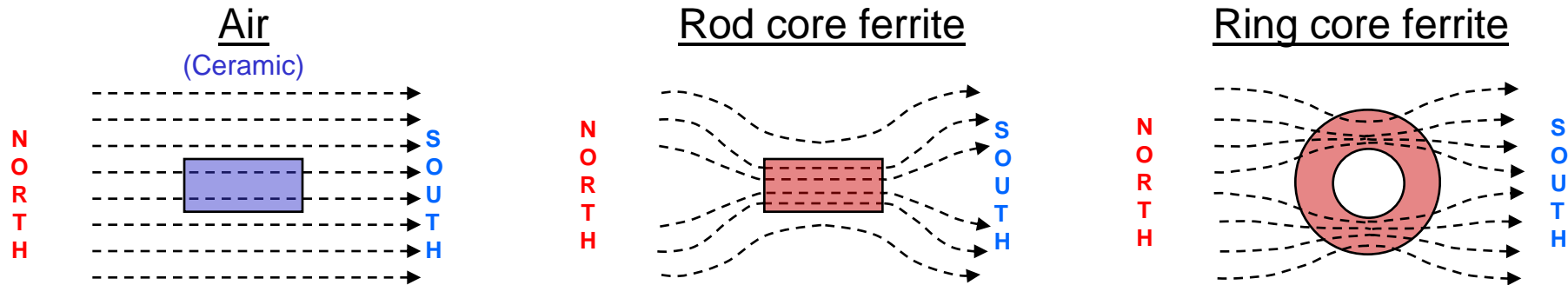
NOT FROM MATERIAL

Rod choke



$$H = \frac{N \cdot I}{l}$$

Magnetic Fields - The magnetic field



Induction in air:

$$B = \mu_0 \cdot \mu_r \cdot H$$

$$B = \mu_0 \cdot H$$

linear function because $\mu_r = 1 = \text{constant}!$

The relative permeability is:

Induction in Ferrite:

$$B = \mu_0 \cdot \mu_r \cdot H$$

**Material-
Frequency-
Temperature-
Current-
Pressure-**
-dependent parameter

Magnetic Fields - What is permeability?

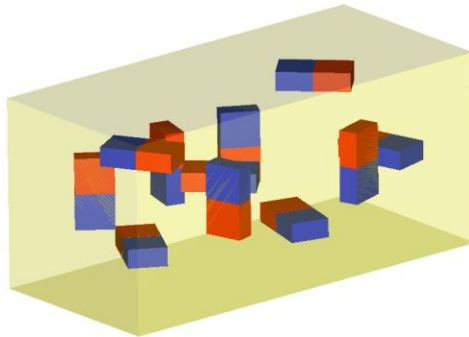
Relative Permeability

→ describe the capacity of concentration of the magnetic flux in the material

→ is a factor of energy needed to magnetize

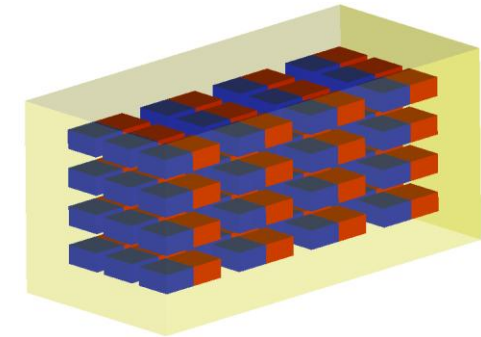
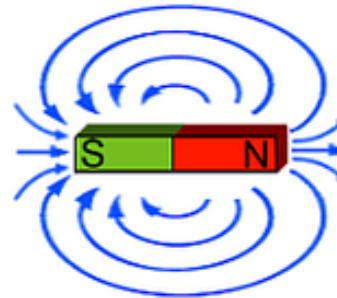
$$\mu_r = \frac{1}{\mu_0} \frac{\Delta B}{\Delta H}$$

Ferrite material



- un ordered
- soft magnetic

Permanent magnet



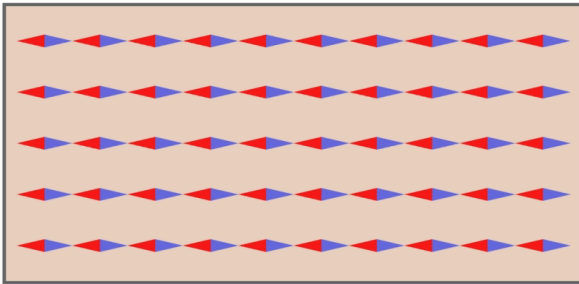
- ordered
- hard magnetic

Typical permeability μ_r :

- Iron power / Superflux : 50 ~ 150
- Nickel Zinc : 40 ~ 1500
- Manganese Zinc : 300 ~ 20000

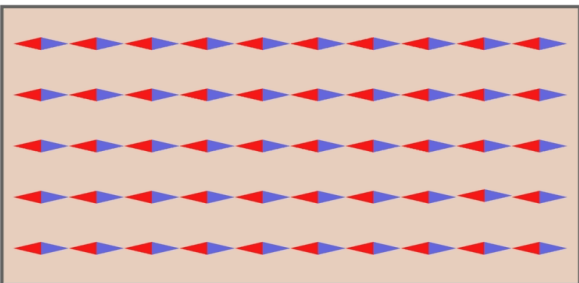
Magnetic Fields - Magnetic Domains Simulation

B 
H 



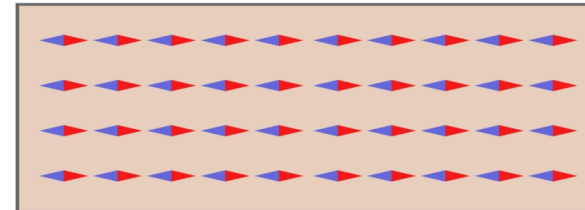
Linear hysteresis loop

B 
H 



Rectangular hysteresis
loop

H 



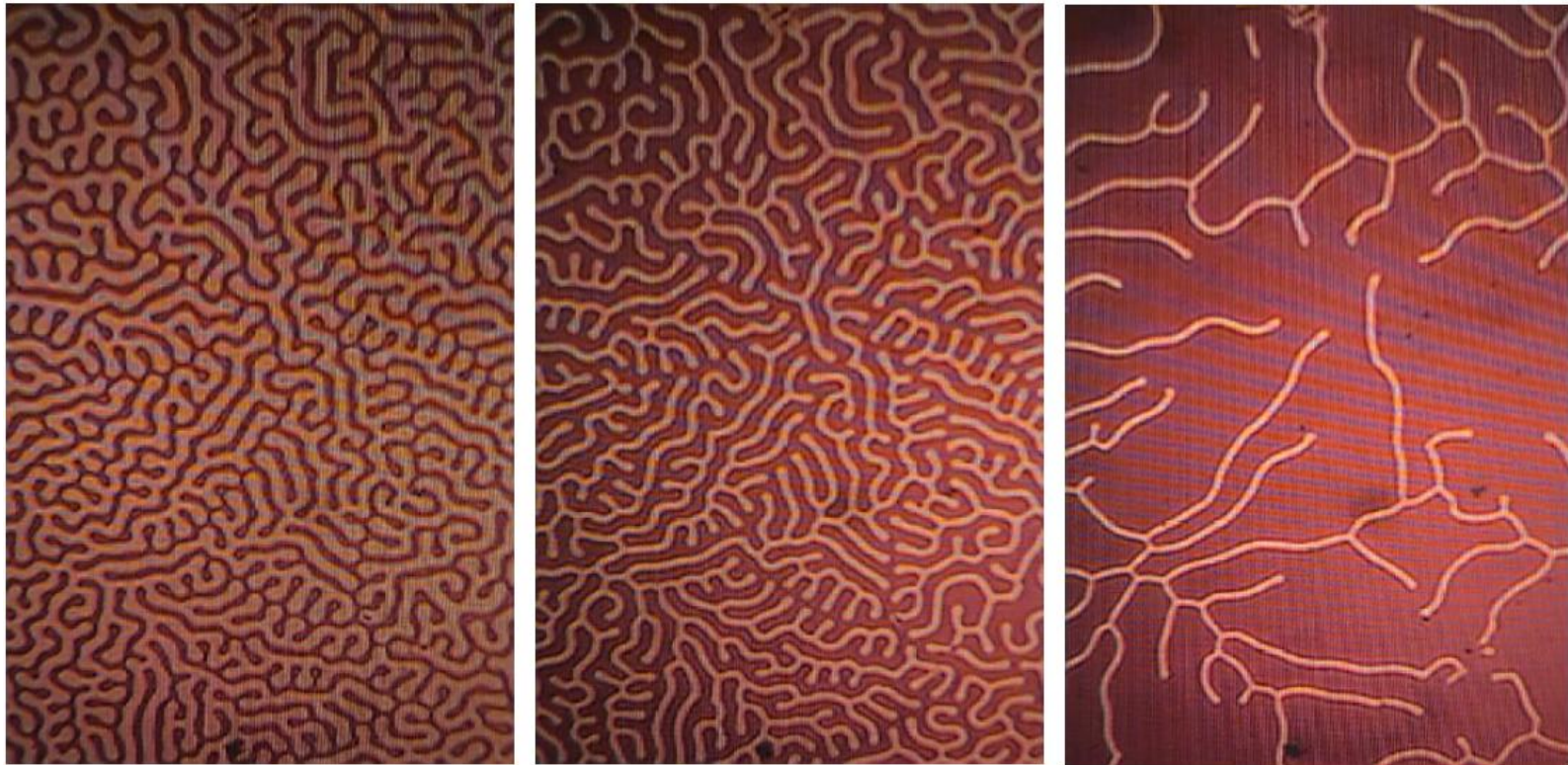
Magnetostriction

Magnetic Fields – Permeability (Core material parameter)



Domain limits in a magnetic field

- the domain limits are melting together with higher magnetic flux



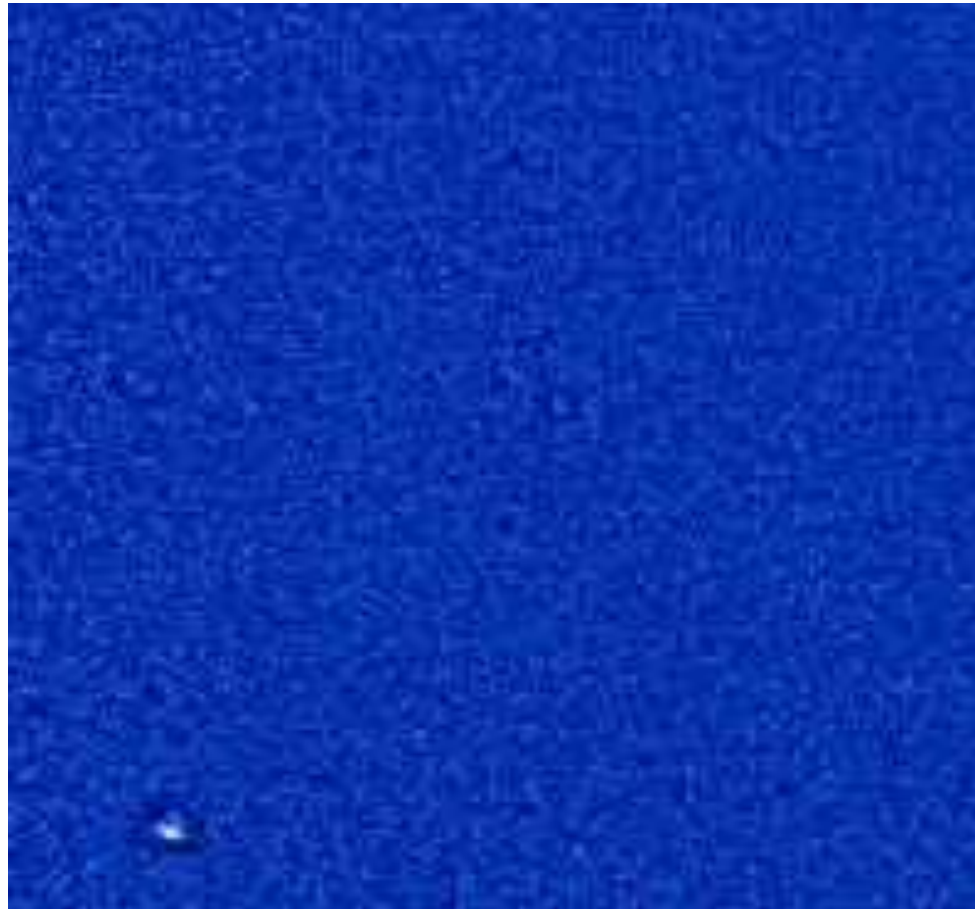
Quelle: Universität Dortmund (Polarisationsmikroskop)

Magnetic Fields – Permeability (Core material parameter)



Domain limits in a magnetic field

- the domain limits are melting together with higher magnetic flux

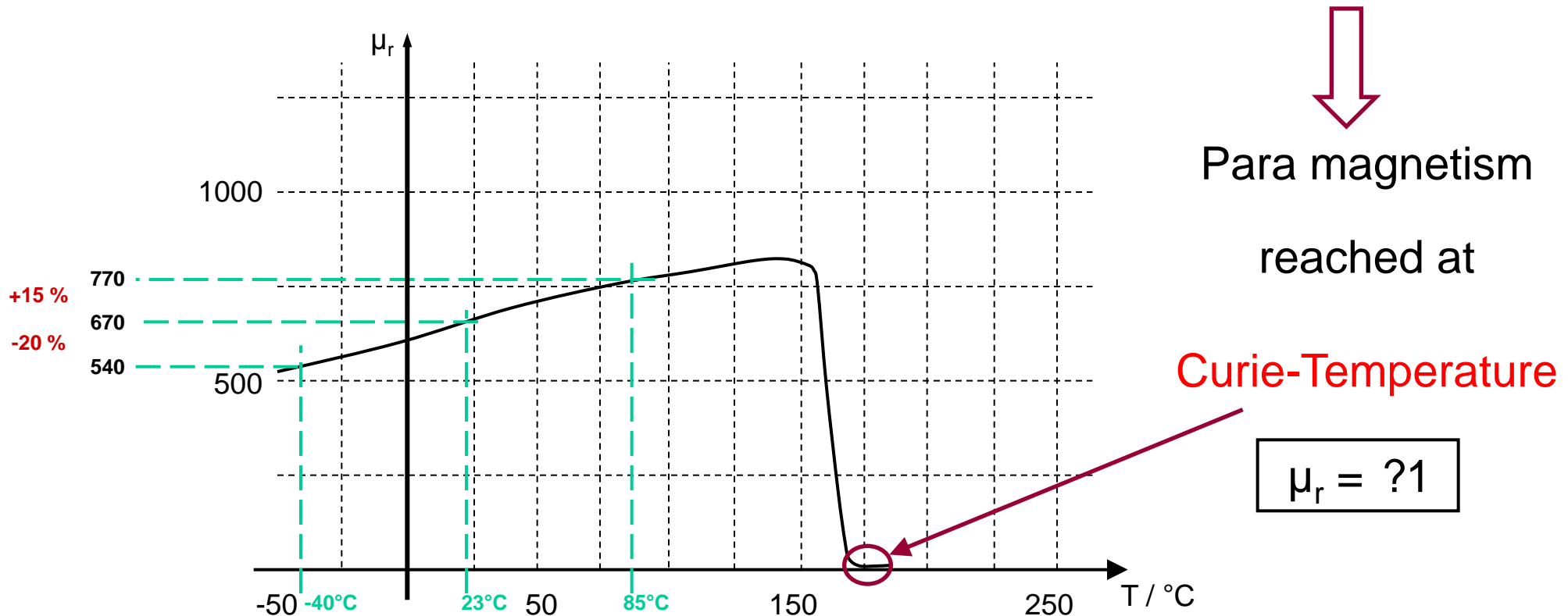


Magnetic Fields – Permeability (Core material parameter)

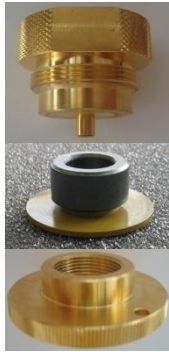
Dependence on temperature

-the magnetizing are influenced from the heating energy

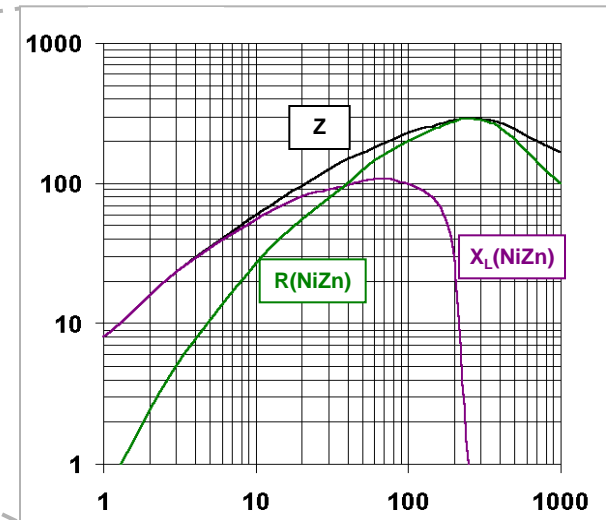
$T \uparrow \rightsquigarrow$ thermal motion $\uparrow \rightsquigarrow$ degree of order $\downarrow \rightsquigarrow$ Alignment of elementary magnets



Magnetic Fields – Permeability (Complex permeability)

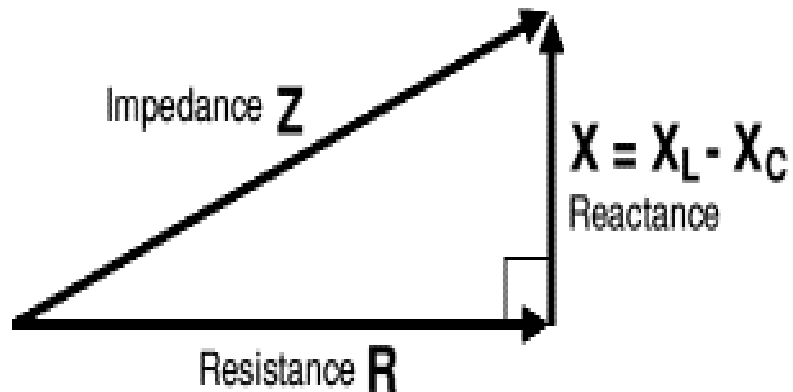
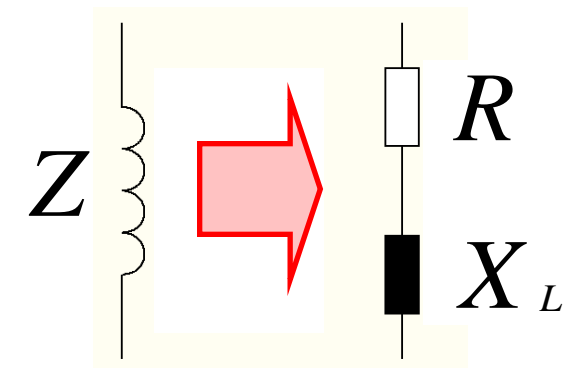


=1 turn



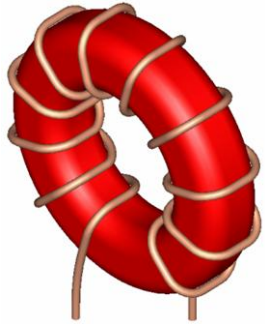
Core material-Parameter

Equivalent circuit

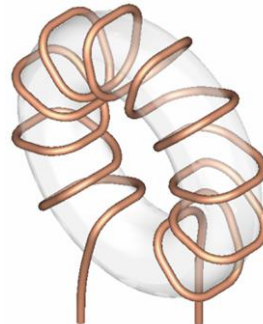


$$\text{Impedance, } Z = \sqrt{R^2 + X^2}$$

Magnetic Fields – Permeability (Complex permeability)



=



•



Impedance of winding on
with core material

=

Impedance of winding
w/o core material

•

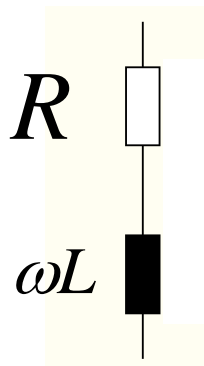
core material

↓
 \underline{Z}

=

↓
 $j\omega L_0$

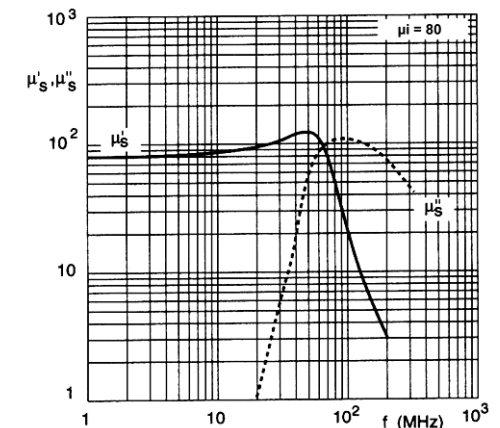
↓
 $(\mu' - j\mu'')$



=

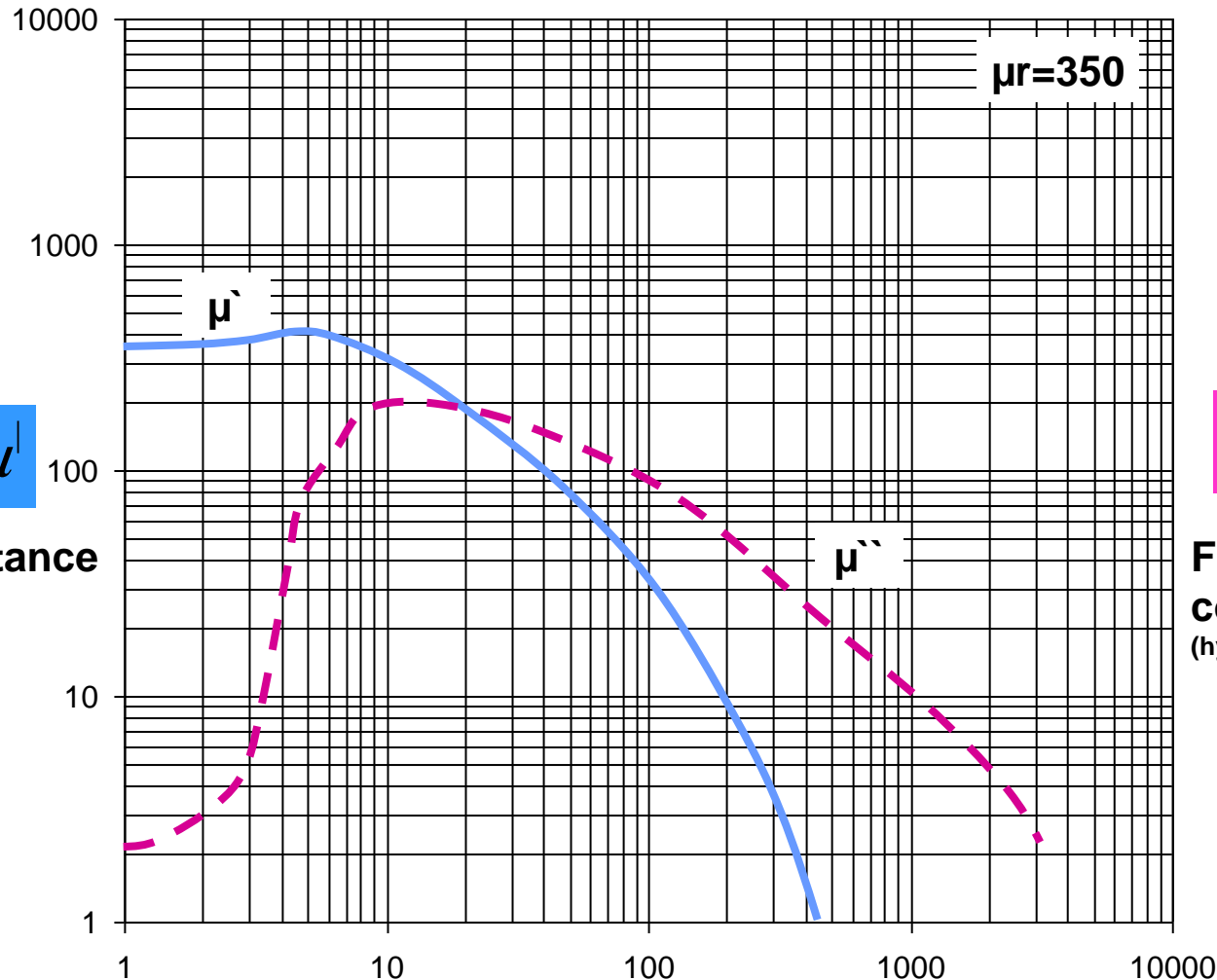


•



Magnetic Fields – Permeability (Complex permeability)

$$\underline{Z} = j\omega L_0 (\mu' - j\mu'') = R + jX$$



$$X_L = j\omega L_0 \mu'$$

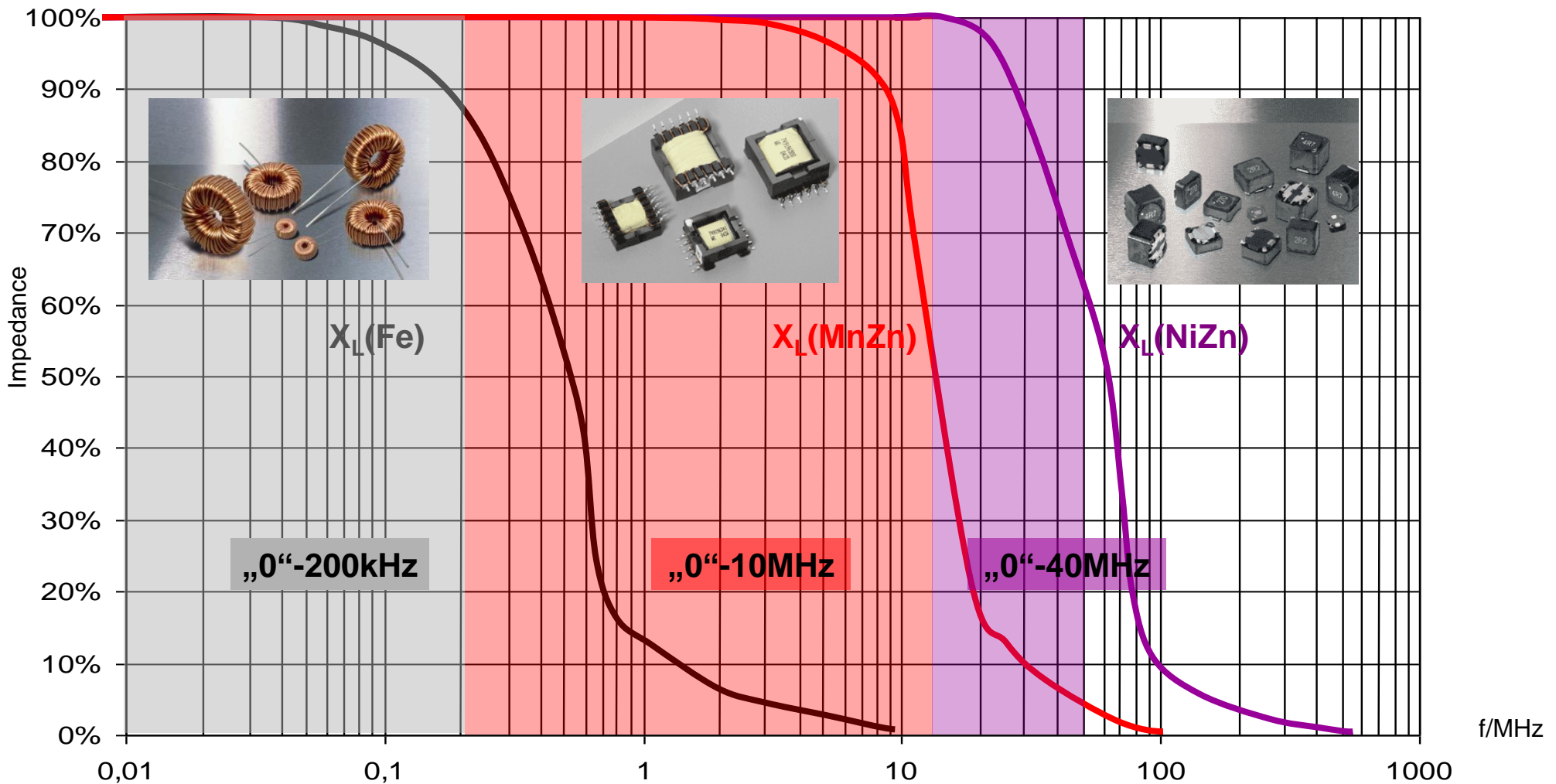
Inductance reactance
(energy storage)

$$R = \omega L_0 \mu''$$

Frequency dependent core losses
(hysteresis & eddy current losses)

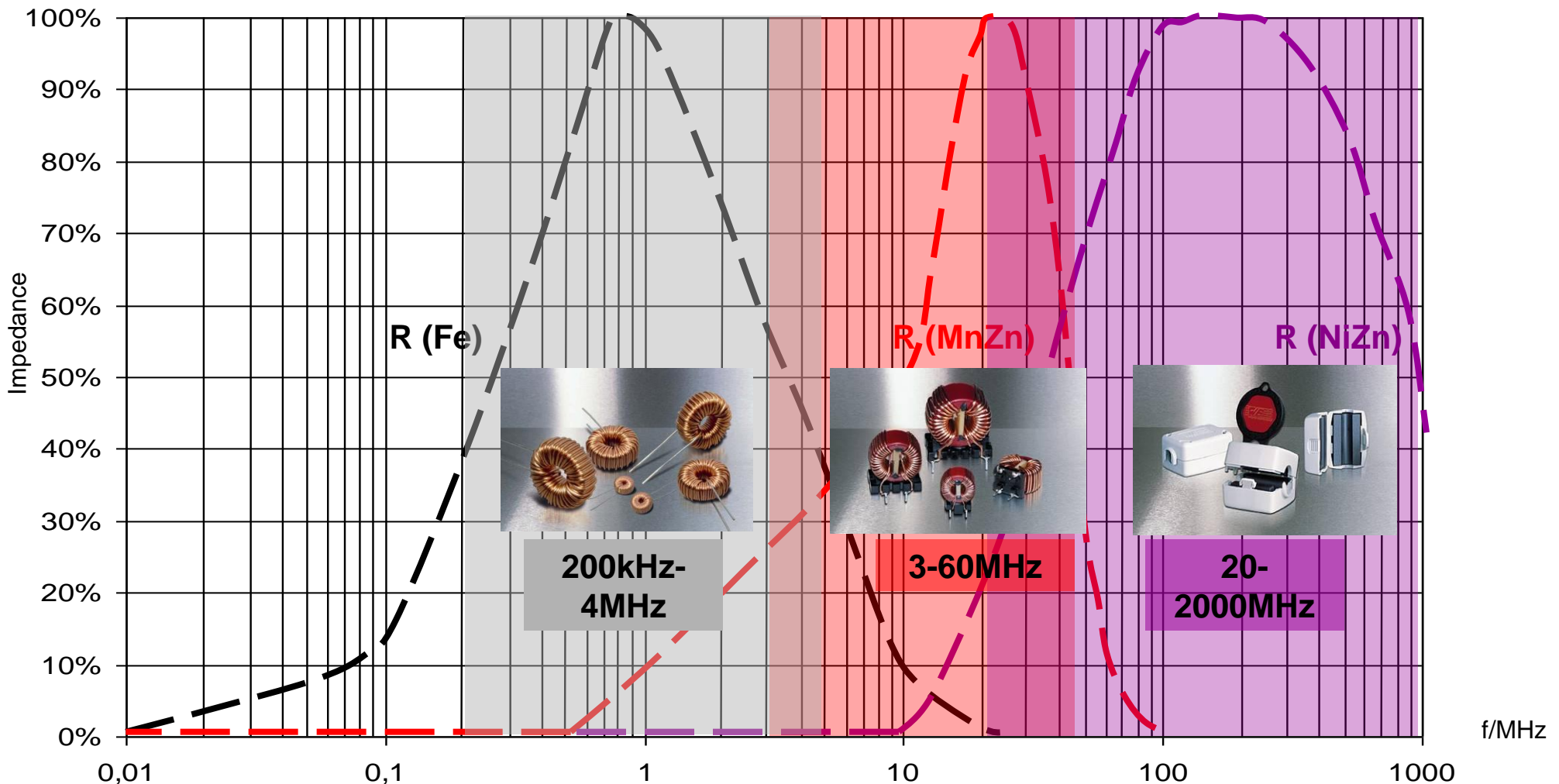
Magnetic Field - Core material (Inductors {Storage})

knowledge of operating frequency



Magnetic Fields - Core material (Choke {Filter})

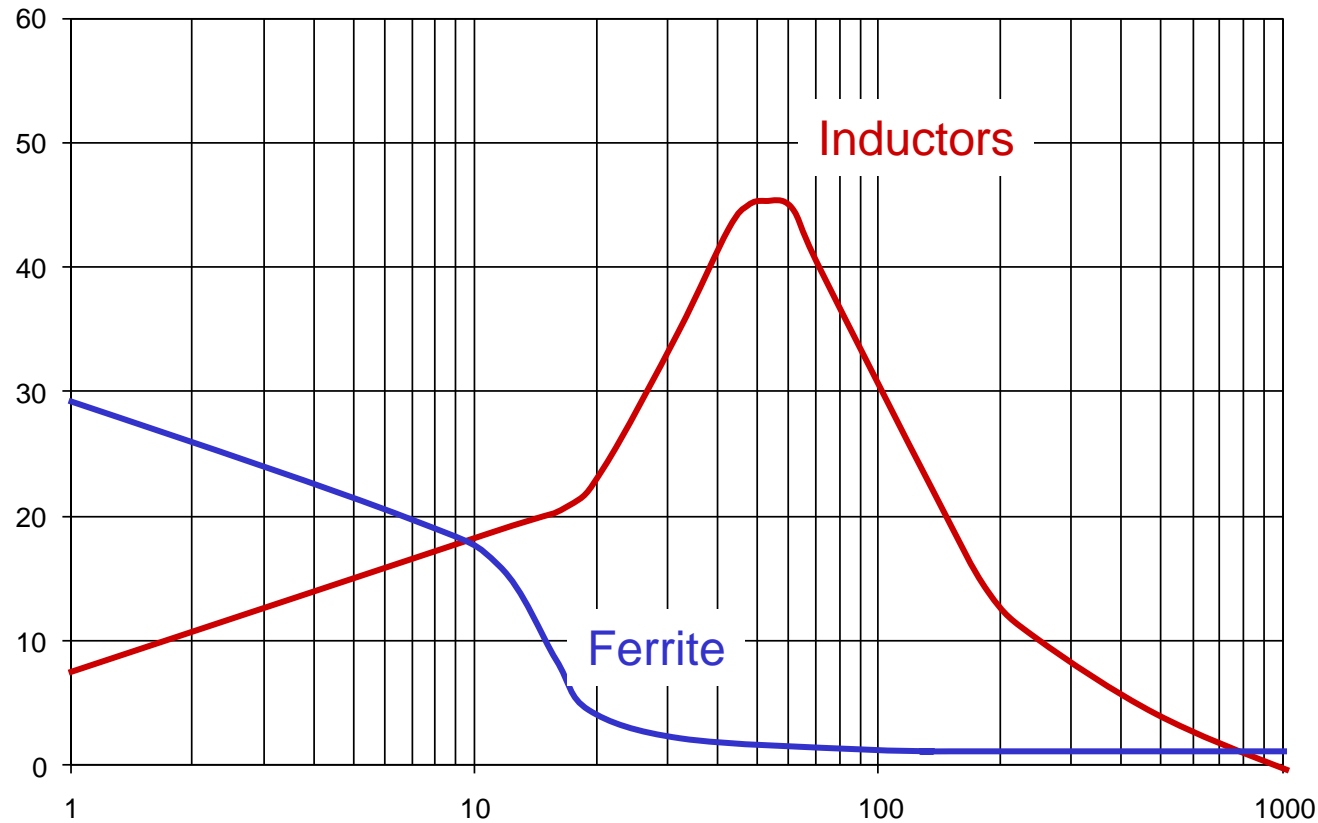
knowledge of noise frequency range



Magnetic Fields - Core material (Inductor / EMC Ferrite)



- Compare the Q



$$Q = \frac{X_L}{R}$$

Magnetic Fields - Core material (Inductor / EMC Ferrite)



1. Application: **Storage inductor**

Request: - lowest possible core losses at switching frequency

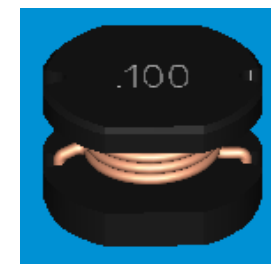
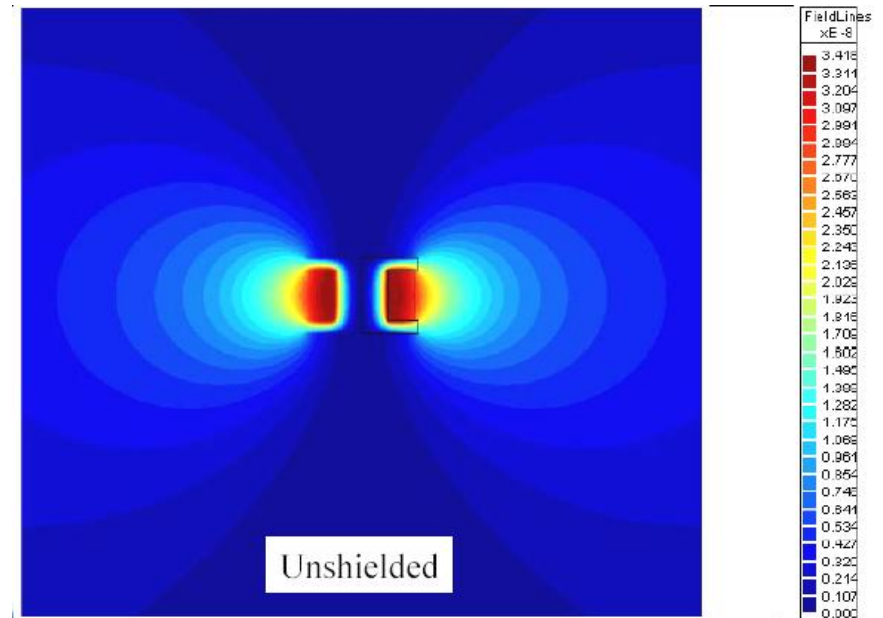
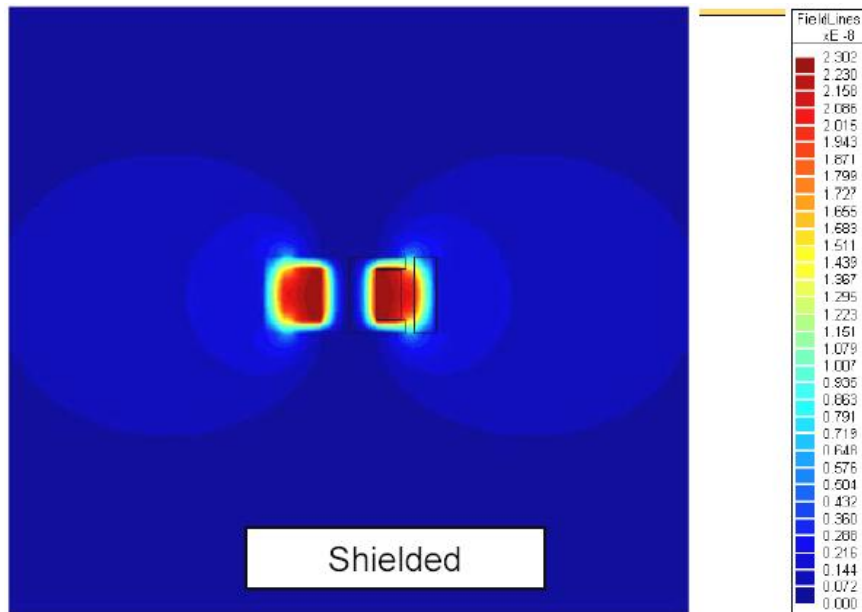
HIGH Q

2. Application: **Absorber / Filter**

Request – highest possible core losses at application frequency

LOW Q

Magnetic Fields - Shielded vs. Unshielded power inductor

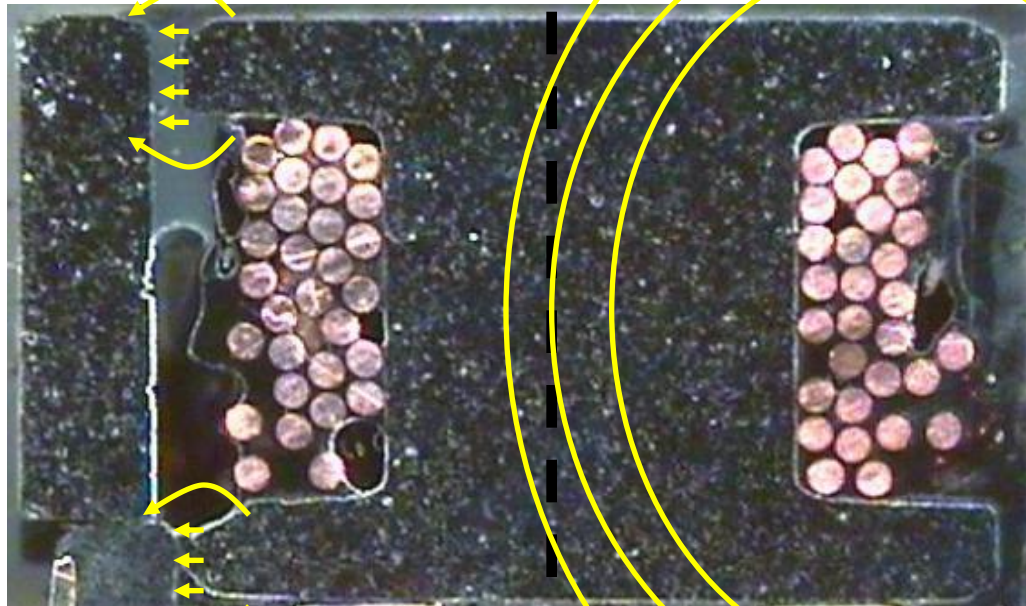


Magnetic Fields - Shielded vs. Unshielded power inductor

- Magnetic field

shielded

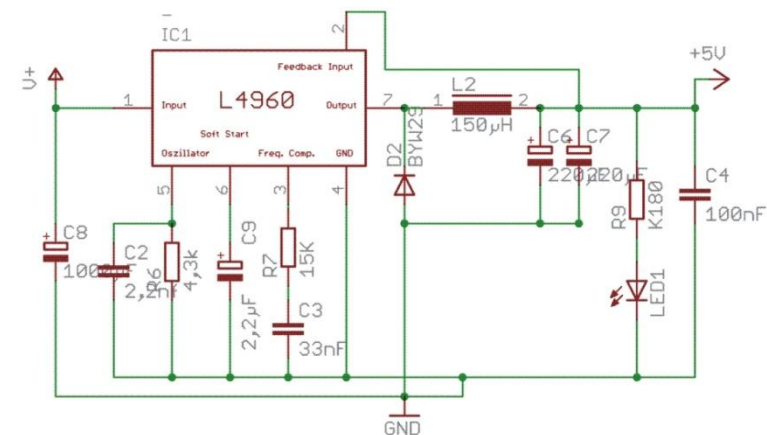
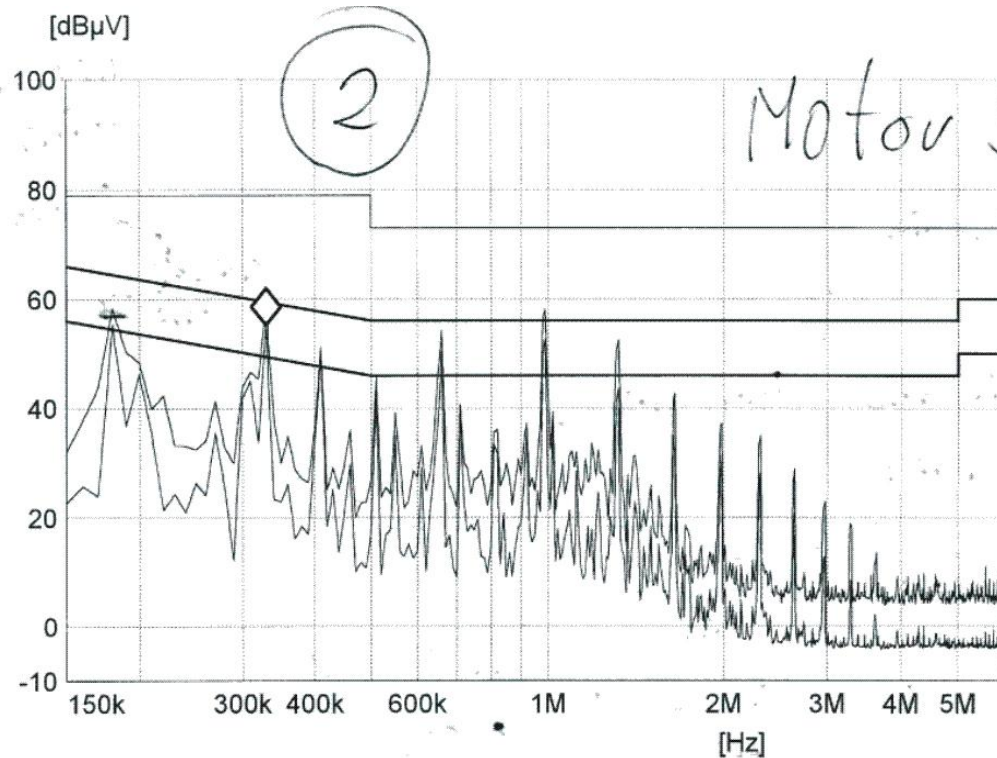
unshielded



Magnetic Fields - Conducted Emission Measurement



Power supply V 1.0

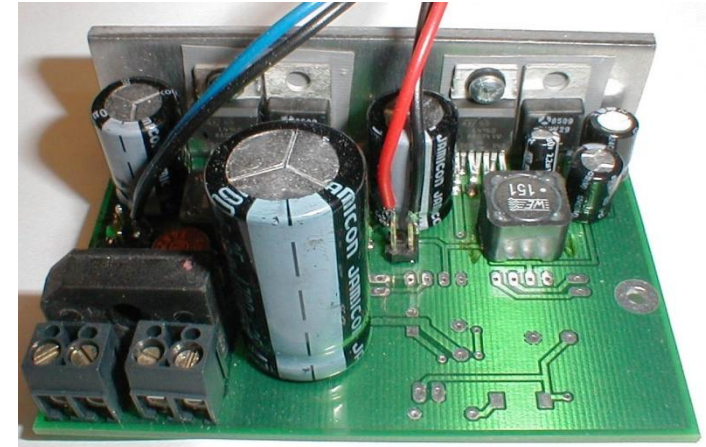
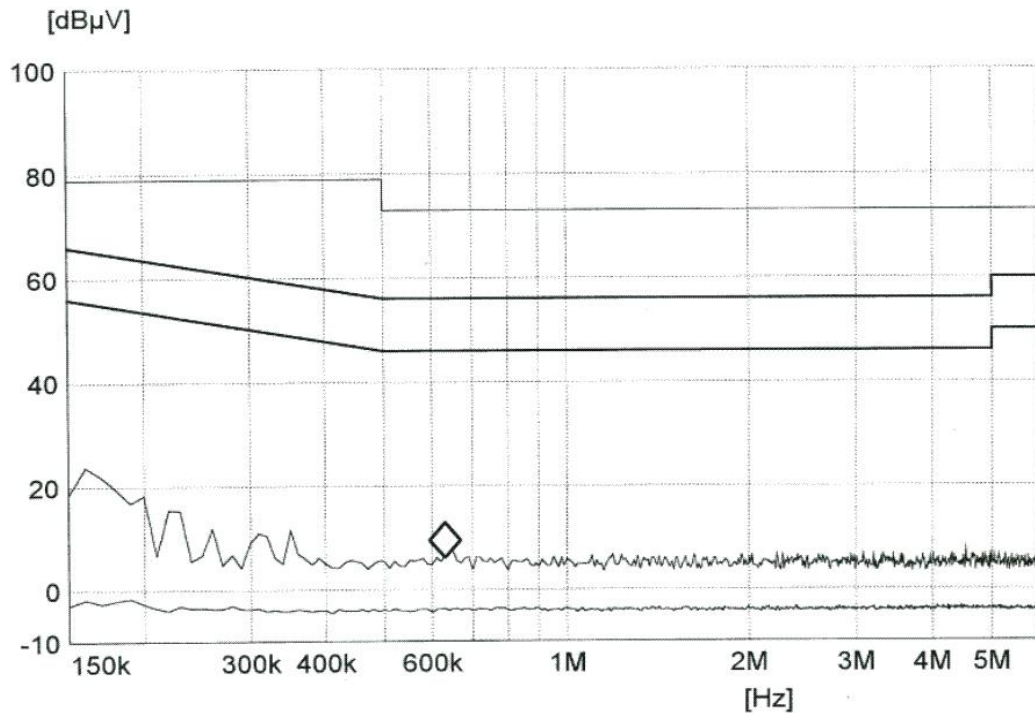


Buck Converter ST L4960/2.5A/fs 85-115KHz

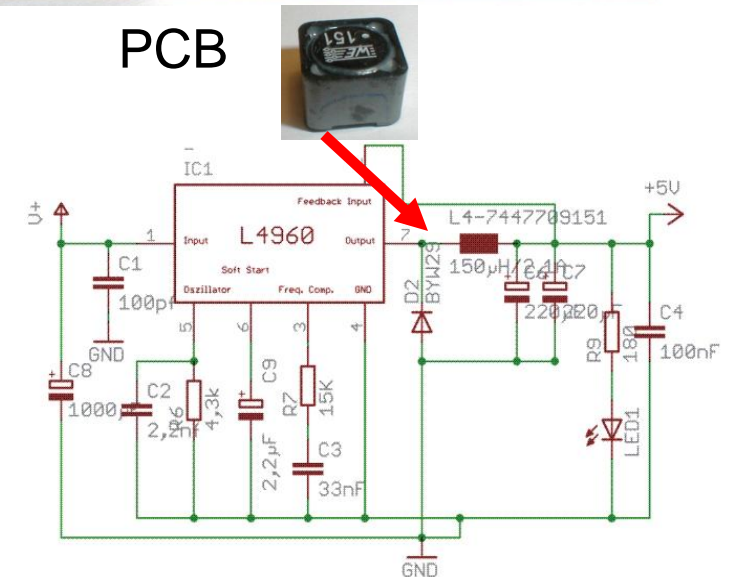
Magnetic Fields - Conducted Emission Measurement



Power supply V 1.1



PCB



Schematic

Magnetic Fields - Be Aware!

- Select the right parts for your application.
- Do not always look on cost

Very easy solution with a
dramatic result!!!



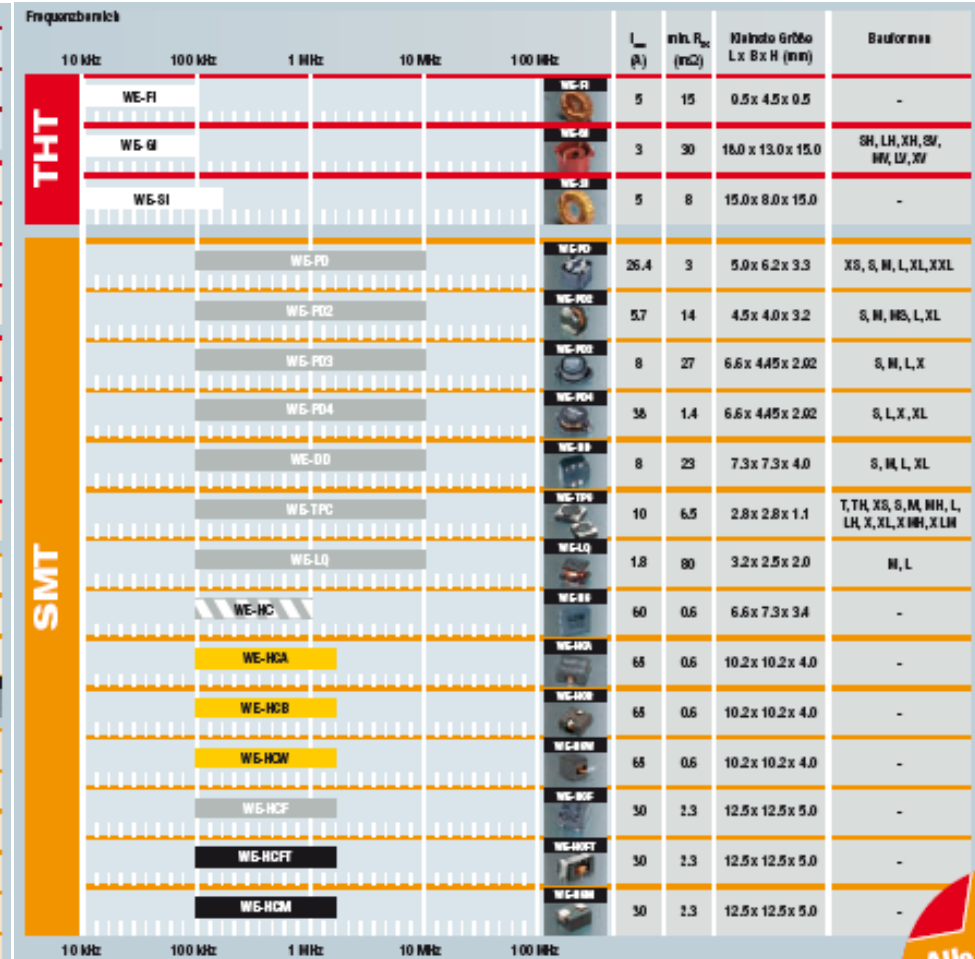
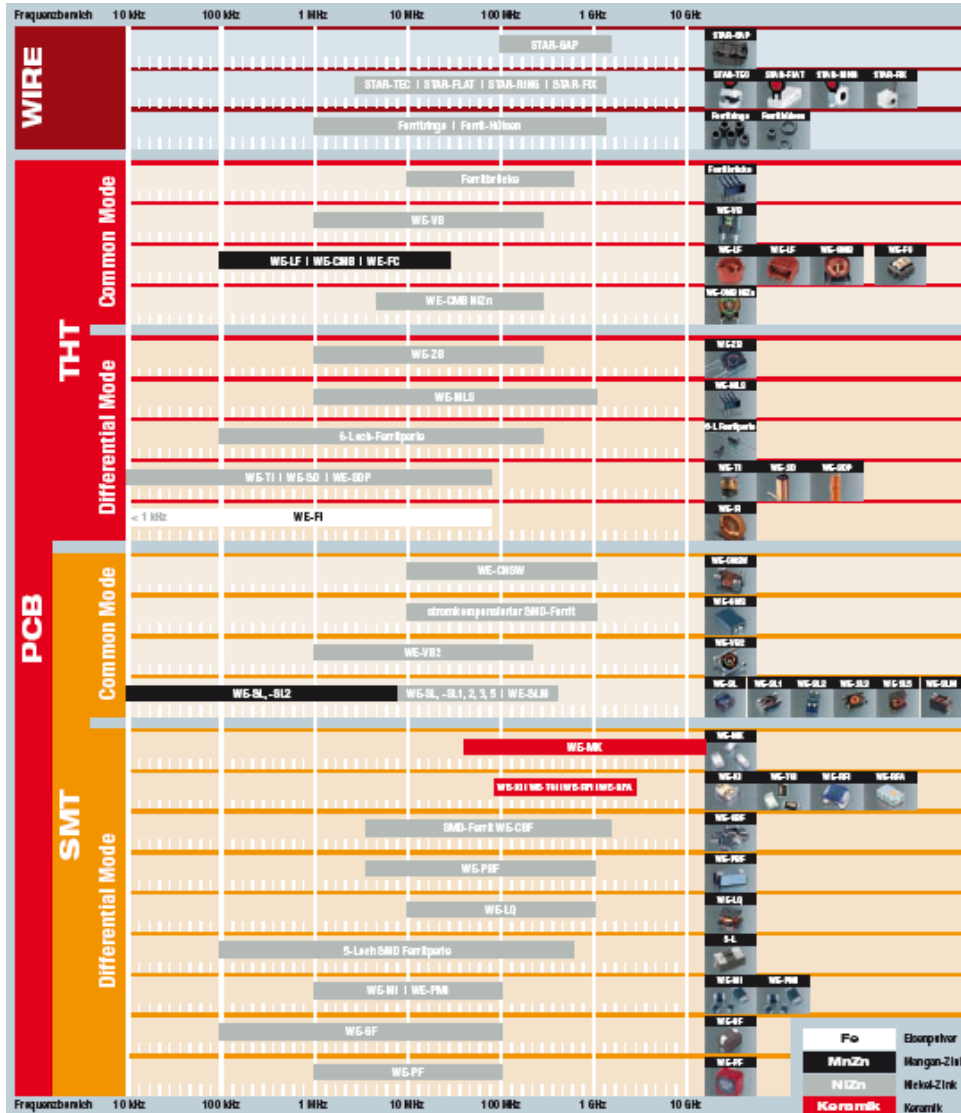
Choke before



or



Choke after



MnZn

Mangan-Zink

Keramik

Keramik

Filter and Signal



Filter and Signal - Basics

The energy can not disappear it will be just transformed into other energy form → law of conservation of energy

- e.g. electrical energy transformed into → thermal energy



- the core losses from ferrite transform the noise energy into heat

MAIN TARGET:

Noise energy **should not occur** at all!



Filter and Signal - Basics

What is filtering?

- Useful to reduce coupling of noise from device A to device B
- Reduce noise emission
- Increase noise immunity
- The signal should be not affected

Efforts?

- Filtering can be very **difficult** if signal and noise frequency are close to each other
- if signal and noise frequency are far away from each other, then a filter design is **very easy**

Filter and Signal - Structured interference suppression



- Recognize the coupling mode:
 - Common mode noise
 - Differential mode noise

Filter and Signal – Determining type of interference

Common mode or differential mode?

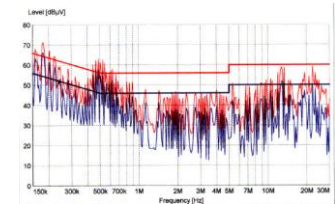
take a Snap Ferrite and fix it on the cable
(both lines e.g. VCC and GND)

if noise is reduced or
noise immunity
is increased

Common Mode interferences

if not

Differential Mode interferences



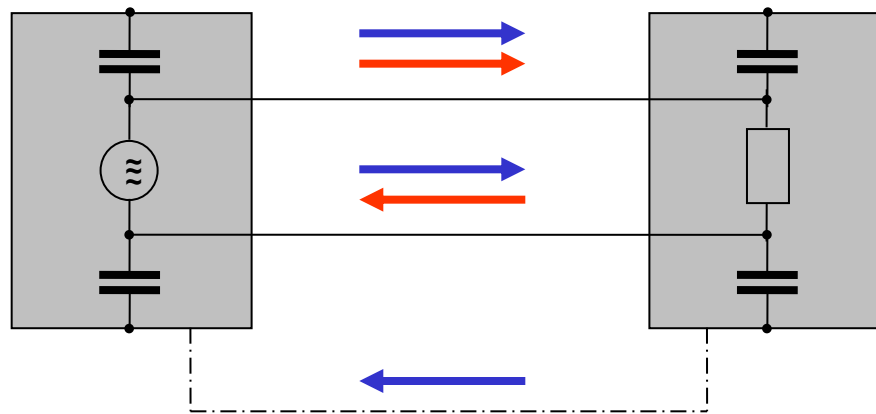
e.g. Common mode
choke

e.g. chip bead ferrite

Filter and Signal - Common Mode Filter

Interference source

interference sink



differential mode current

symmetrical interference voltage

Common mode current

Asymmetrical interference voltage

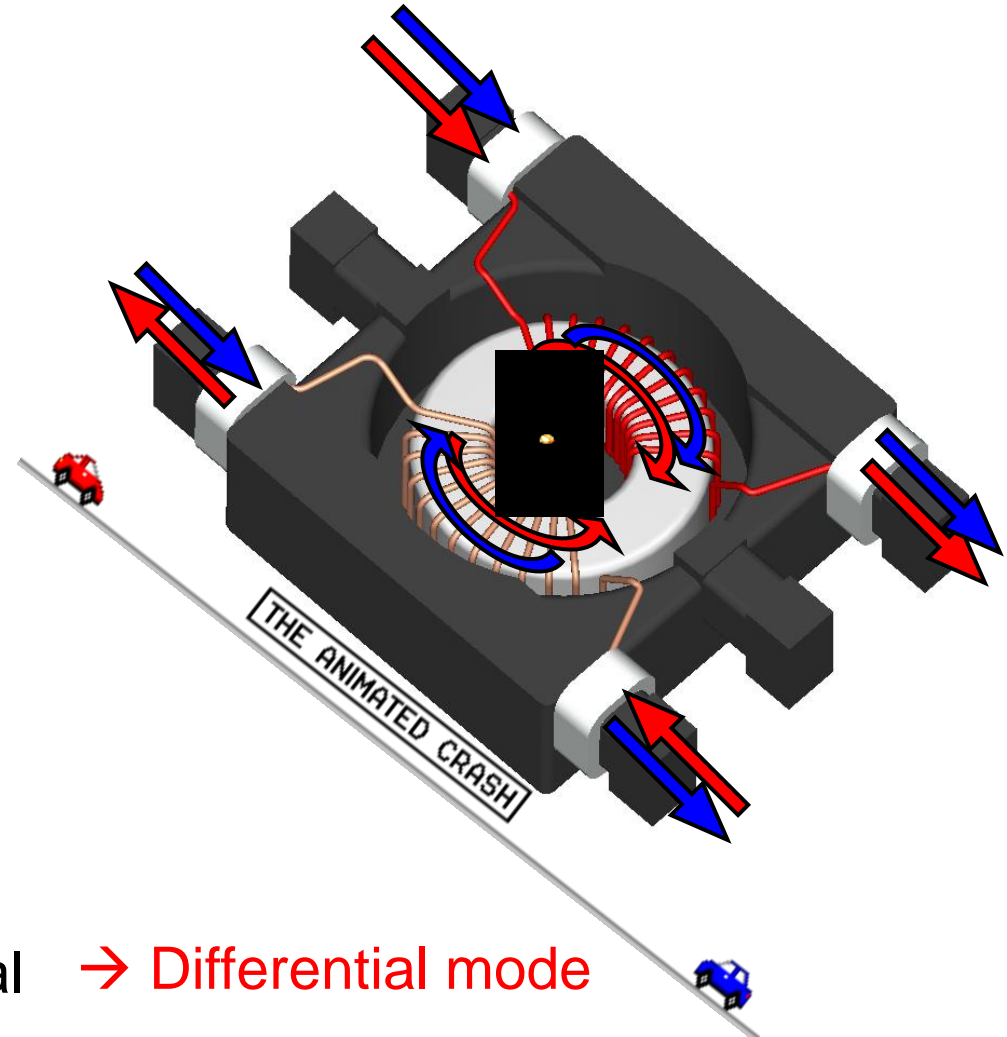
The DIFFERENTIAL Mode Signal creates a Flux in opposite directions – Thereby canceling

The COMMON MODE signal does not cancel and an Inductive Impedance is created thereby acting as a filter

Filter and Signal - Common Mode Filter (Signal theories)

Reduction of noise

- from device to environment
- from environment to device

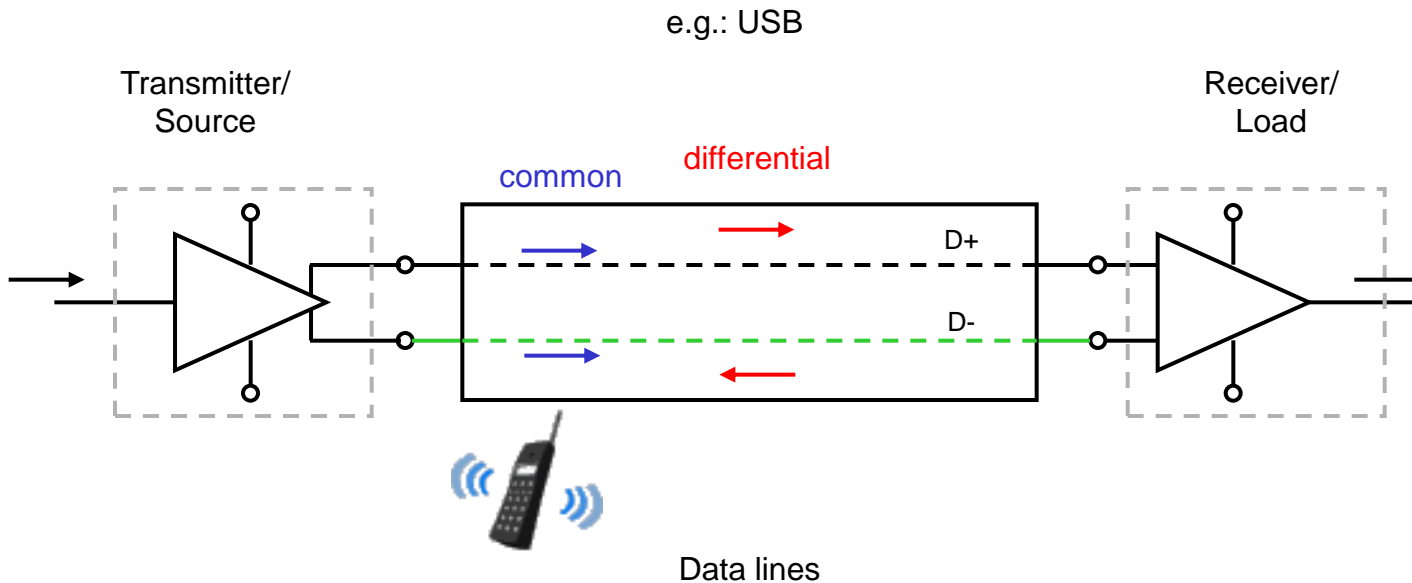


Conclusion:

- “almost” no influencing of the signal → Differential mode
- high attenuation of noise → Common mode

Filter and Signal - Common Mode Filter (Signal theories)

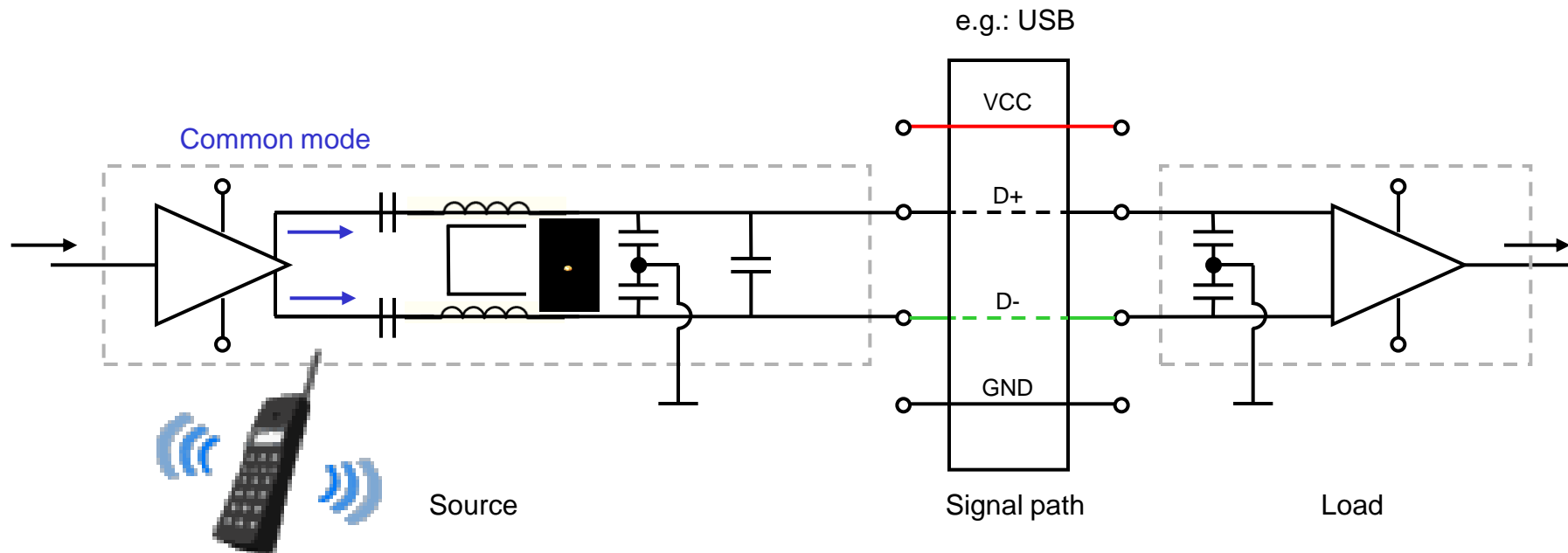
different kinds of noise:



- Common mode noise
- Differential mode noise

Filter and Signal - Common Mode Filter (Signal theories)

Filtering



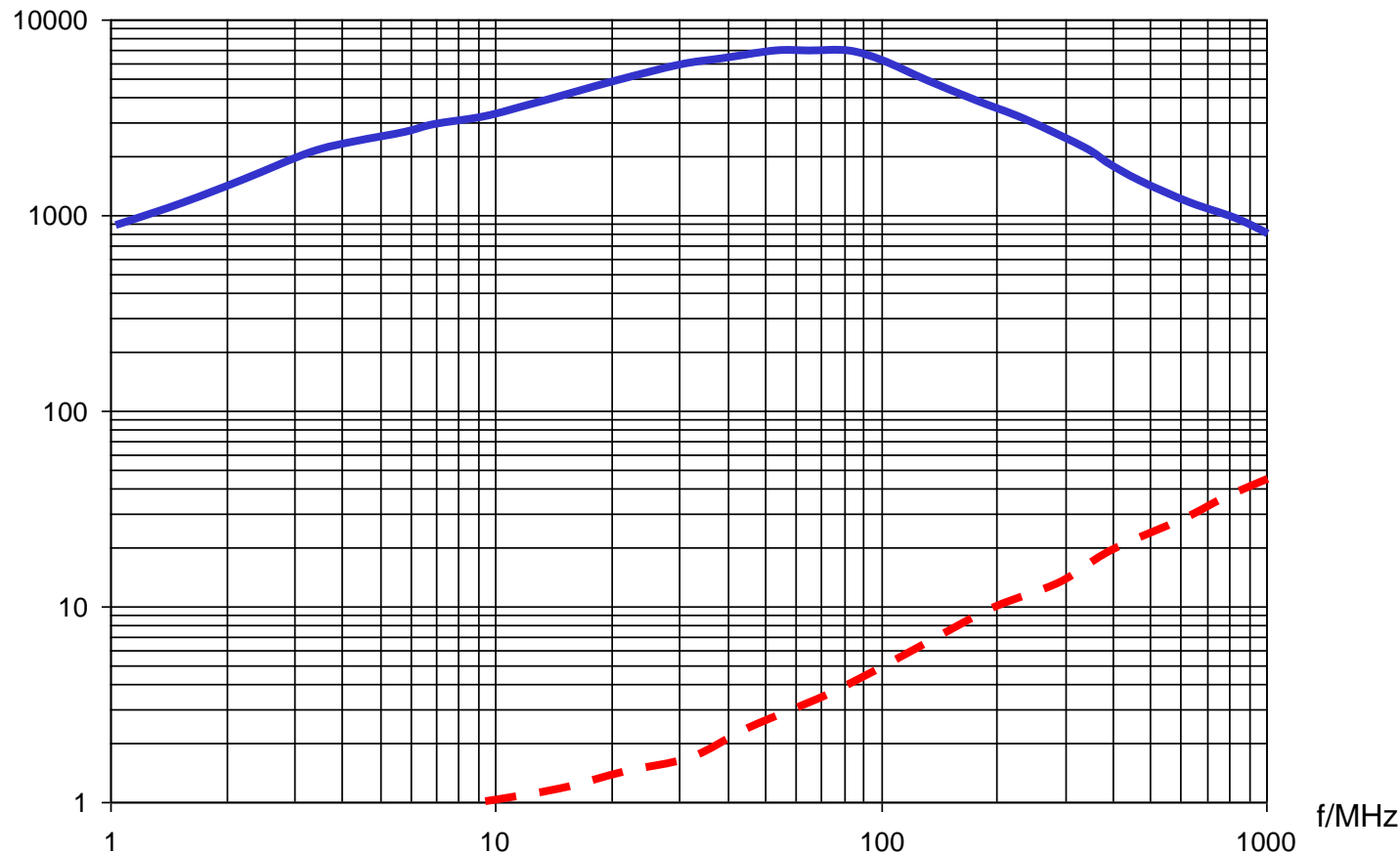
WE-CNSW Type 0805

Order Code	Impedance (Ω) @ 100 MHz	Rated voltage (V)	DCR (Ω)	Rated Current (mA)	Suitable for
744231061	67	50	0.25	400	
744231091	90	50	0.30	370	USB 2.0

Filter and Signal - Common Mode Filter (Signal theories)

When will the signal be attenuated?

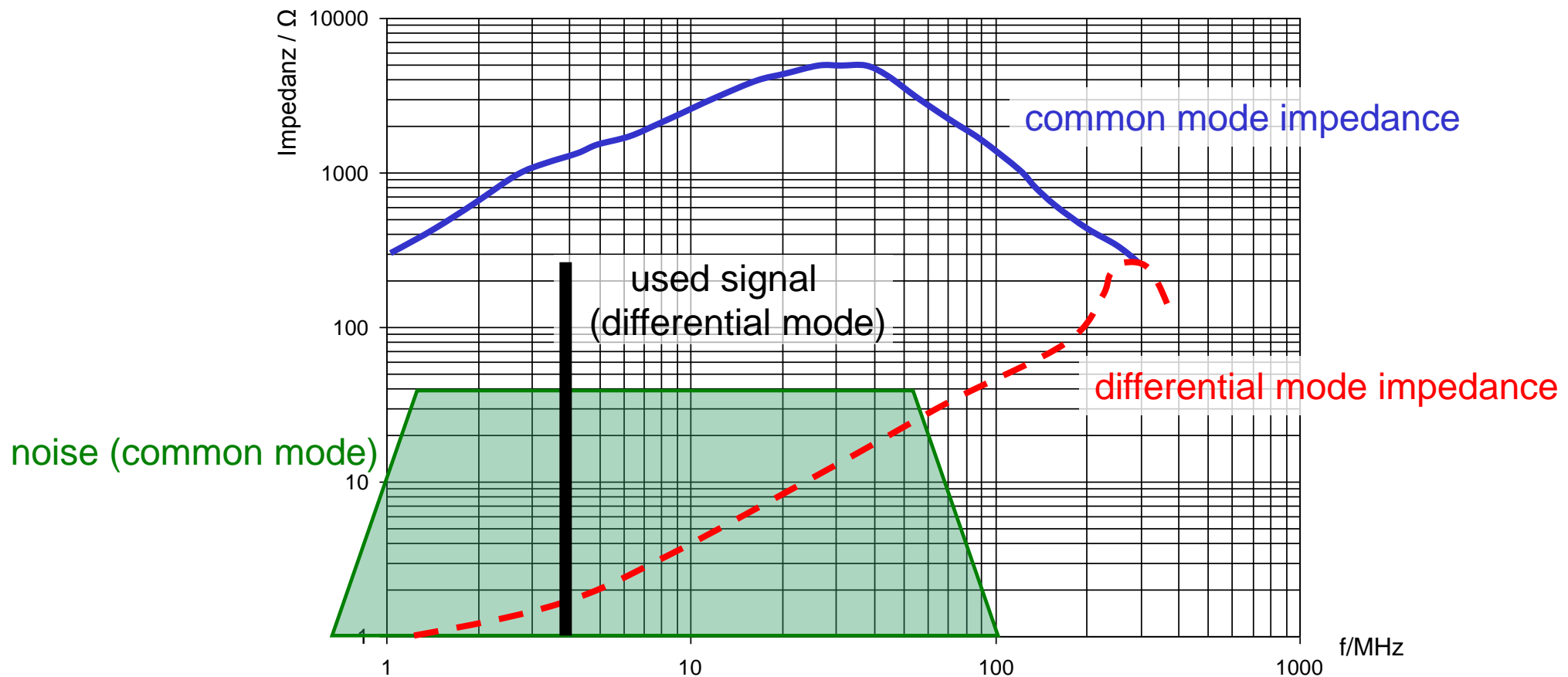
- the **Differential mode** impedance will also attenuate the **signal**



- The **Common mode** impedance will attenuate just the **noise**

Filter and Signal - Common Mode Choke

- What is the best solution to filter noise close to signal frequency?



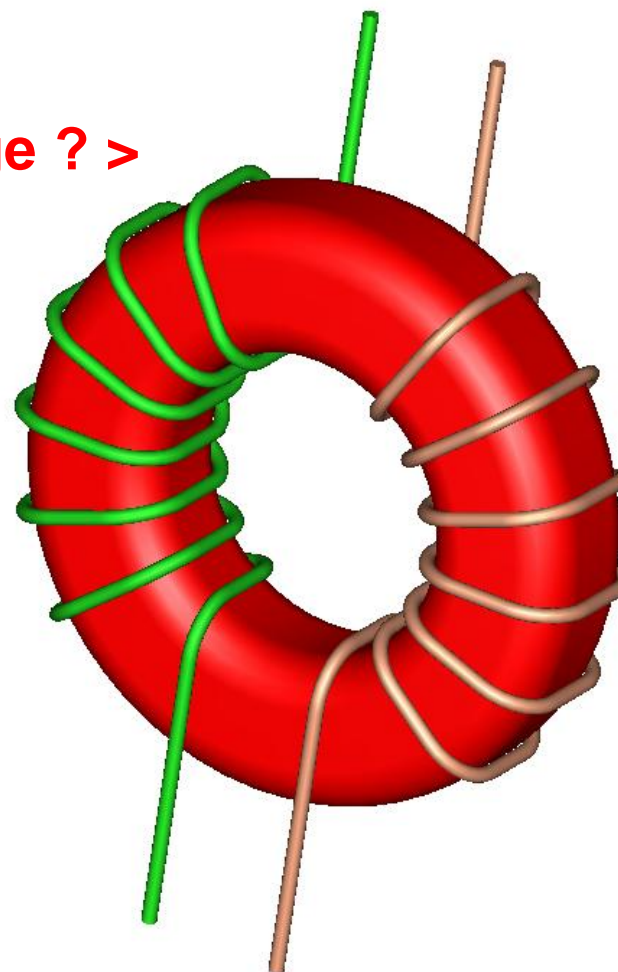
Filter and Signal - Common mode choke (Construction)

bifilar



$$L_S \sim 0,01 \dots 0,1 \% * L_R$$

sectional



$$L_S \sim 0,5 \dots 2\% * L_R$$

< ? Advantage ? >

Filter and Signal - Common mode choke (Construction)



bifilar



- Less differential impedance
- High capacitive coupling
- Less leakage inductance



- Data lines
→ USB, Fire-wire, CAN, etc.
- Power supply
- Measuring lines
- Sensor lines

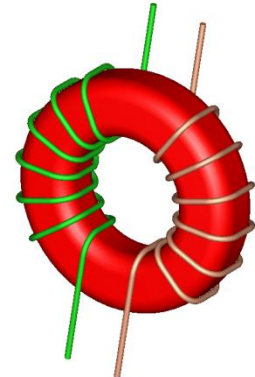


• WE-CNSW



• WE-SLM

sectional



- Low capacitive coupling
- High leakage inductance



- Power supply input /output filter
→ CMC for mains power
- High voltage application
- Measuring lines
- Switching power supply decoupling



- WE-LF
- WE-SLx-Series

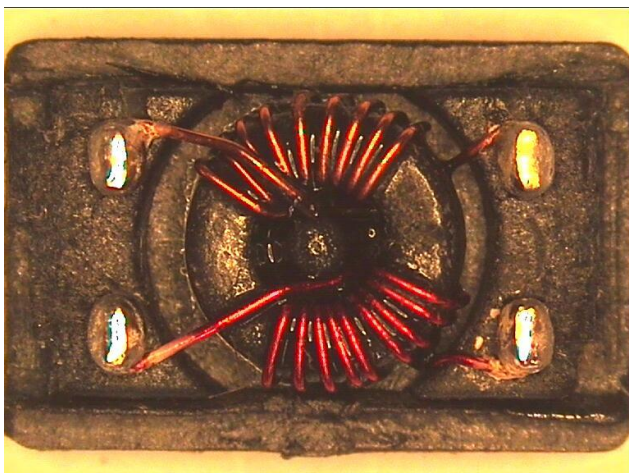


- WE-CMB
- WE-VB / VB2

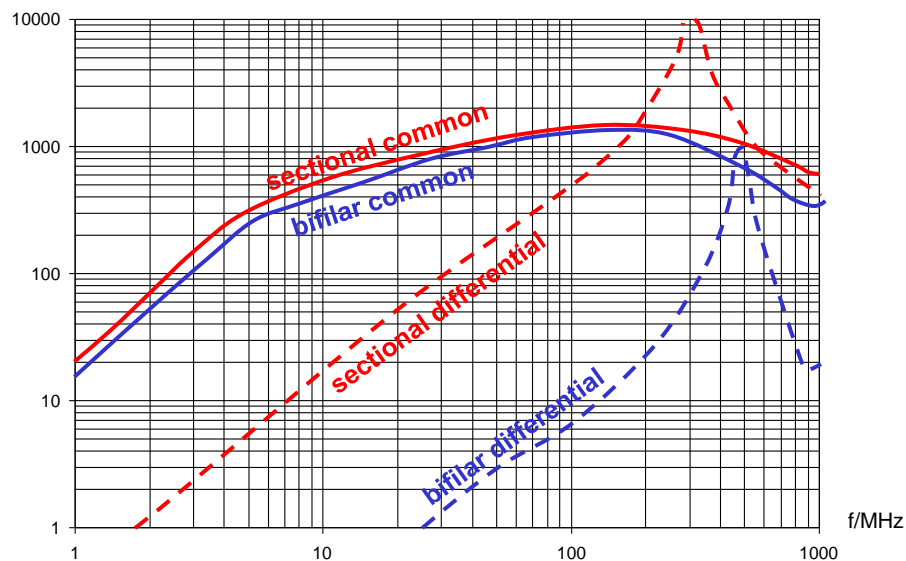
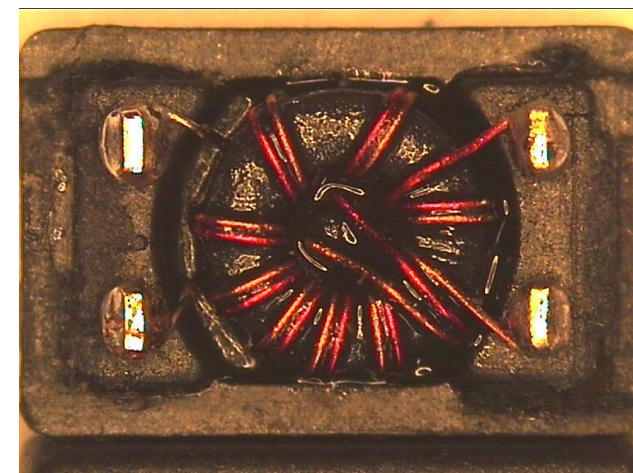
Filter and Signal - Common mode choke (Construction)



WE-SL2 744227S
sectional winding



WE-SL2 744227
bifilar winding

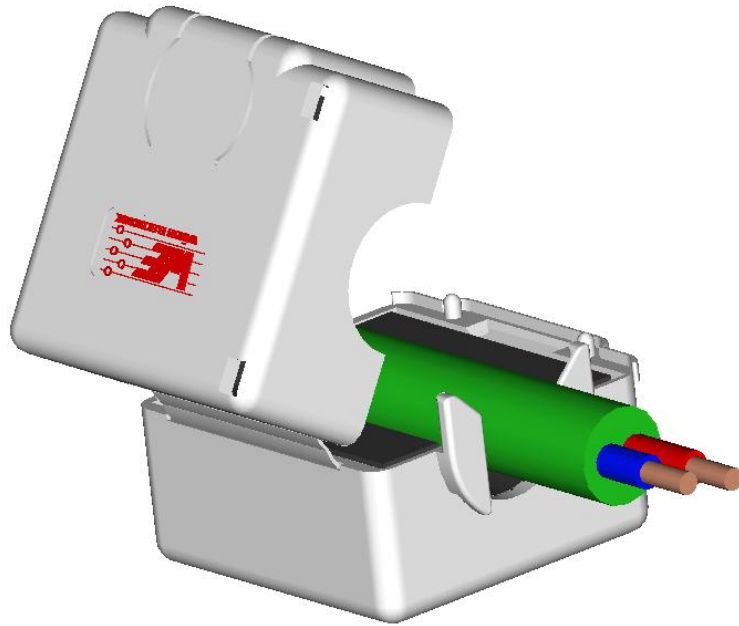


Filter and Signal - Common mode choke (Construction)

WE-split ferrite – Is it a CMC?

- Yes, CMC with one winding

e.g. 74271712



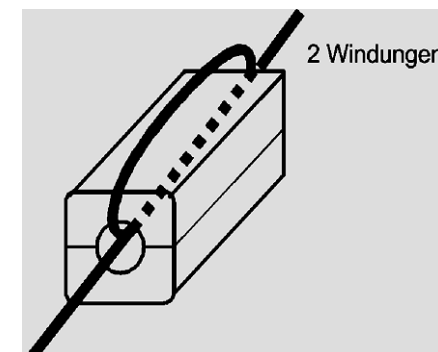
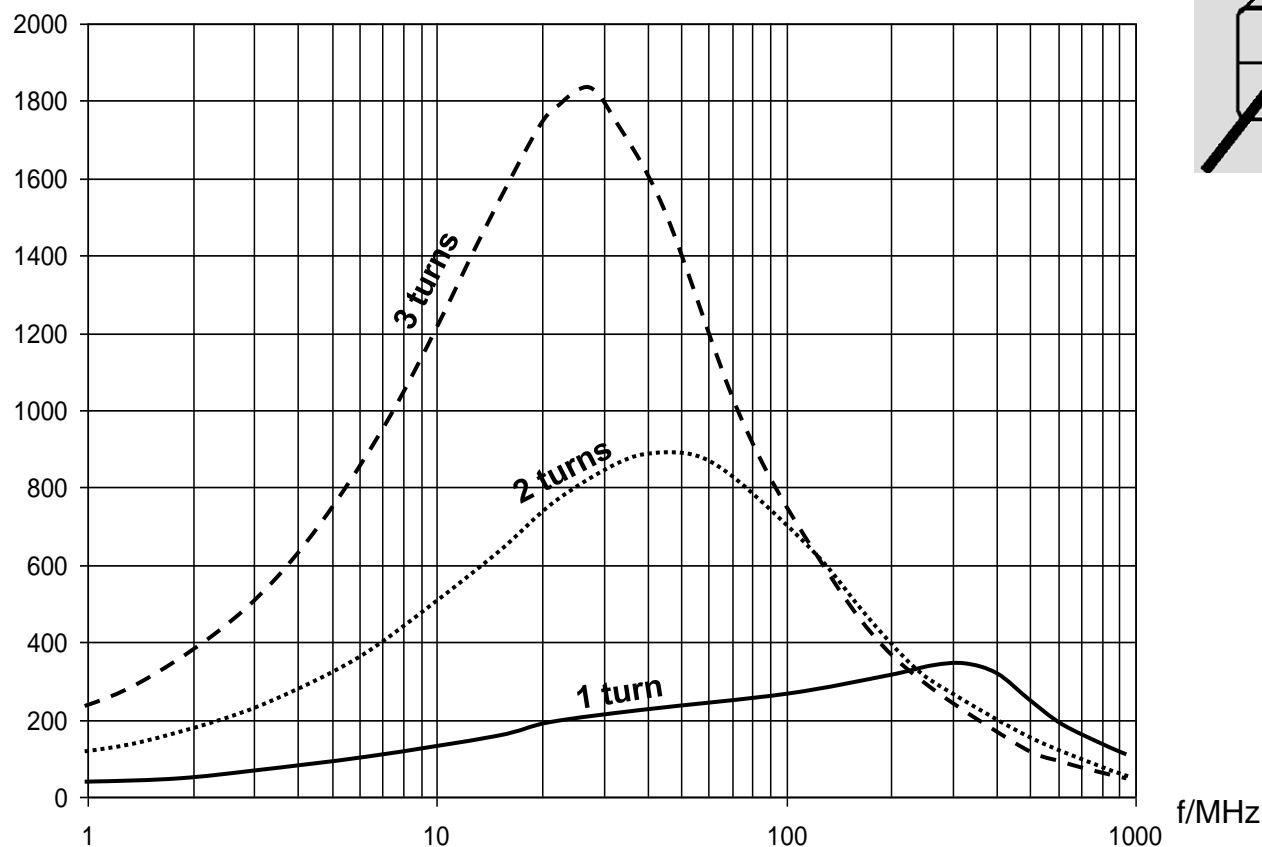
comparable with bifilar winding CMC



- both will absorb Common Mode interferences

Filter and Signal - Common mode choke (Ferrite core)

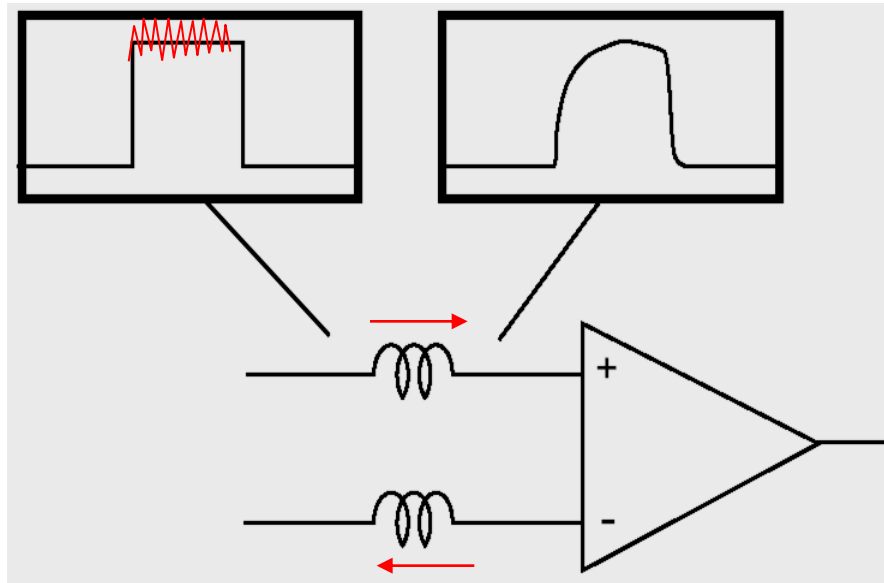
Increase the number of turns means:



Filter and Signal - Common mode choke (Advantages)

Filtering with two inductors or chip beads

Signal before filtering



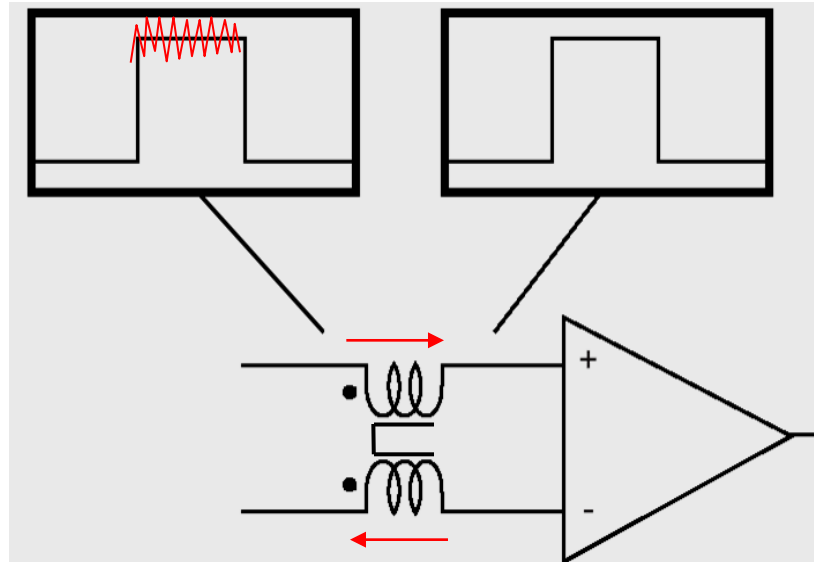
Signal after filtering

Rise time of the signal is affected,
which could cause problems for
fast data signal lines

Filter and Signal - Common mode choke (Advantages)

Filter with a common mode choke

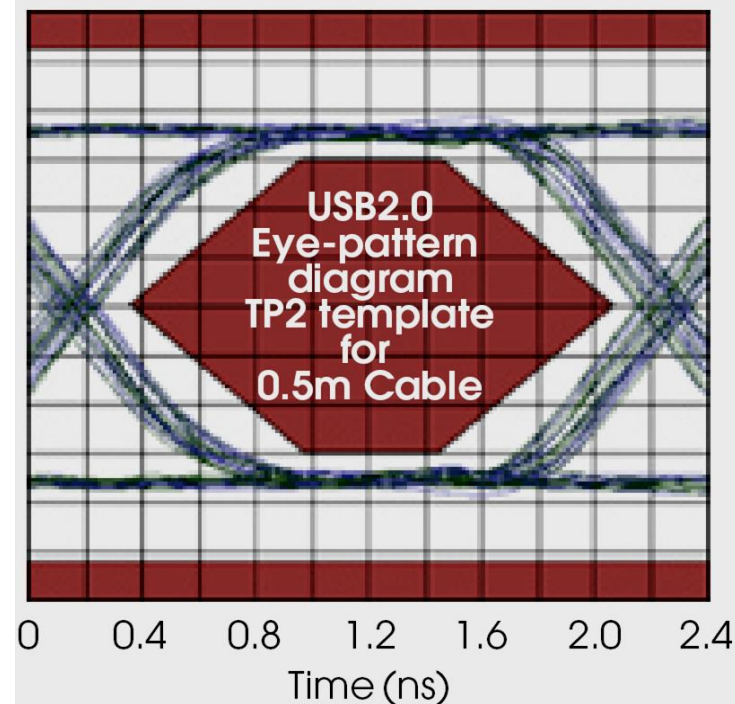
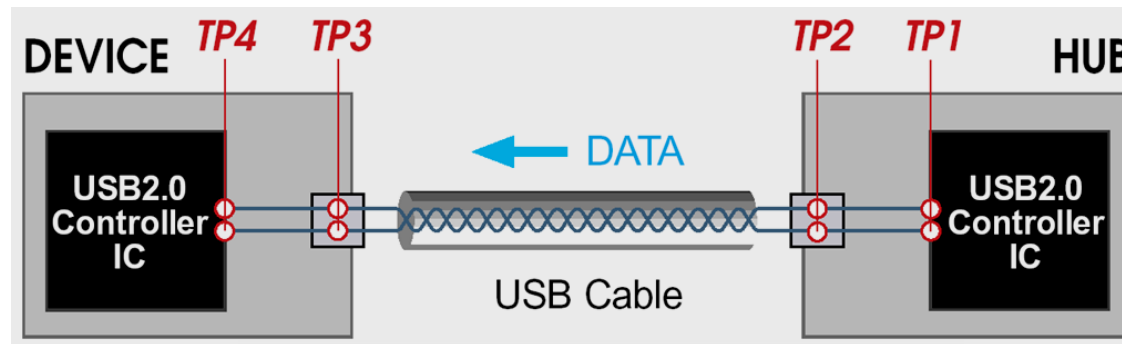
Signal before filtering



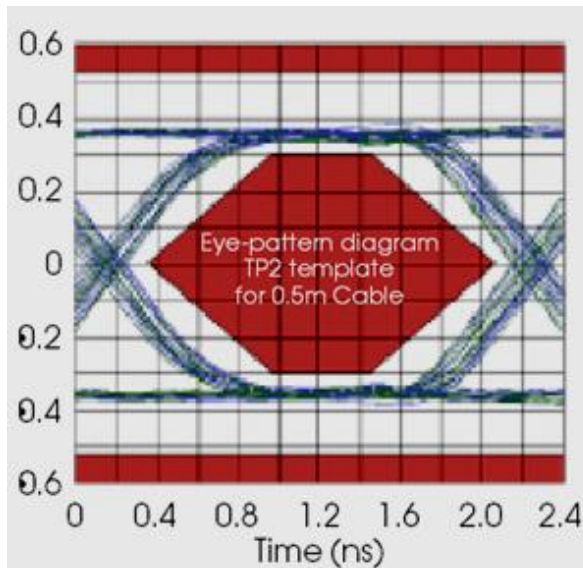
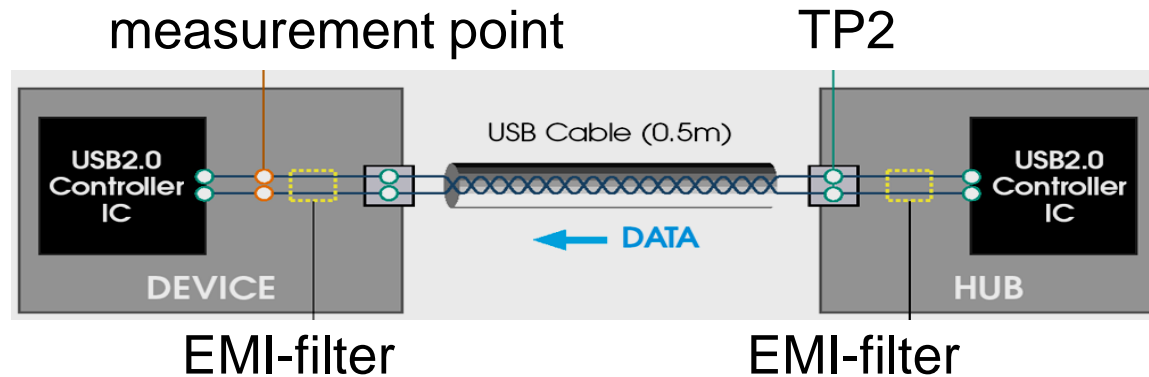
Signal after filtering

No affect on the signal rise time,
because of magnetic field
compensation

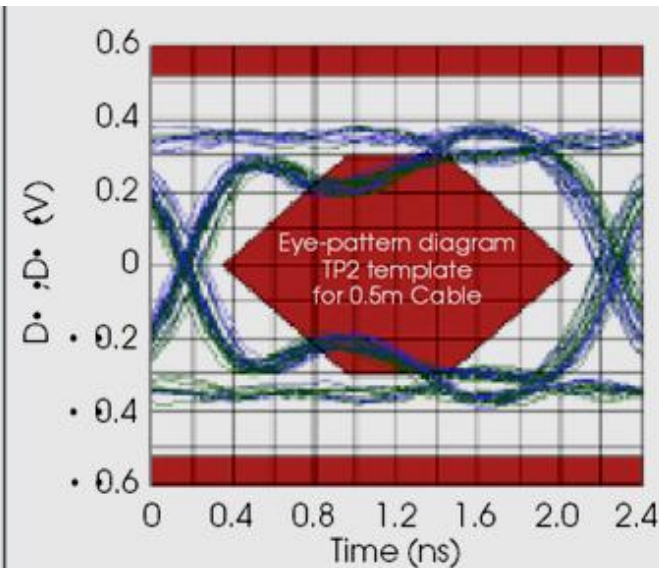
Filter and Signal - USB 2.0 Filtering for common mode noise



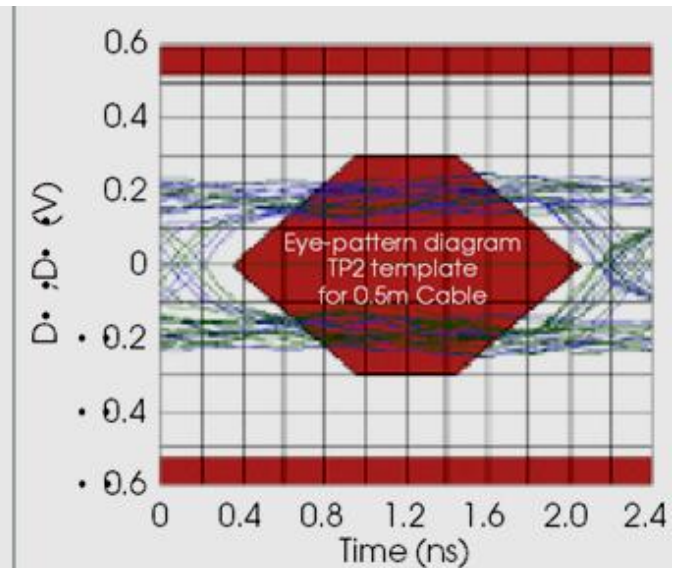
Filter and Signal – USB 2.0 Filtering with WE-CNSW



90 Ohm @ 100 MHz C.M.
20 Ohm @ 240 MHz D.M.



600 Ohm @ 100 MHz C.M.
40 Ohm @ 240 MHz D.M.



WE-CBF
120 Ohm @ 100 MHz

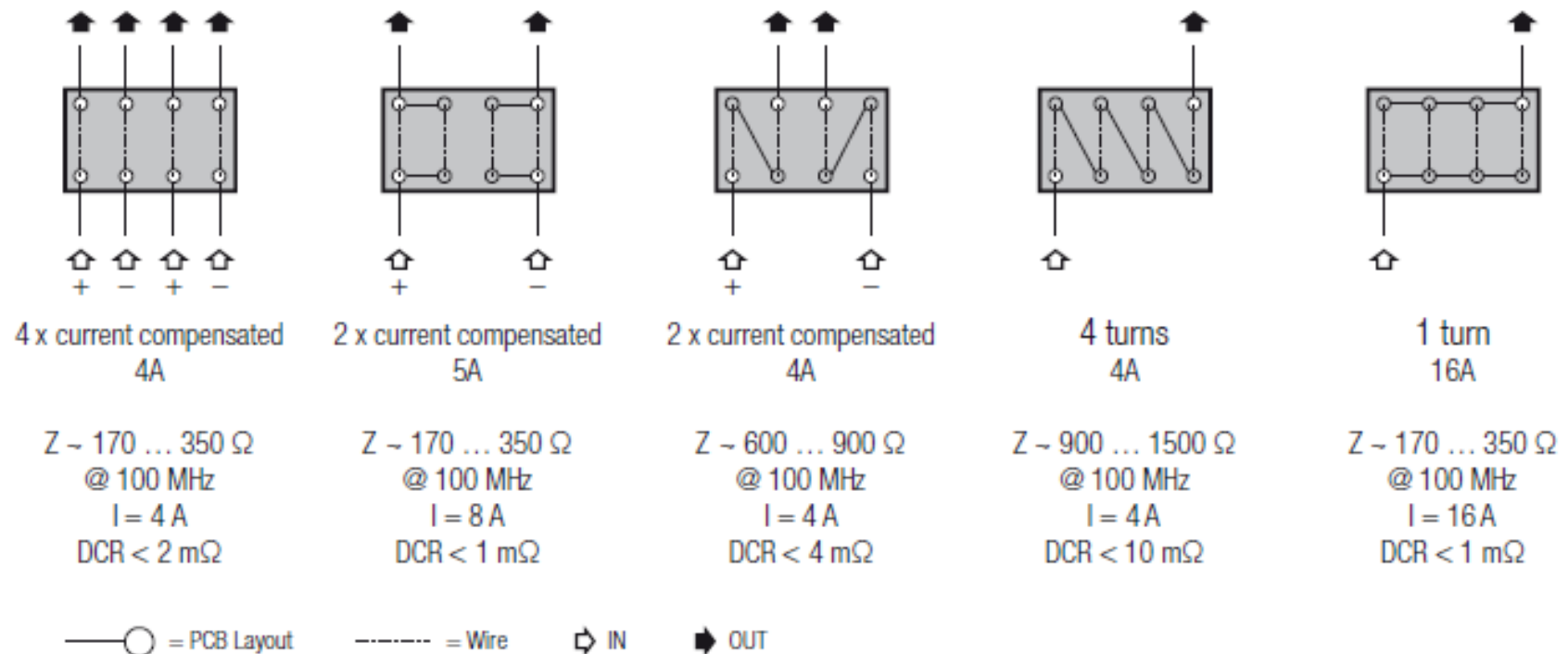
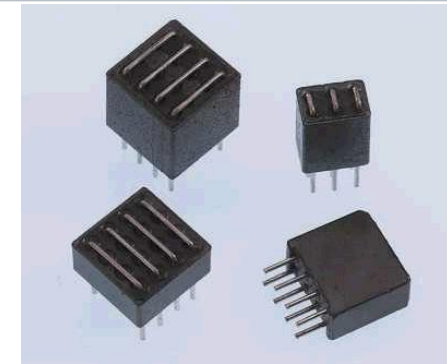
Too much differential mode impedance distorts the USB 2.0 eye pattern



Filter and Signal – CMC (Multiple usage “5in1”)

- WE-MLS

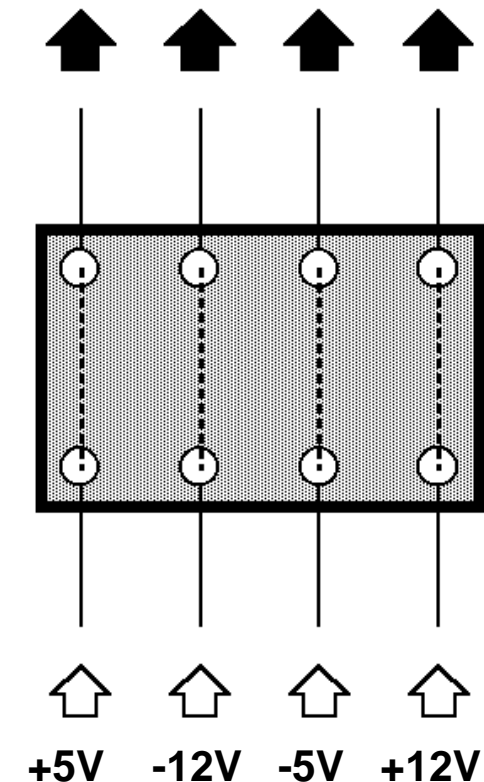
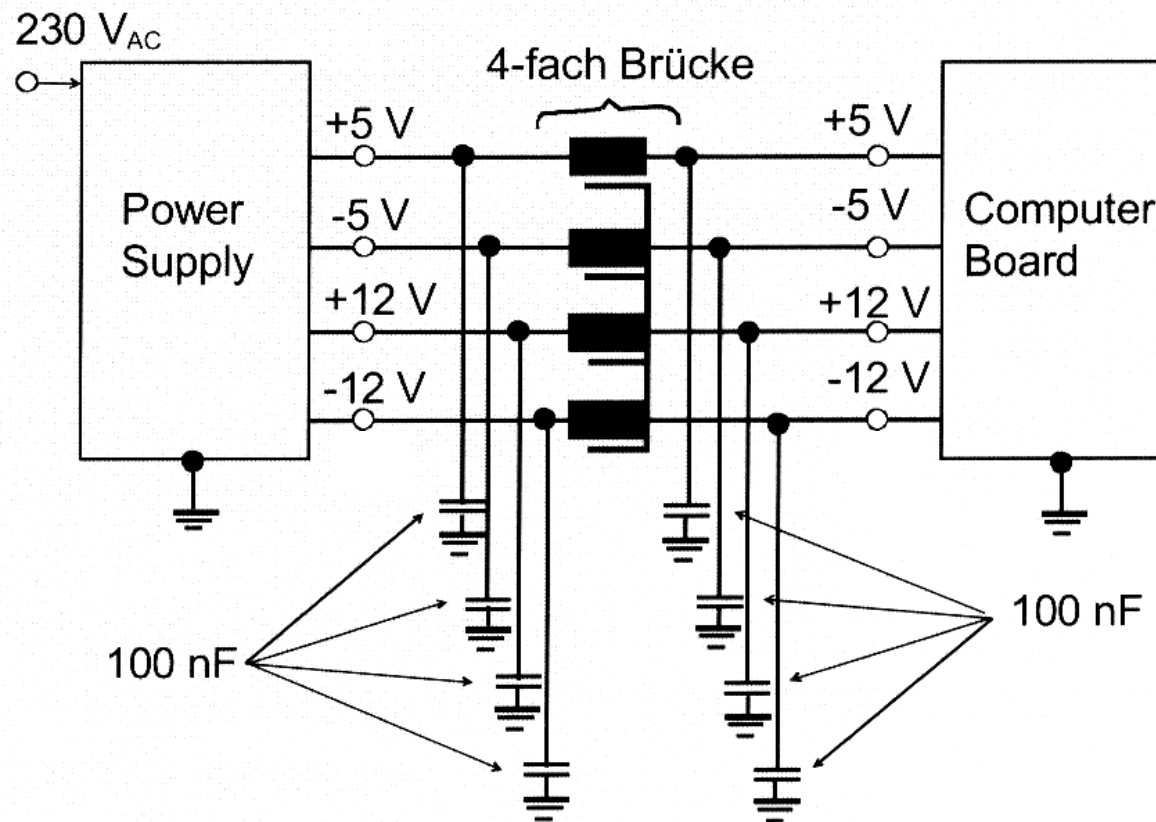
→ can be easy designed at PCB layout connection
1 component for 5 application



Optimal for power supply filtering ($U < 60VDC$); charger, sensors, etc.

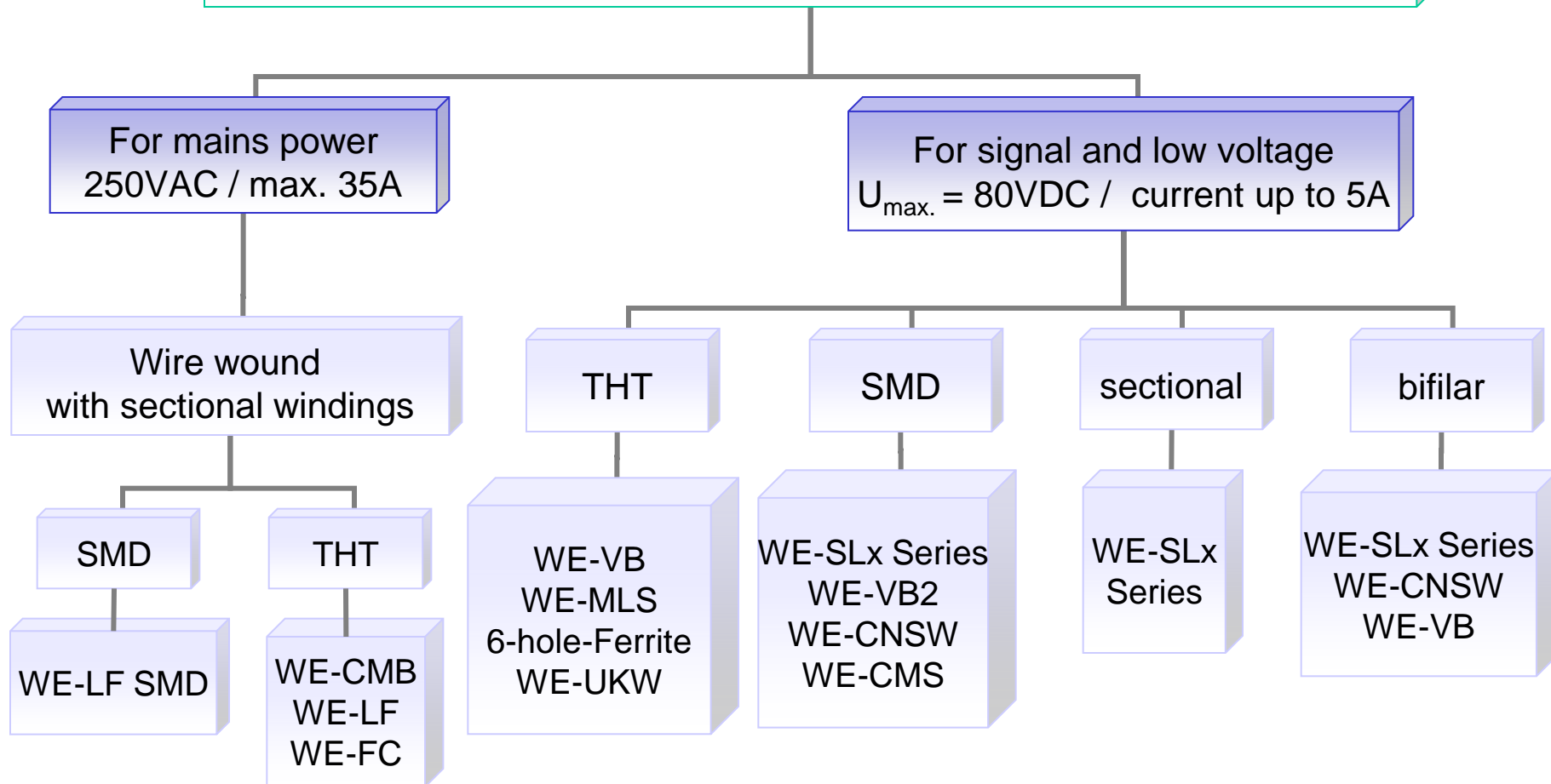
Filter and Signal – CMC (Multiple usage “5in1”)

- Application WE-MLS: power supply filtering



Filter and Signal - Common mode chokes (Line card)

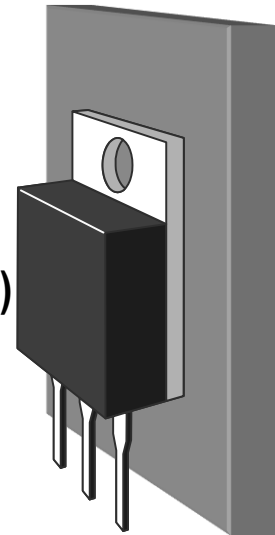
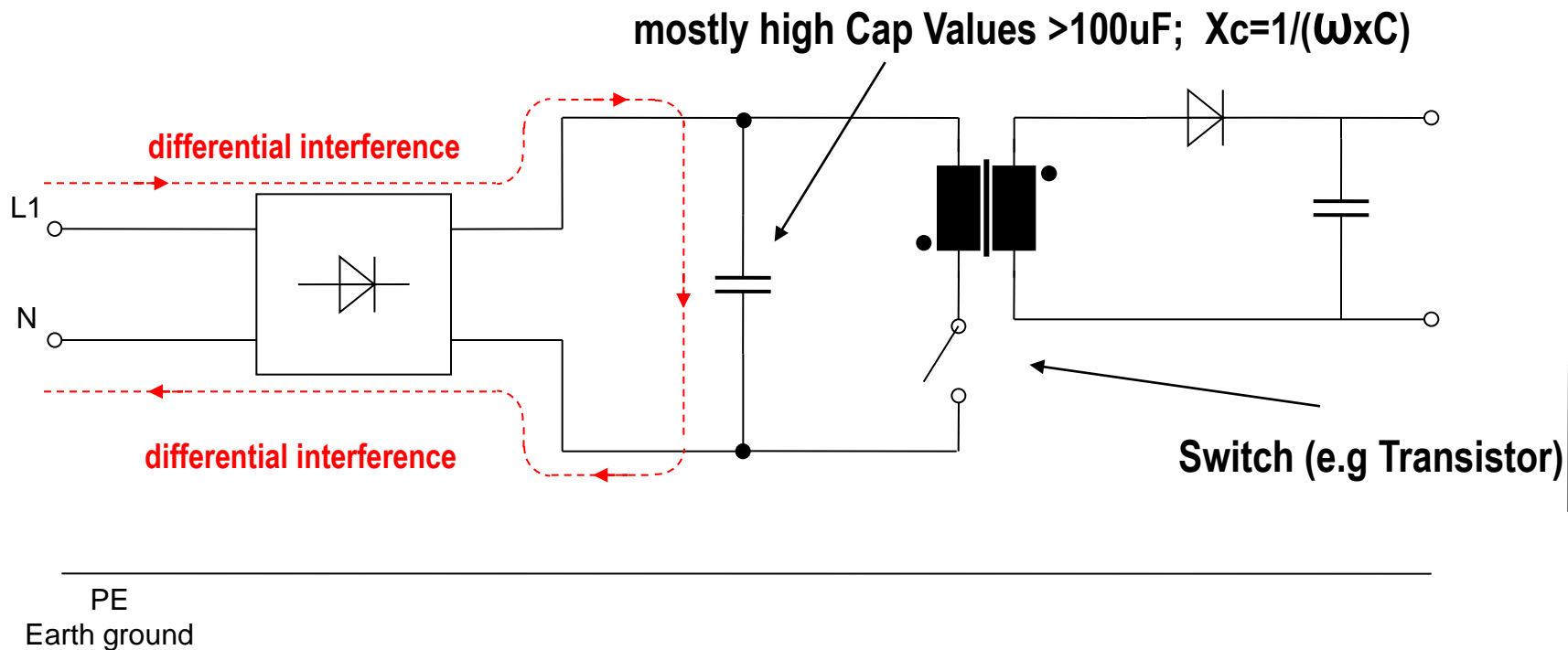
CMC from Würth Elektronik eiSos



Filter and Signal - Differential Mode Noise Flyback Converter



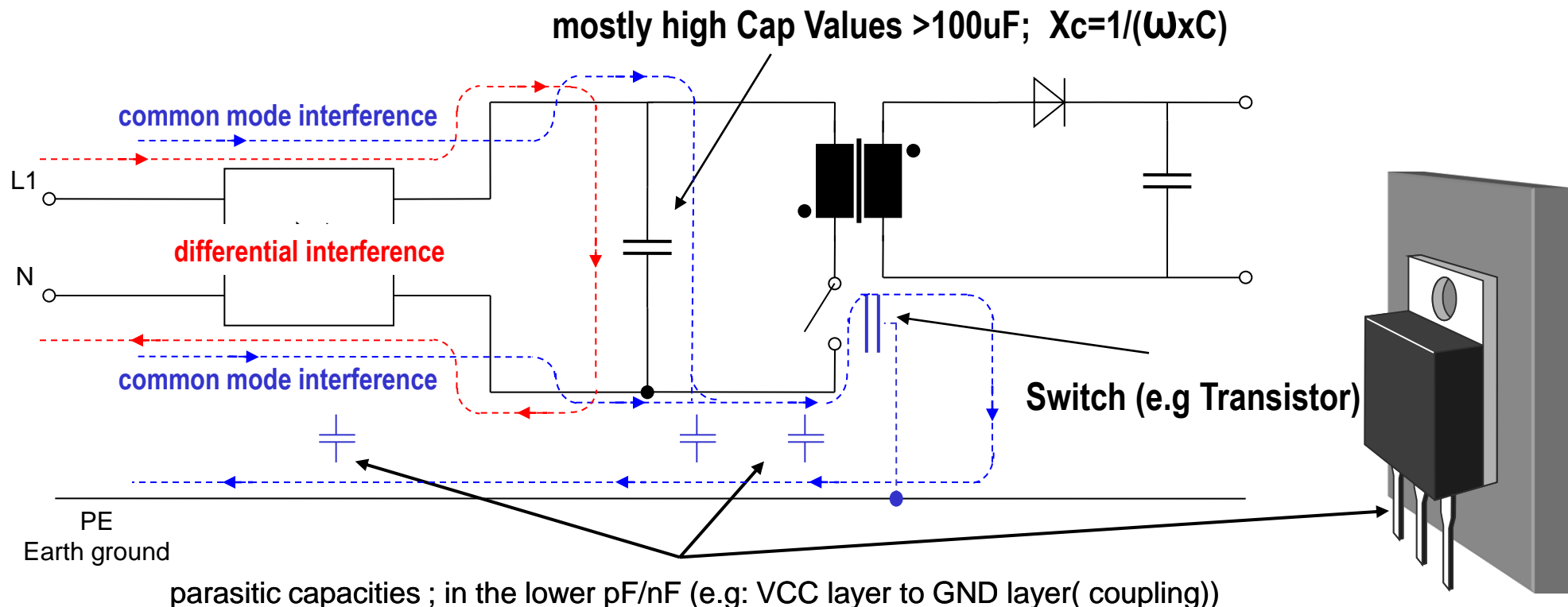
Appearance of **differential noises** on the input line of a Flyback Converter



➔ differential interference occurs mainly at **lower** frequencies

Filter and Signal - Common Mode Noise: Flyback Converter

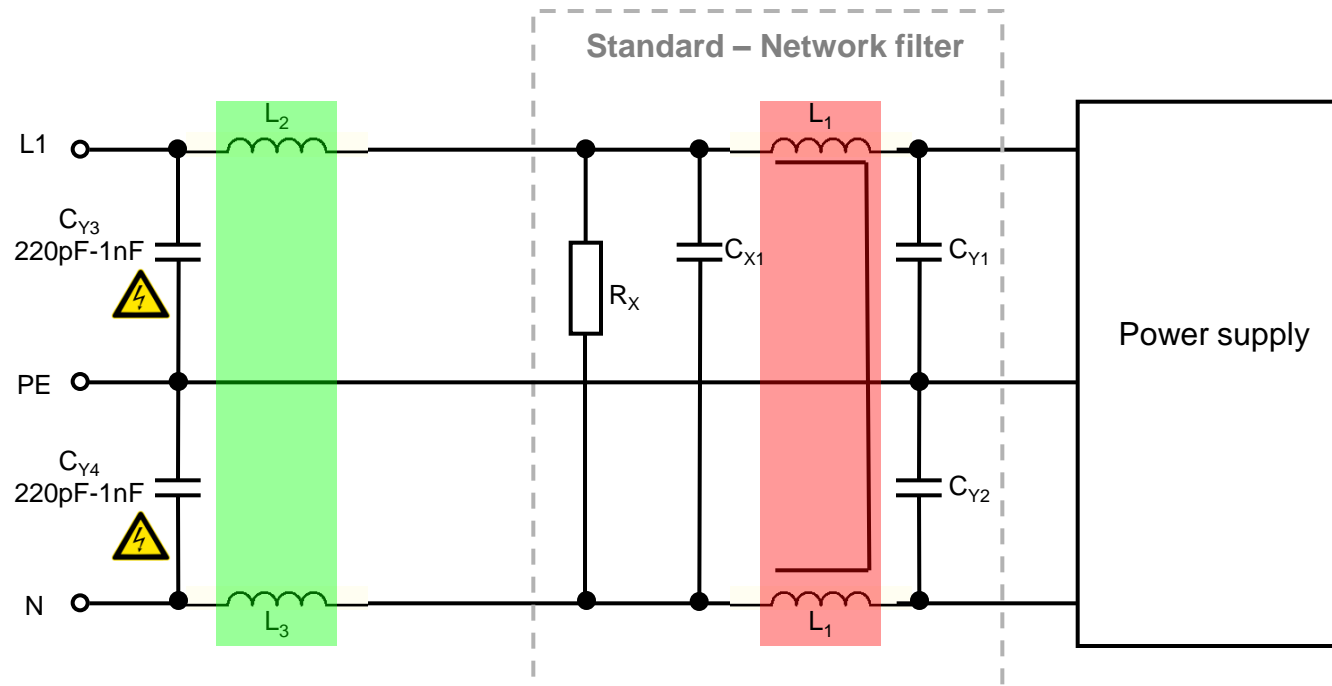
Appearance of **common mode noises** on the input line of a Flyback Converter



 **common mode interference occurs mainly at *higher* frequencies**

Filter and Signal - Usual mains power filter

- Build your own one – possibility for above ~ 30 MHz as well



6-hole-Ferrite bead ($I < 3$ to $5A$)

742 750 1 – 742 750 46

Sleeve choke ($I < 1A$)

742 760 3 – 742 760 6

Rod core inductor ($I \geq 30A$)

744 710 1 – 744 716 0

WE-LF

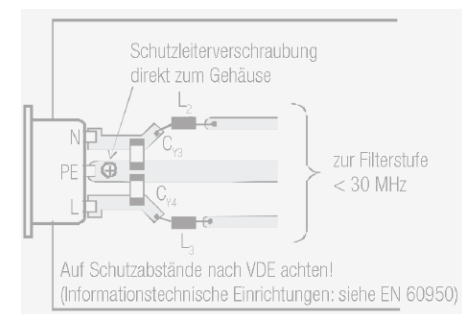
744 612 002 7

WE-CMB

744 821 039

WE-FC

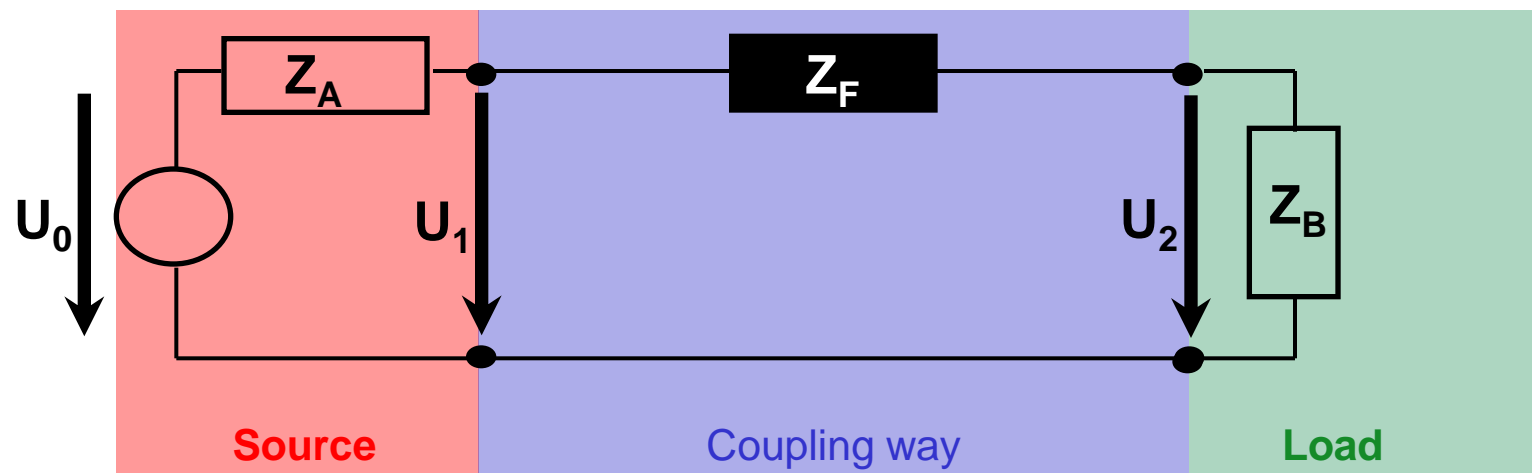
744 864 040 4



Insertion Loss



Insertion loss - Definition



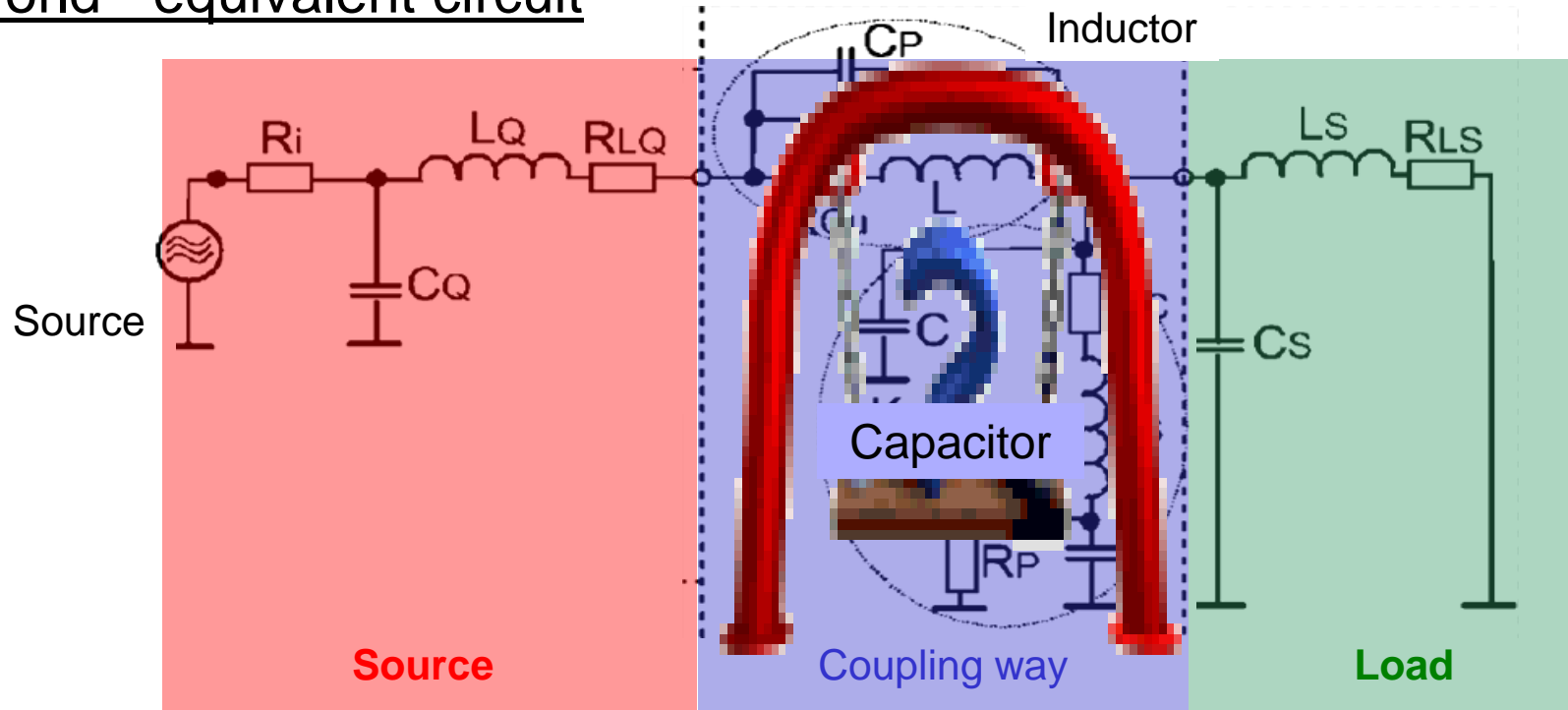
Impedance =>
$$Z_F = 10 \log \left(\frac{P_{in}}{P_{out}} \right)$$

System Attenuation =>

$$A = 20 \log \frac{Z_A + Z_F + Z_B}{Z_A + Z_B} \quad in \quad (dB)$$

Insertion loss - Definition

The real world - equivalent circuit

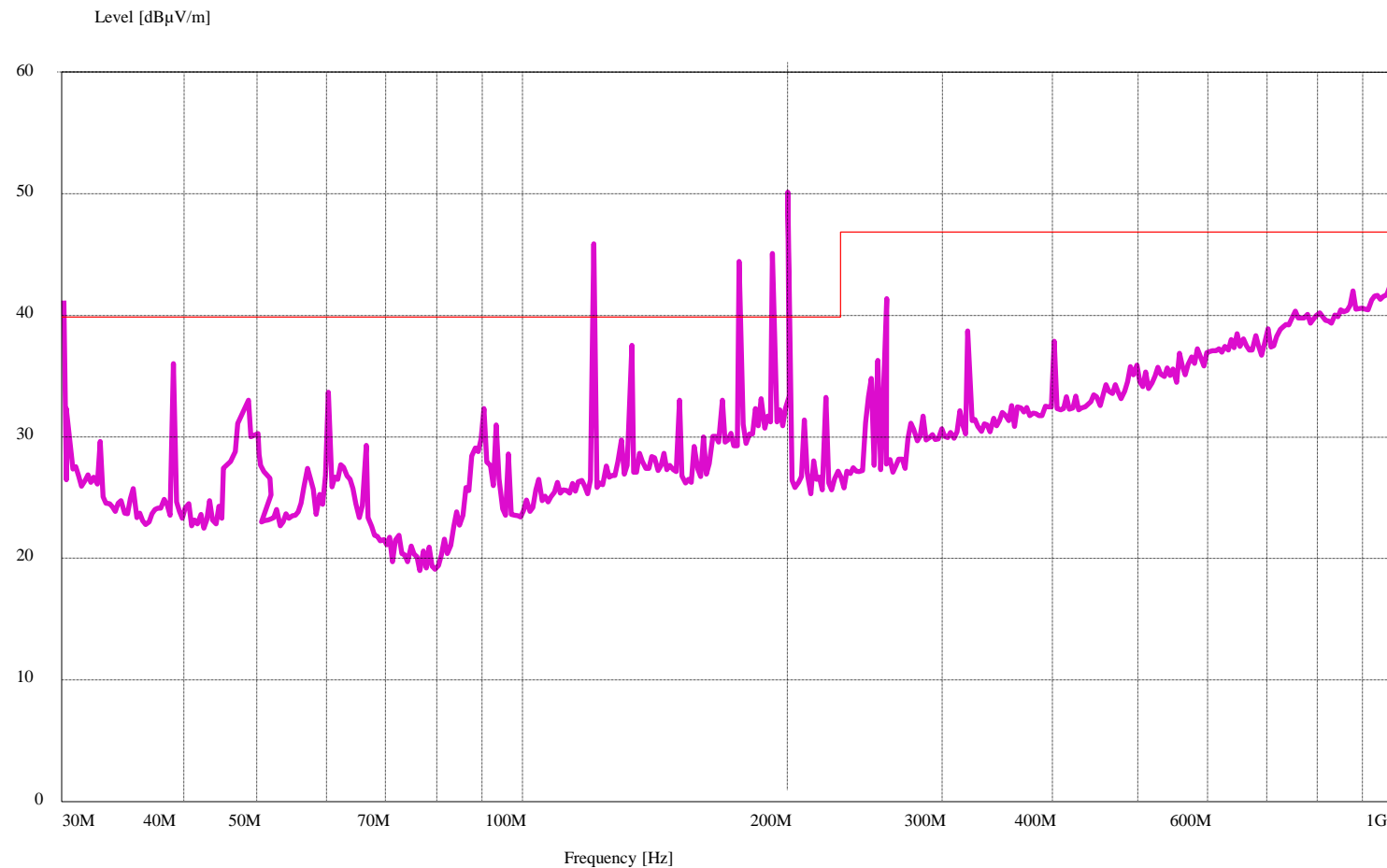


- practical values for source and load impedance

→ Grounding planes	1 ... 2 Ω
→ Vcc distribution	10 ... 20 Ω
→ Video- /Clock- /Data line	50 ... 90 Ω
→ long data lines	90 ... >150 Ω

Insertion loss - Example

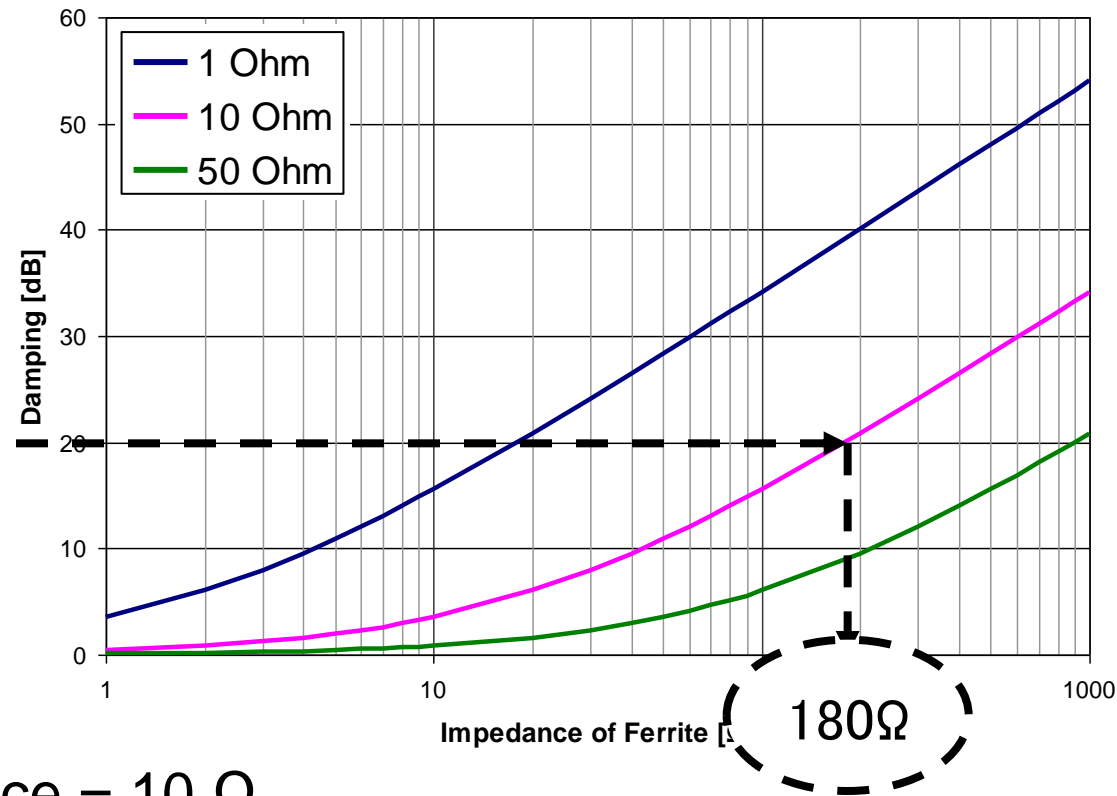
- Original measurement



Insertion loss - Example

→ Application: Power supply

→ 20dB @ 200 MHz

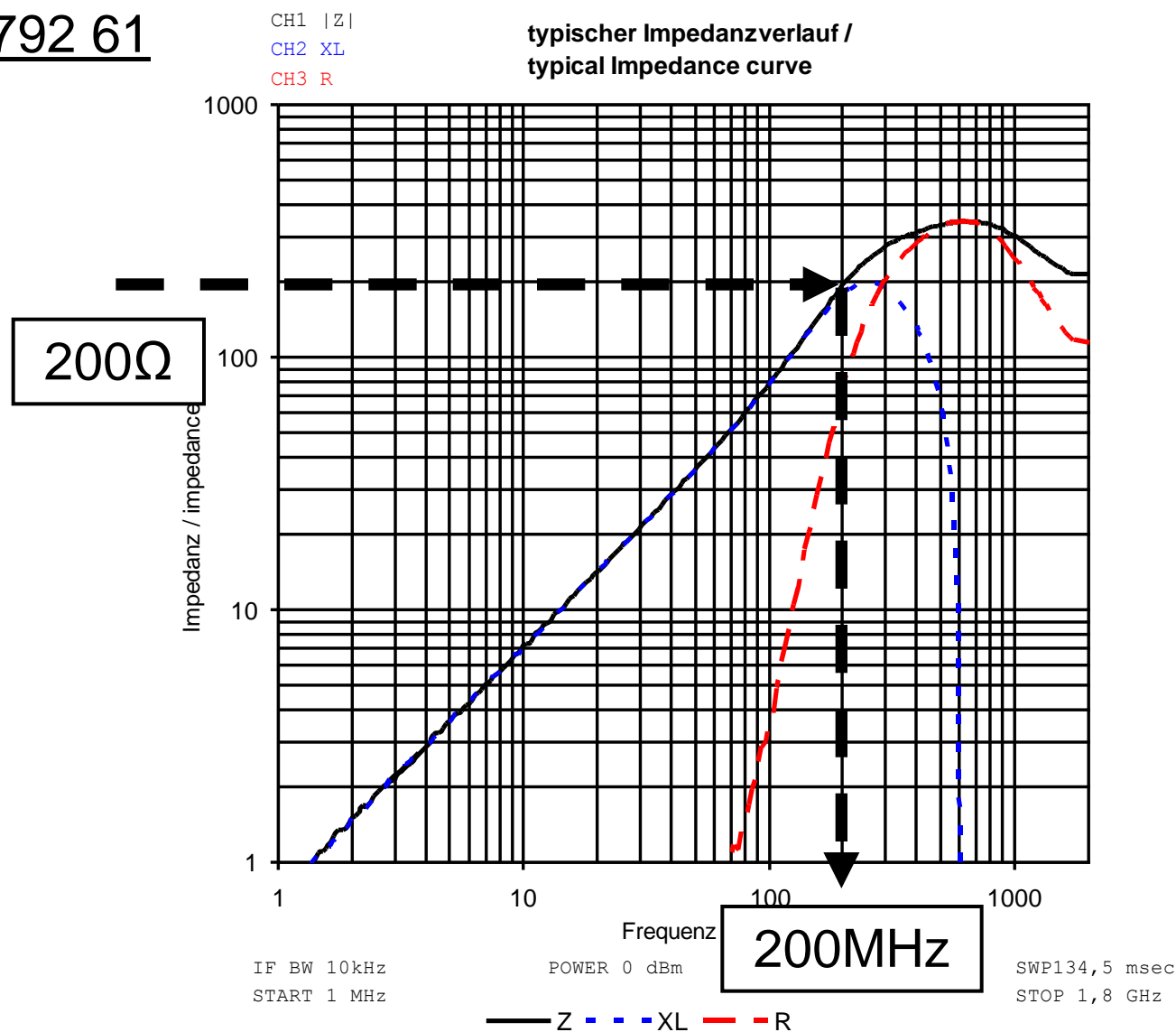


- system impedance = 10 Ω

→ catalogue: WE-CBF 742 792 61

Insertion loss - Example

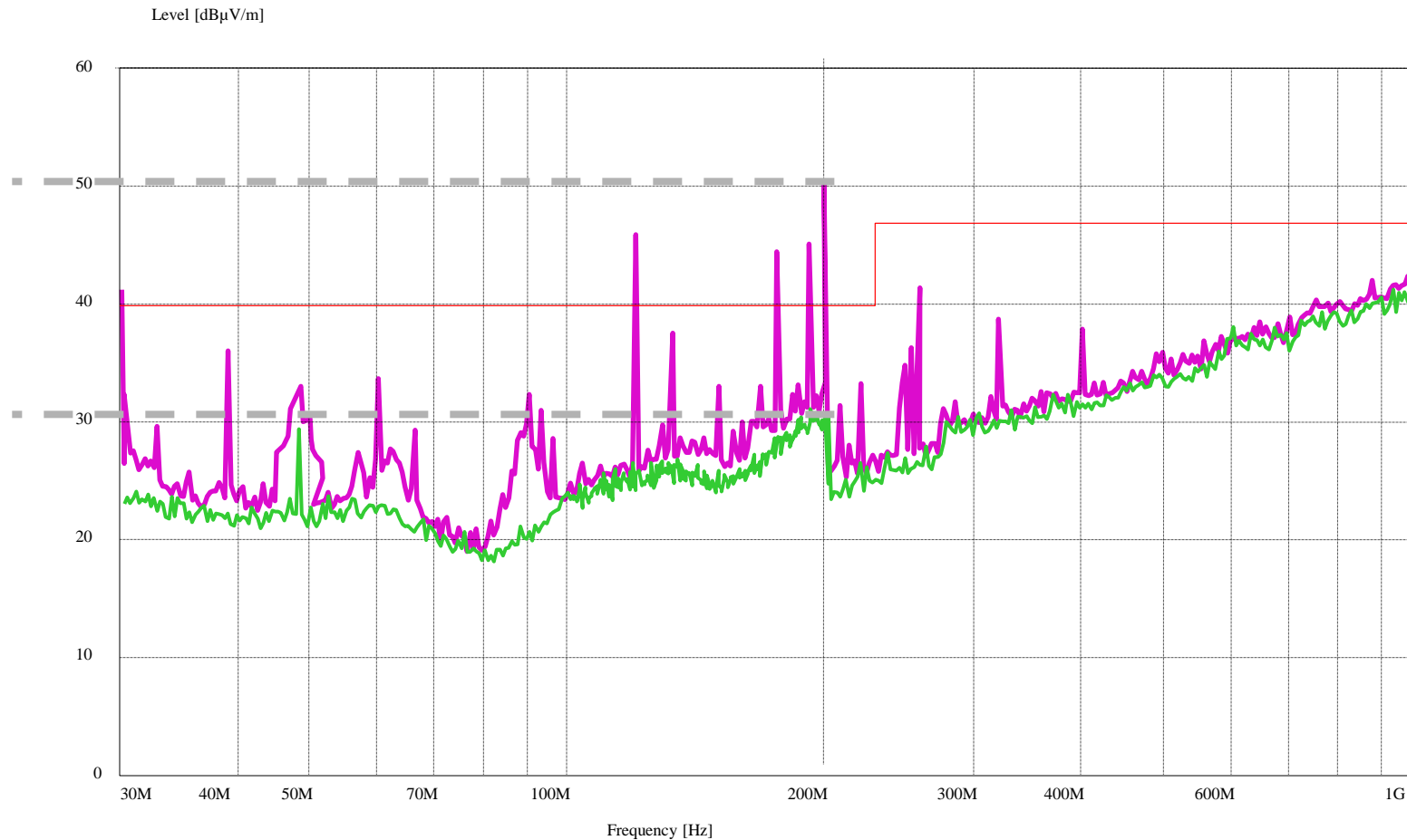
WE-CBF 742 792 61



Insertion loss - Example

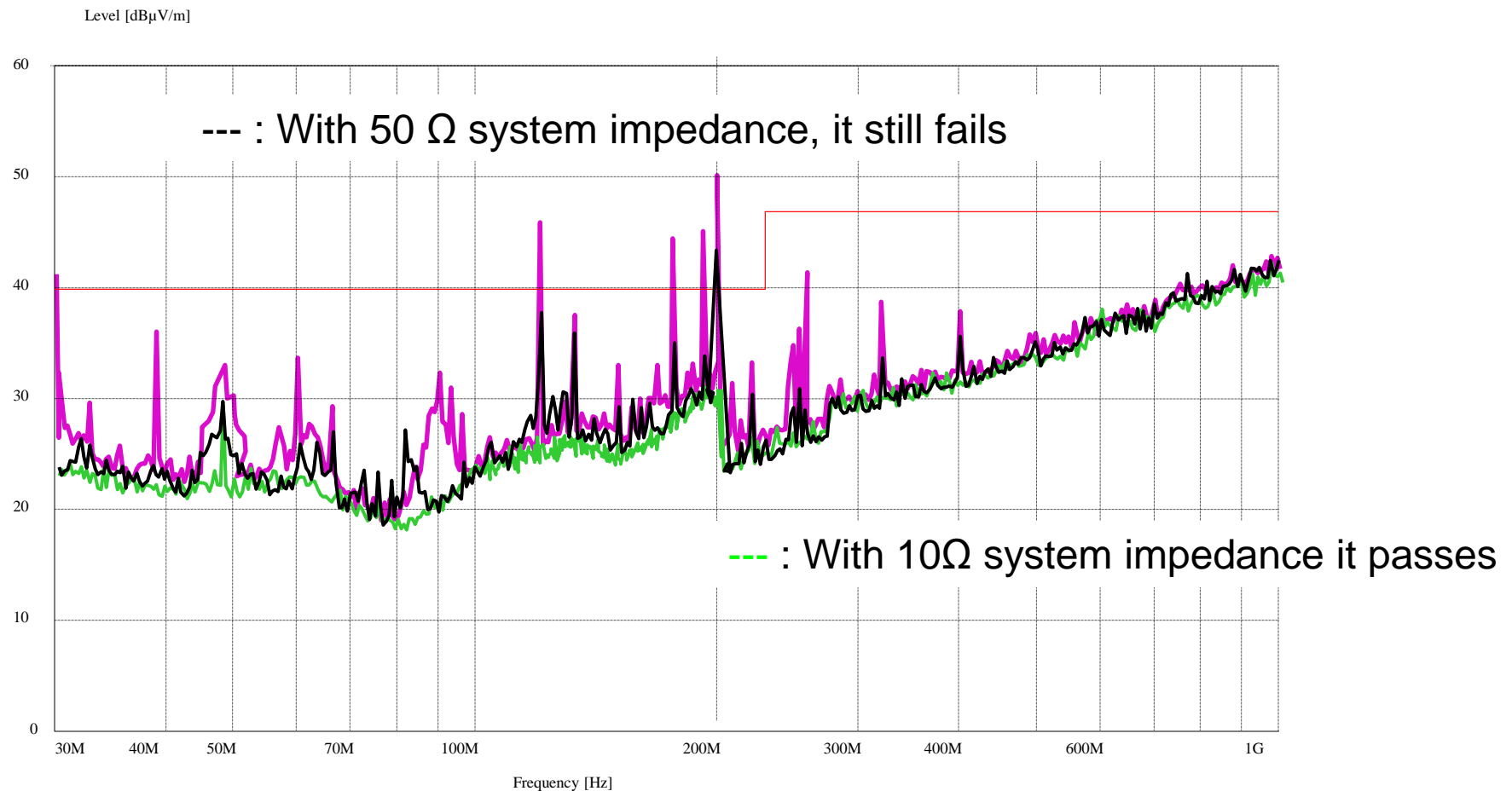
- Check the results

→ Measuring the emission and compare the attenuation



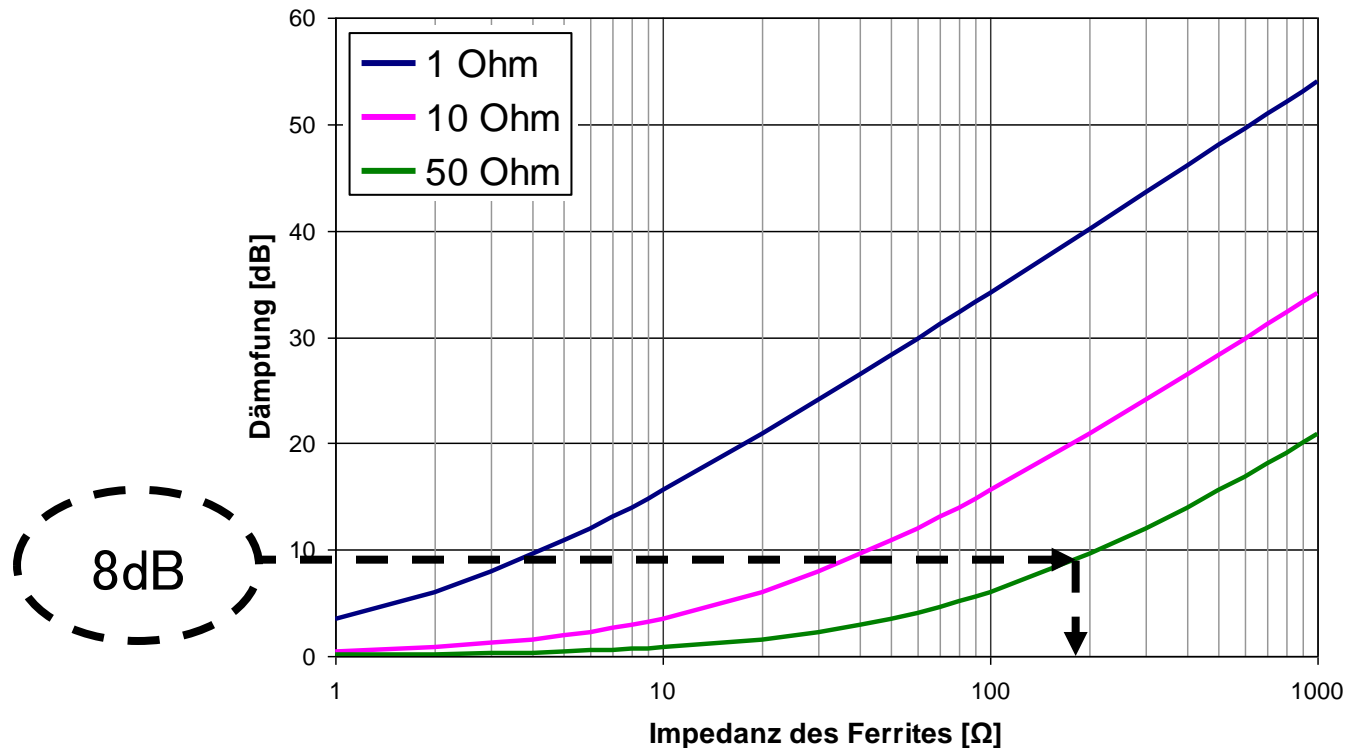
Insertion loss - Example

- Choosing different system impedance
- Effect on video/clock/dataline system impedance (50Ω)



Insertion loss - Example

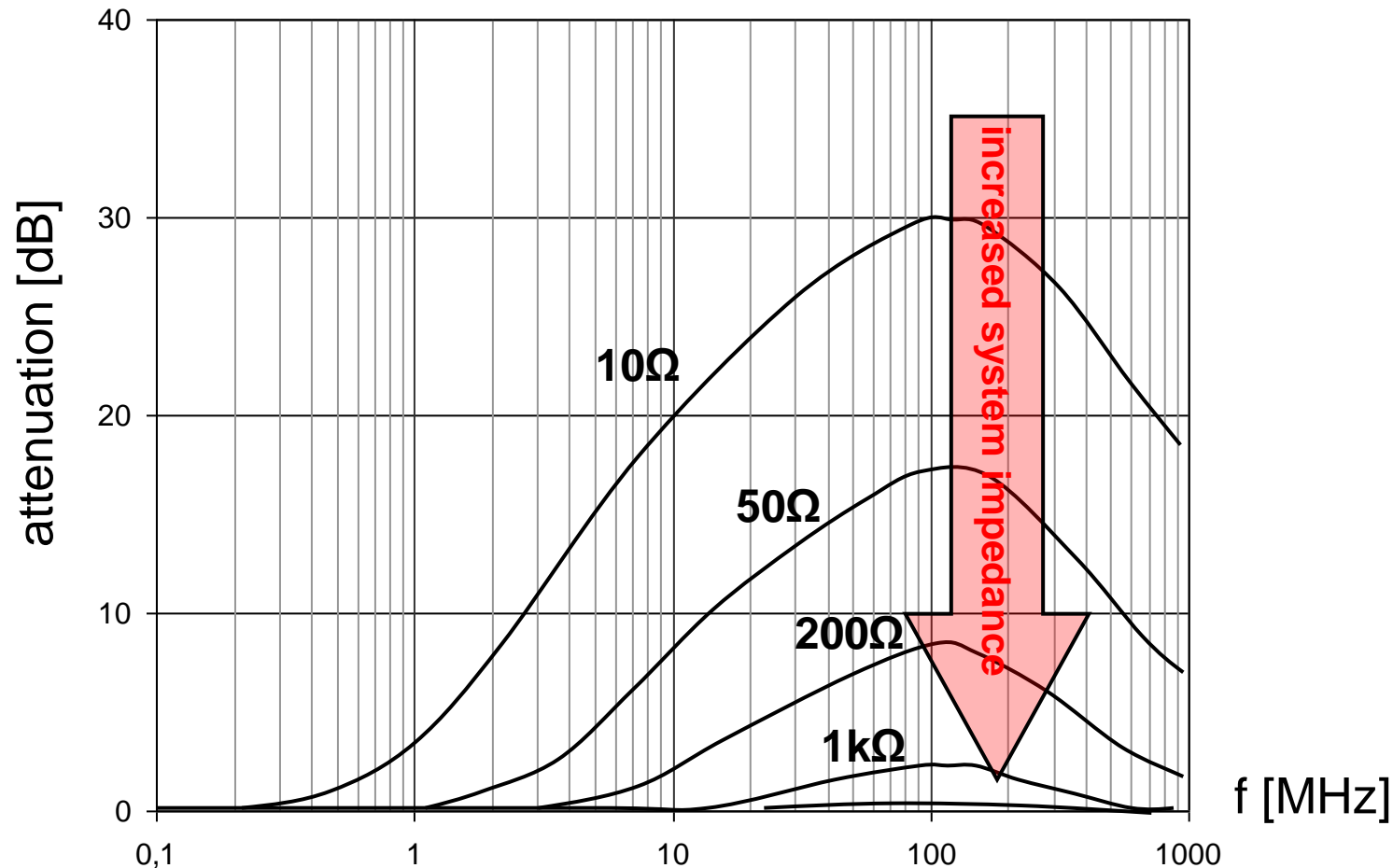
- Possibility: Attenuation too low



→ could be because of wrong system impedance estimation
→ increase the impedance of ferrite ($Z_F \sim 1000 \Omega$)

Insertion loss - Example

- Dependency of system impedance (Source/Load) vs. attenuation
- high system impedances results in a low attenuation



→ Filtering just to a certain system impedance possible

Filter Topologies



Filter Topologies - Recommended filter topologies

Source Impedance

Load Impedance

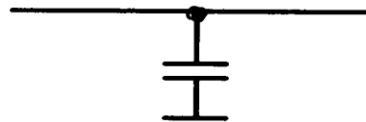
low



high

LC circuit (Induct/Cap)

high



high

Capacitor Filter

high or
unknown



high or
unknown

Pi Filter (low pass filter)

low



low

Inductor Filter

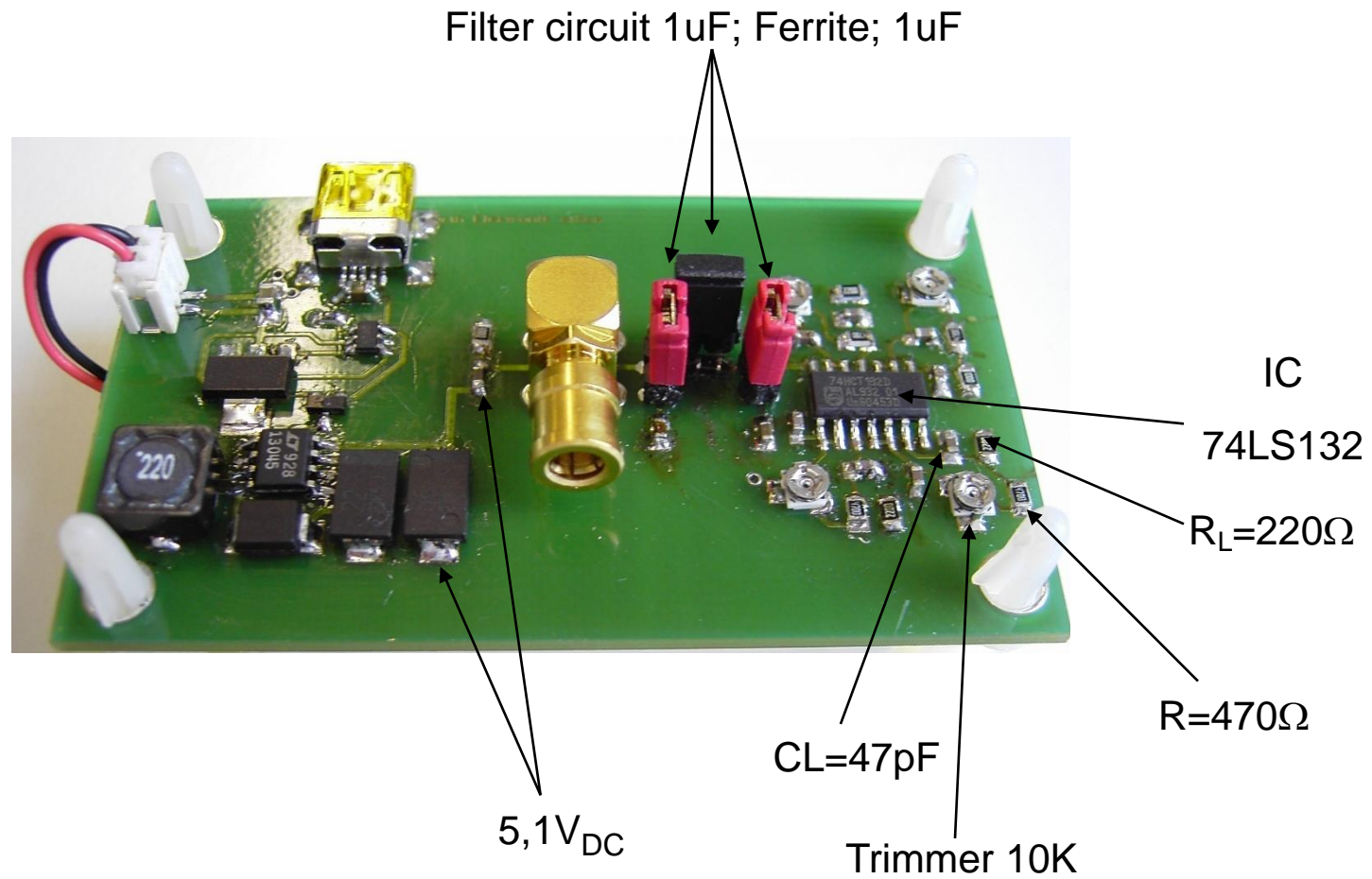
low or
unknown



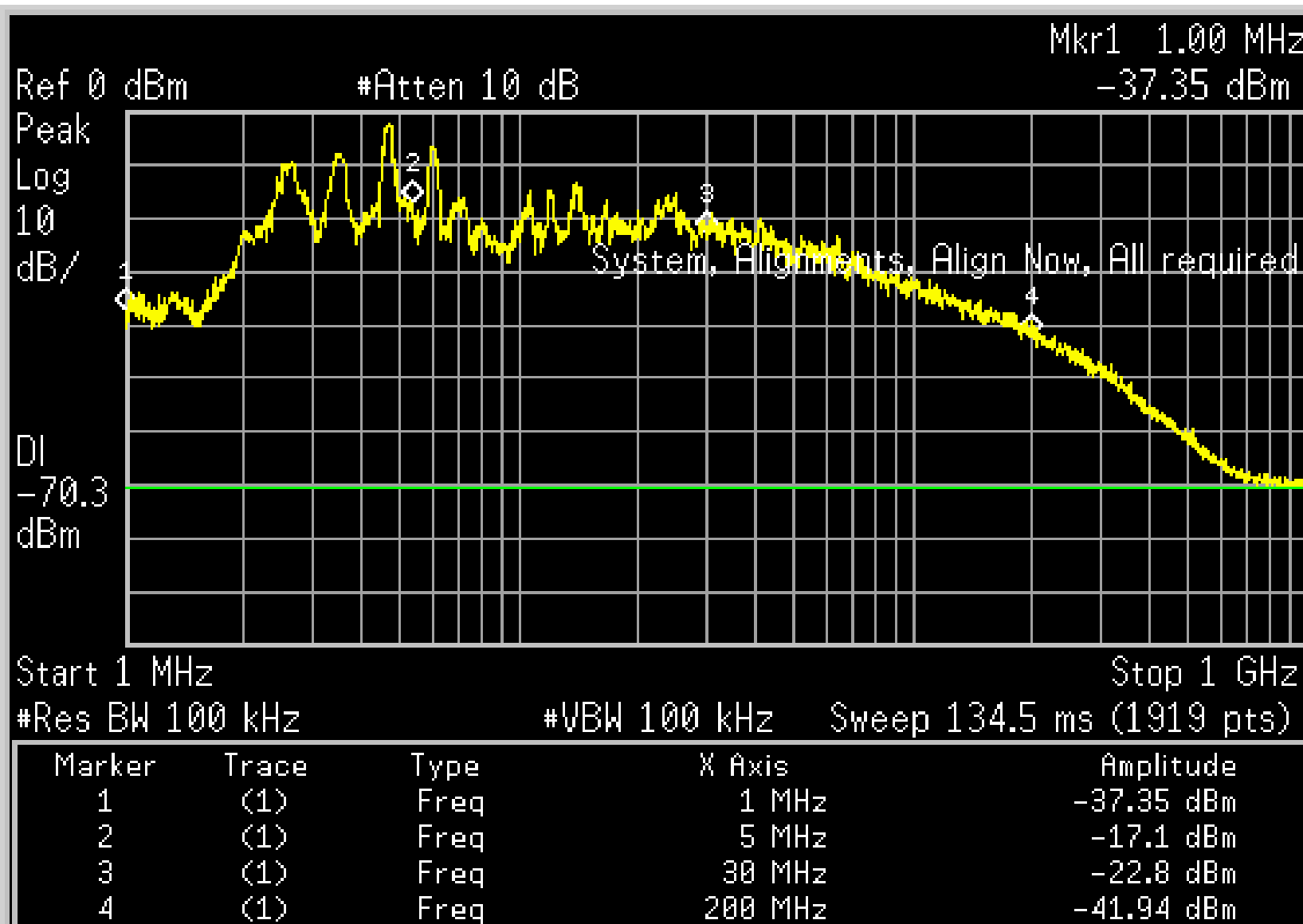
low or
unknown

Tee Filter (low pass filter)

Filter Topologies – Test Board

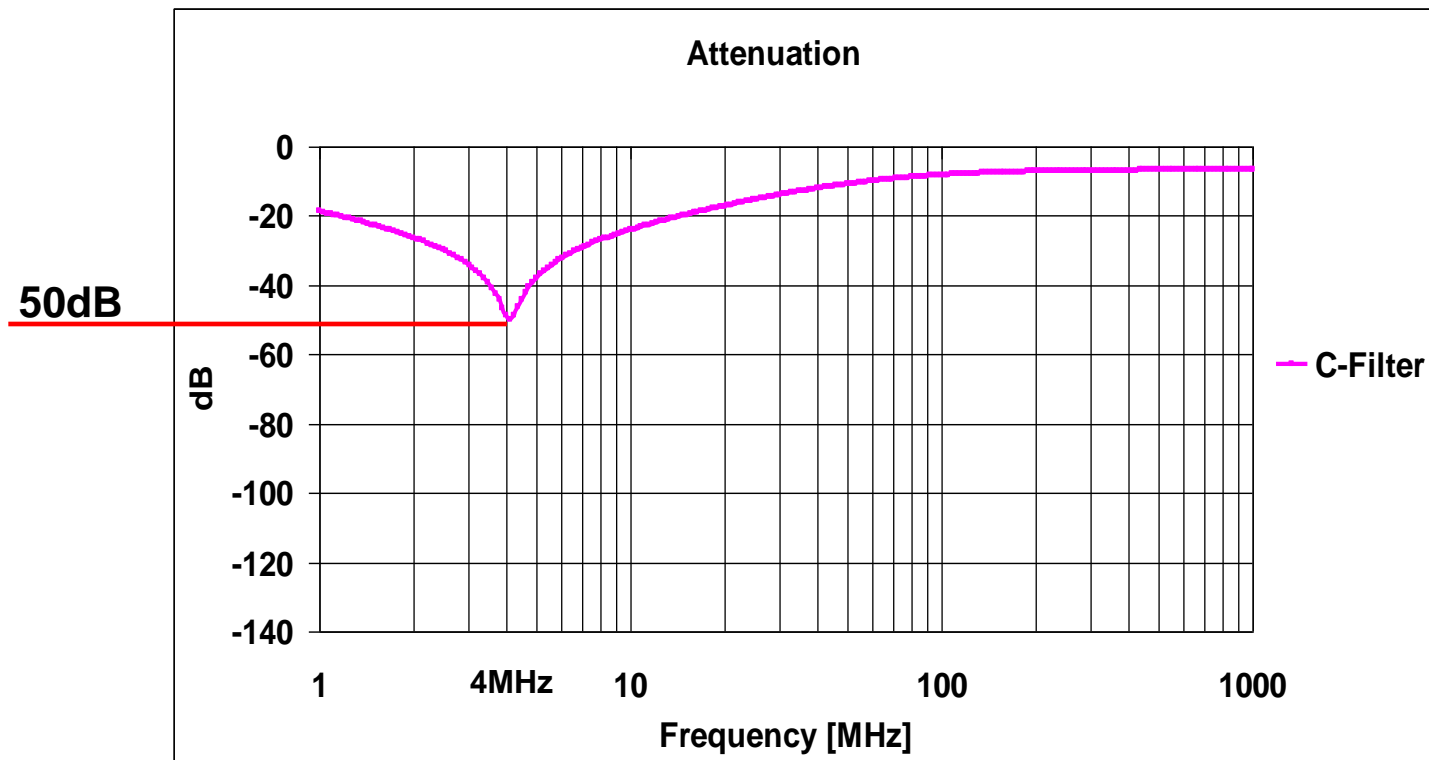
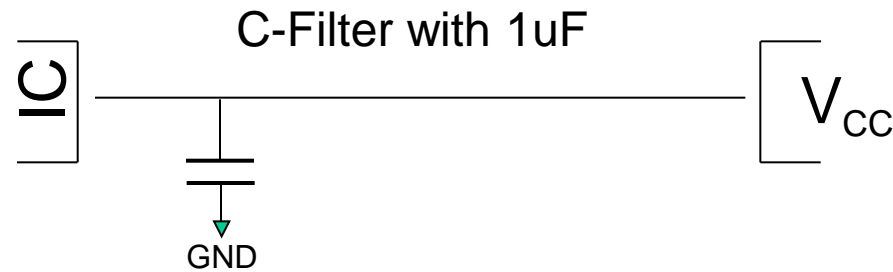


Filter Topologies – Test Board (Vcc Decoupling)



Filter Topologies – Test Board (Vcc Decoupling) [C Filter]

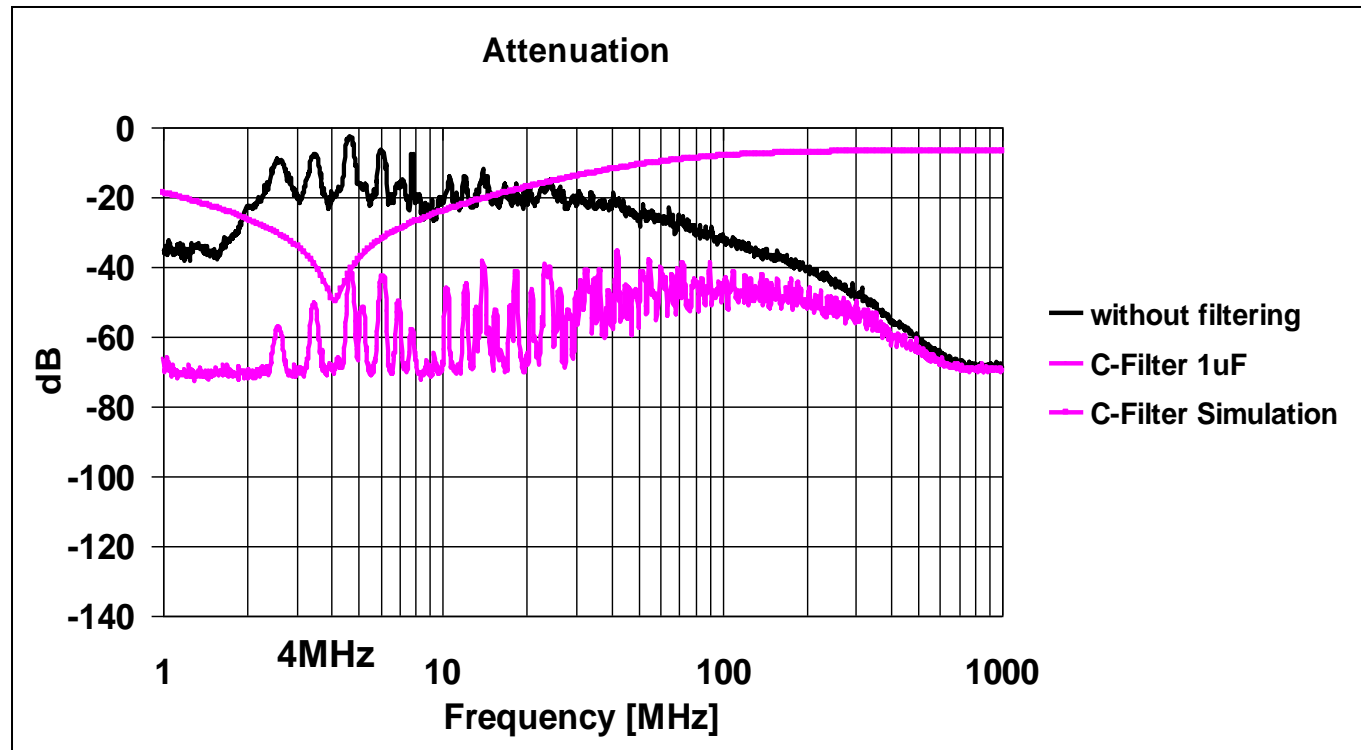
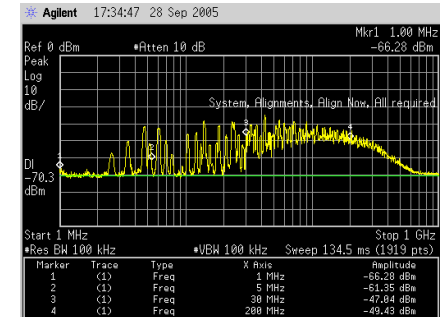
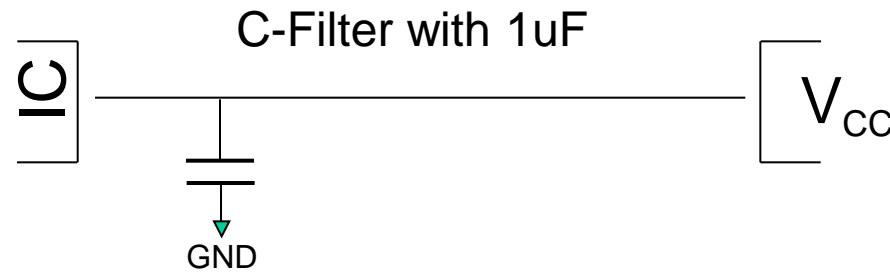
Step 1:
1uF Cap.



Filter Topologies – Test Board (Vcc Decoupling) [C Filter Results]

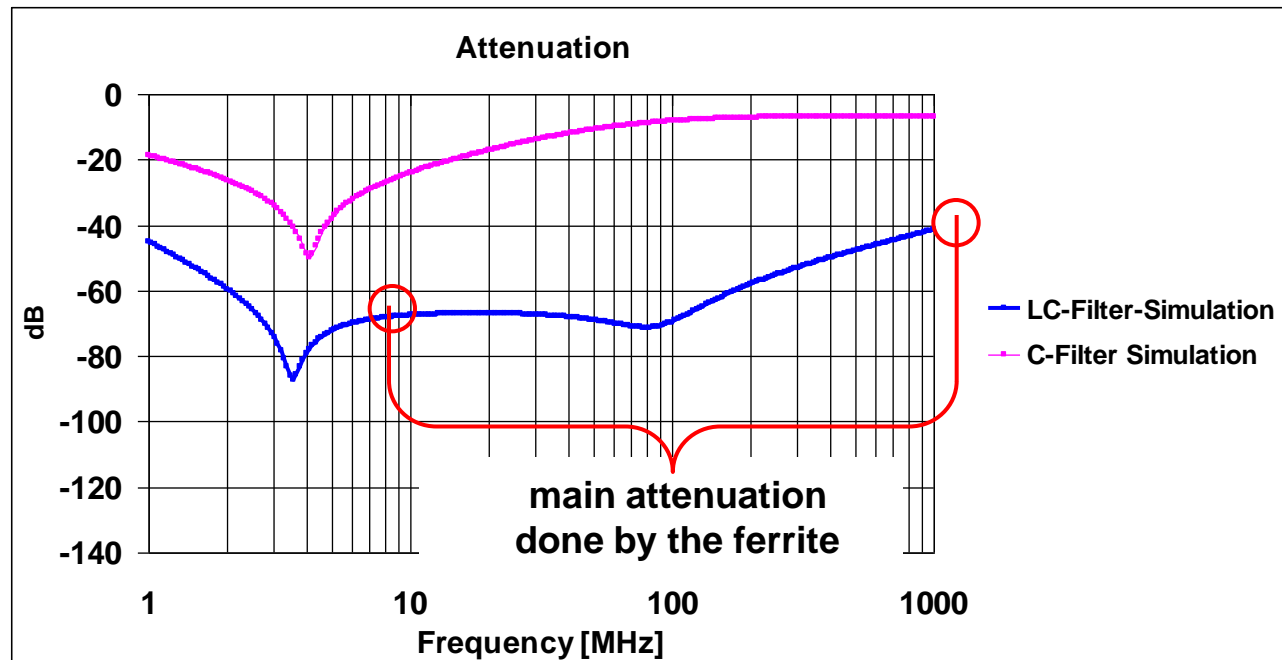
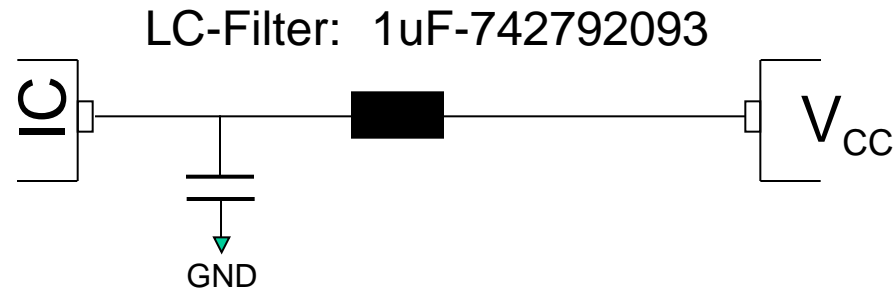


Step 1:
1uF Cap.



Filter Topologies – Test Board (Vcc Decoupling) [LC Filter]

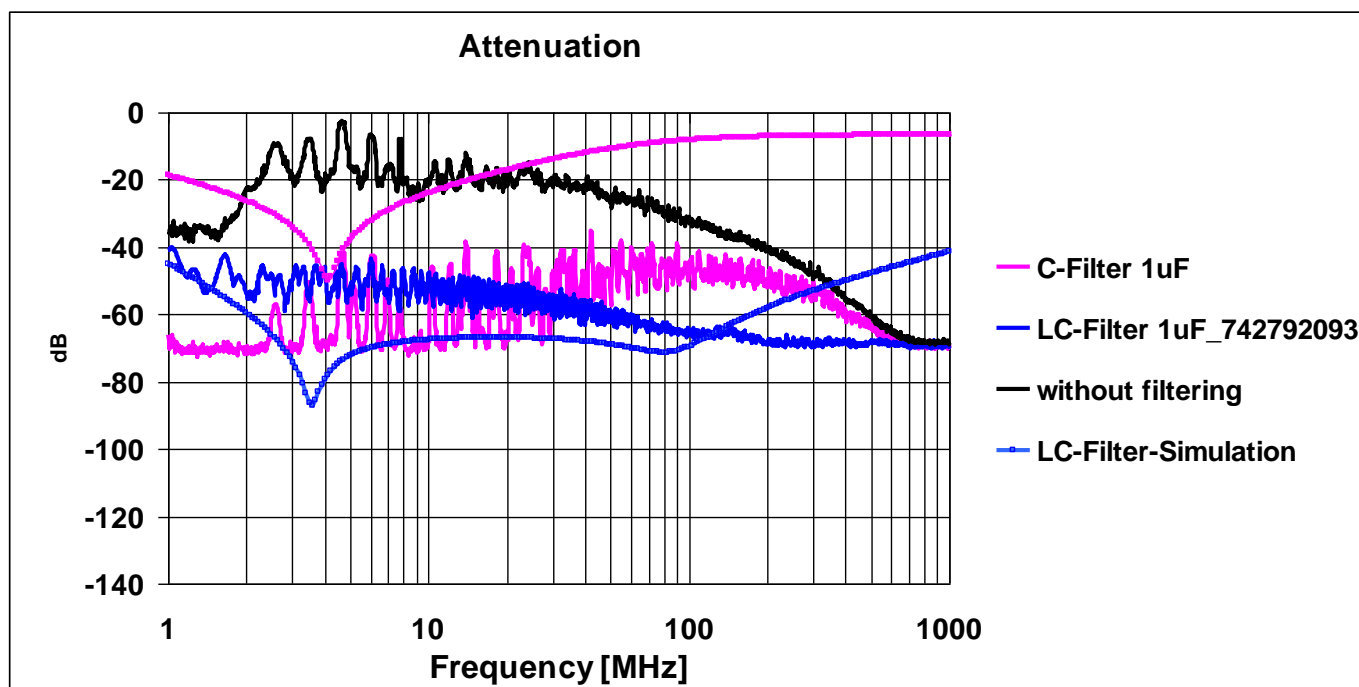
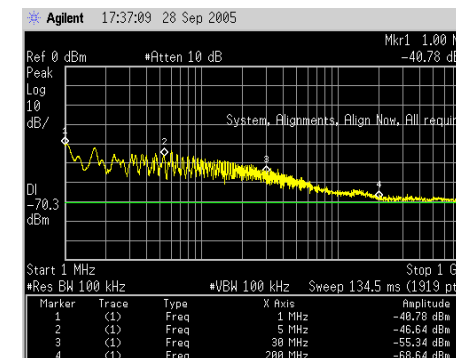
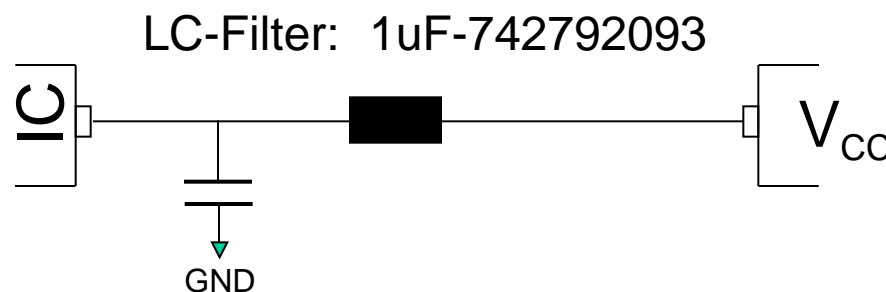
Step 2:
1uF Cap.
& Ferrite



Filter Topologies – Test Board (Vcc Decoupling) [LC Filter Result]



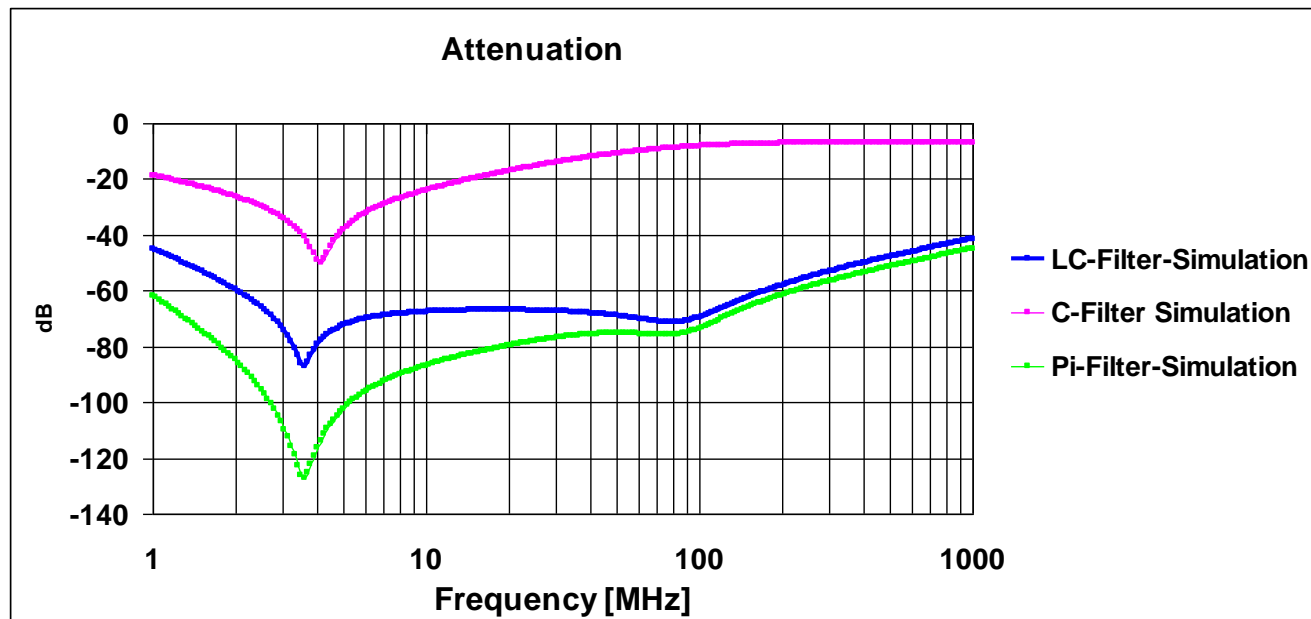
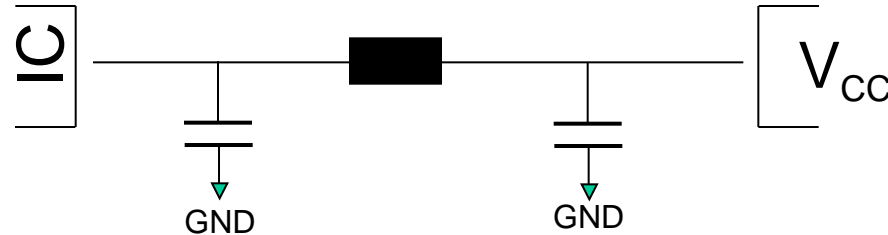
Step 2:
1uF Cap.
& Ferrite



Filter Topologies – Test Board (Vcc Decoupling) [PI Filter]

Step 3:
1uF//1uF Cap.
& Ferrite

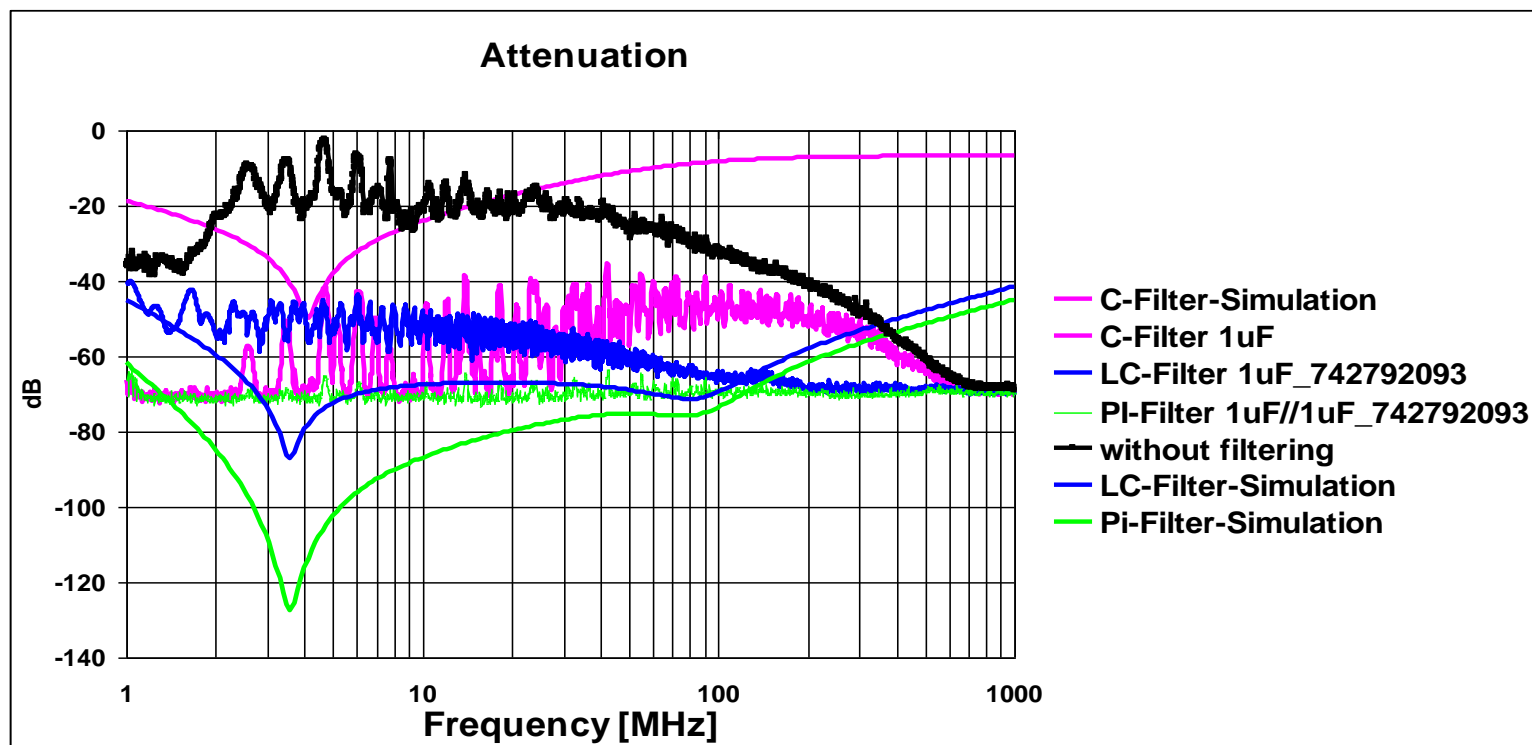
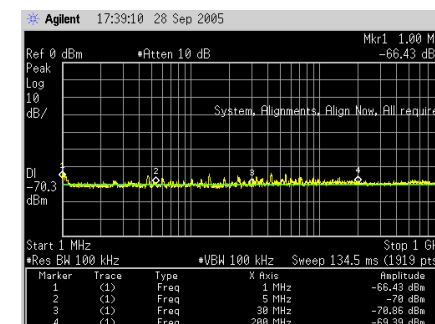
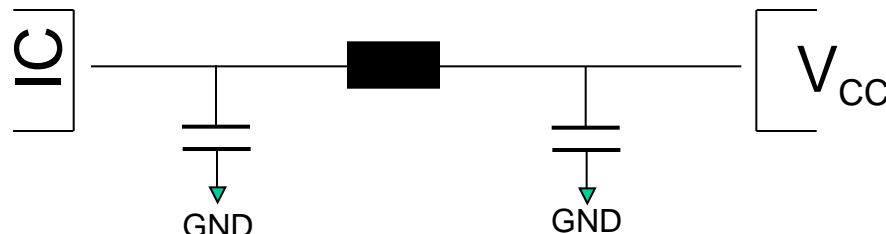
π -Filter: 1uF-742792093-1uF



Filter Topologies – Test Board (Vcc Decoupling) [PI Filter]

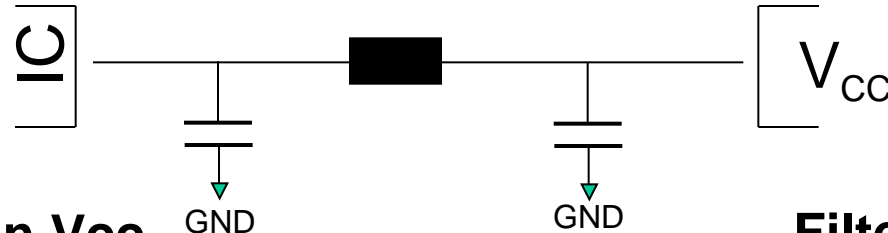
Step 3:
1uF//1uF Cap.
& Ferrite

π -Filter: 1uF-742792093-1uF



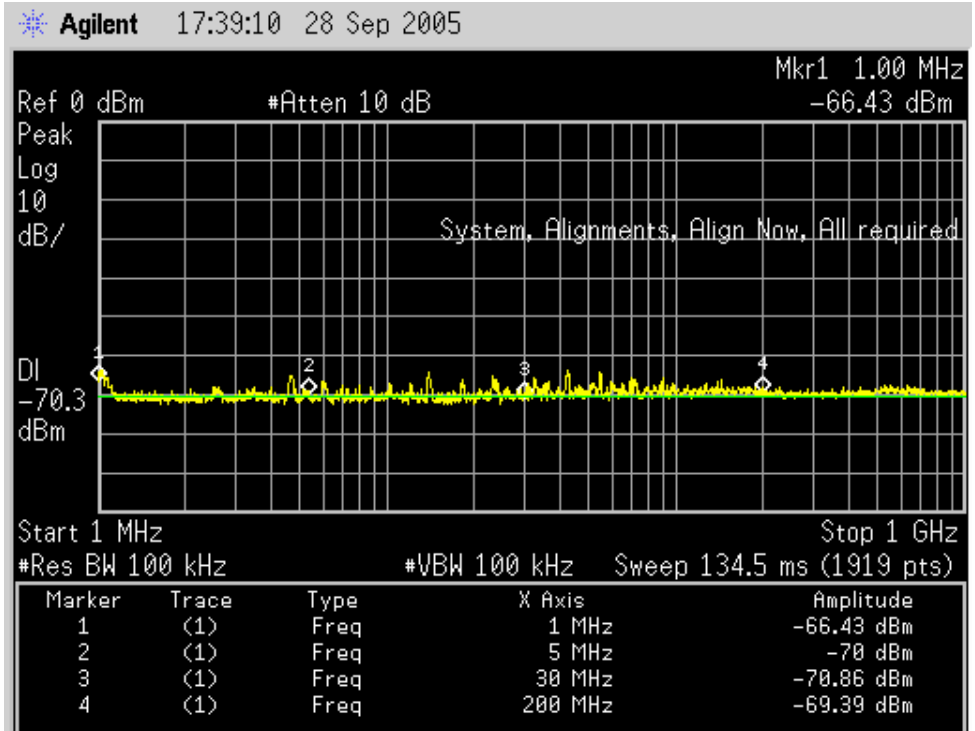
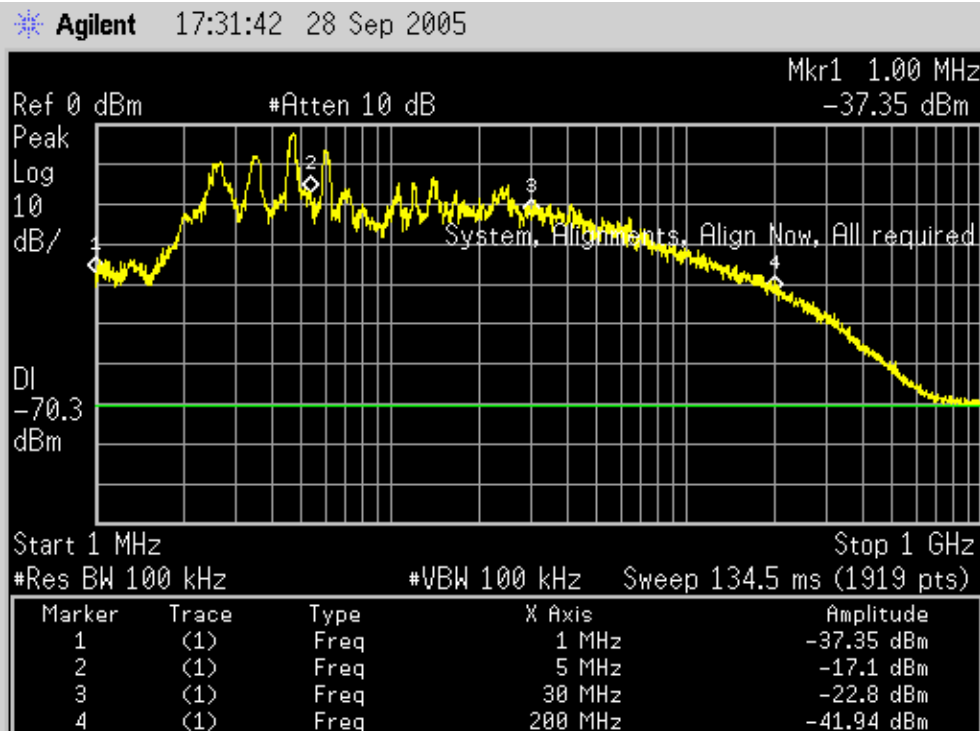
Filter Topologies – Test Board (Vcc Decoupling) [PI Filter]

π -Filter: 1uF-742792093-1uF



No filtering on Vcc

Filtering on Vcc

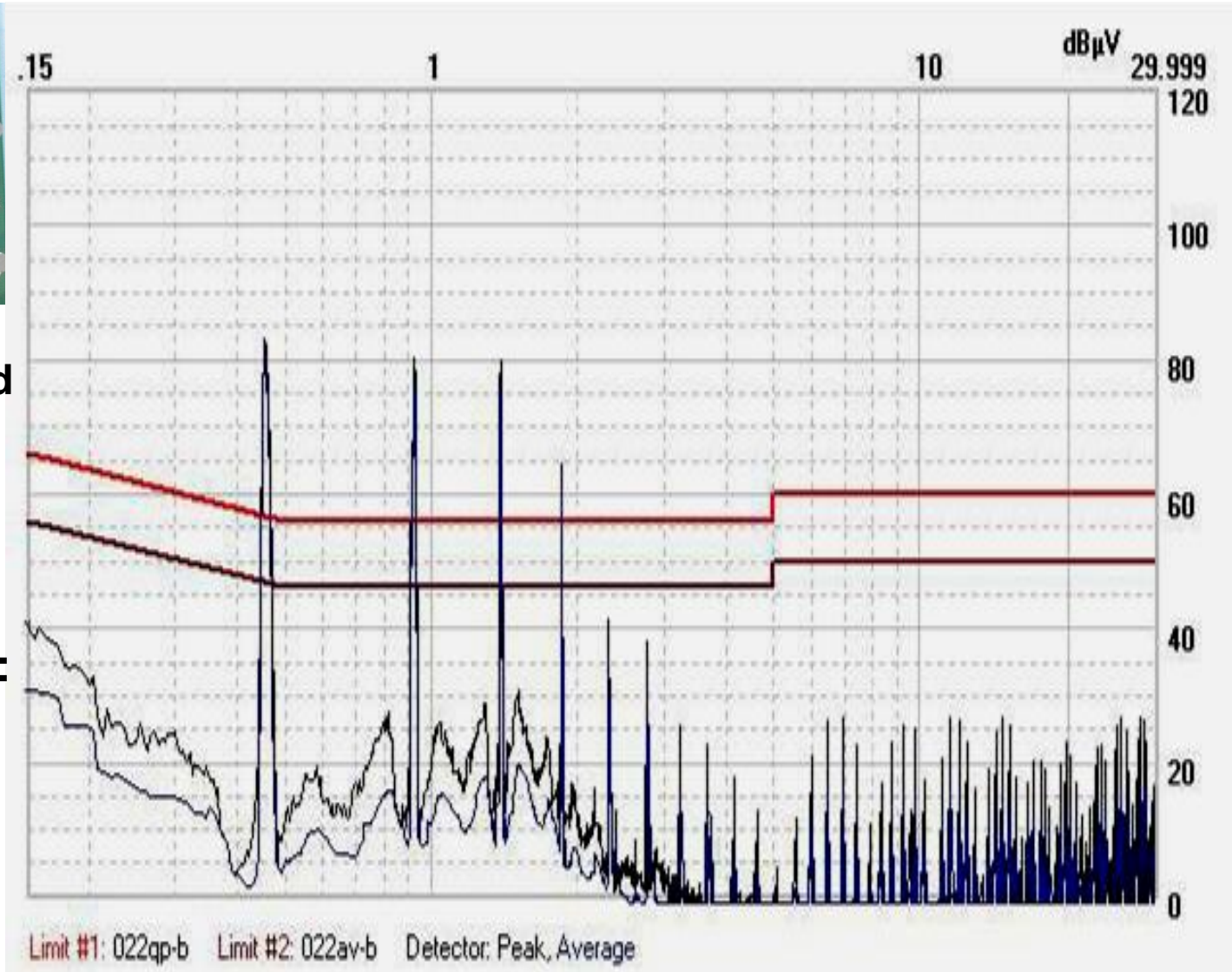


Simulation – Conducted Emissions without filter (Example 1)



LT3481EMSE Demo Board
24V to 3.3V @2A
fsw=800kHz
CEM 0.15 – 30 MHz

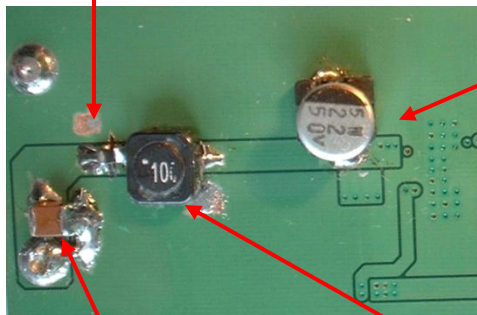
Test without EMC filter:
Peak 82dB μ V
→ 26dB above limit



Simulation – Conducted Emissions with filter (Example 1)

Ferrite bead

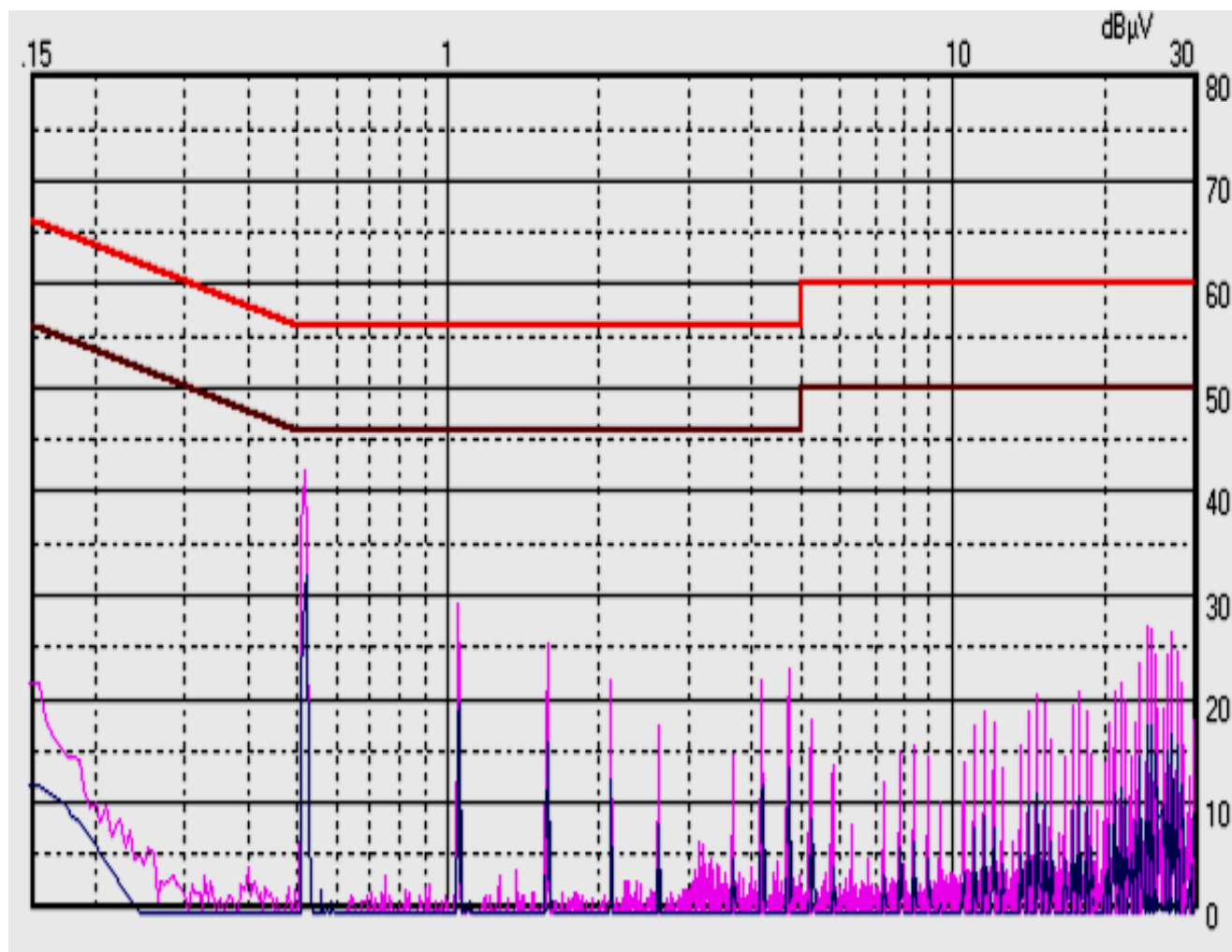
High ESR Elco
to damp cable



Test with additional $L=10\mu\text{H}$,
 $C=3.3\mu\text{F}$ 50V 1210 input filter

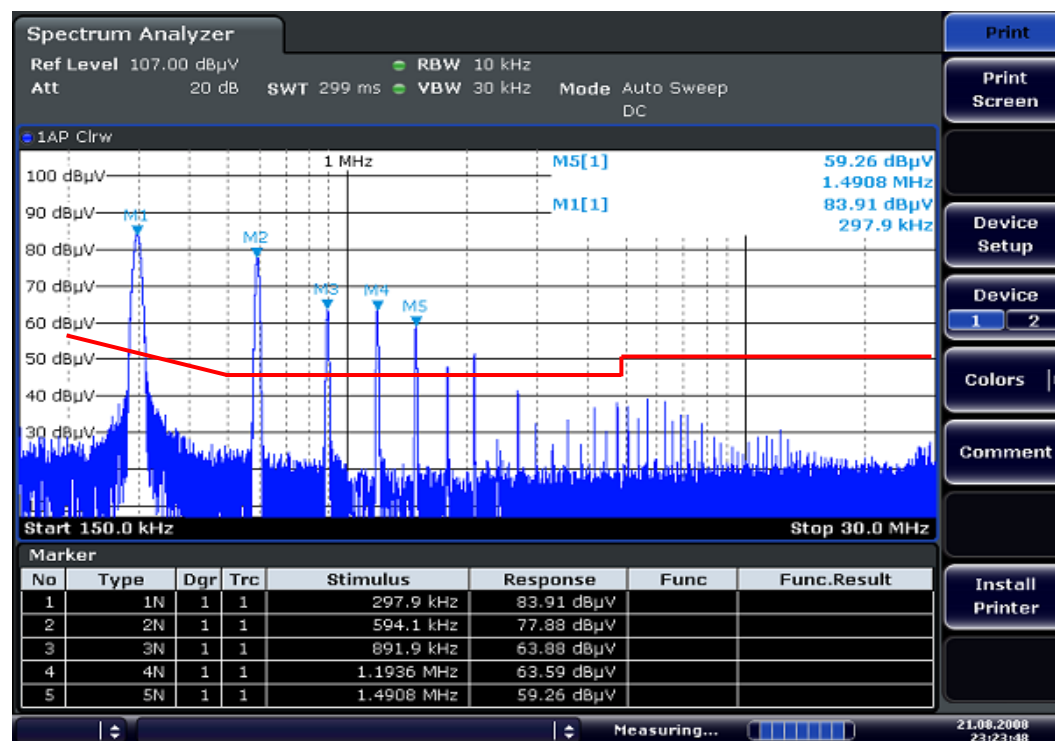
Peak=42dB $\mu\text{V}/\text{m}$
 $\emptyset=32\text{dB}\mu\text{V}/\text{m}$

Peak & \emptyset 14dB below limit

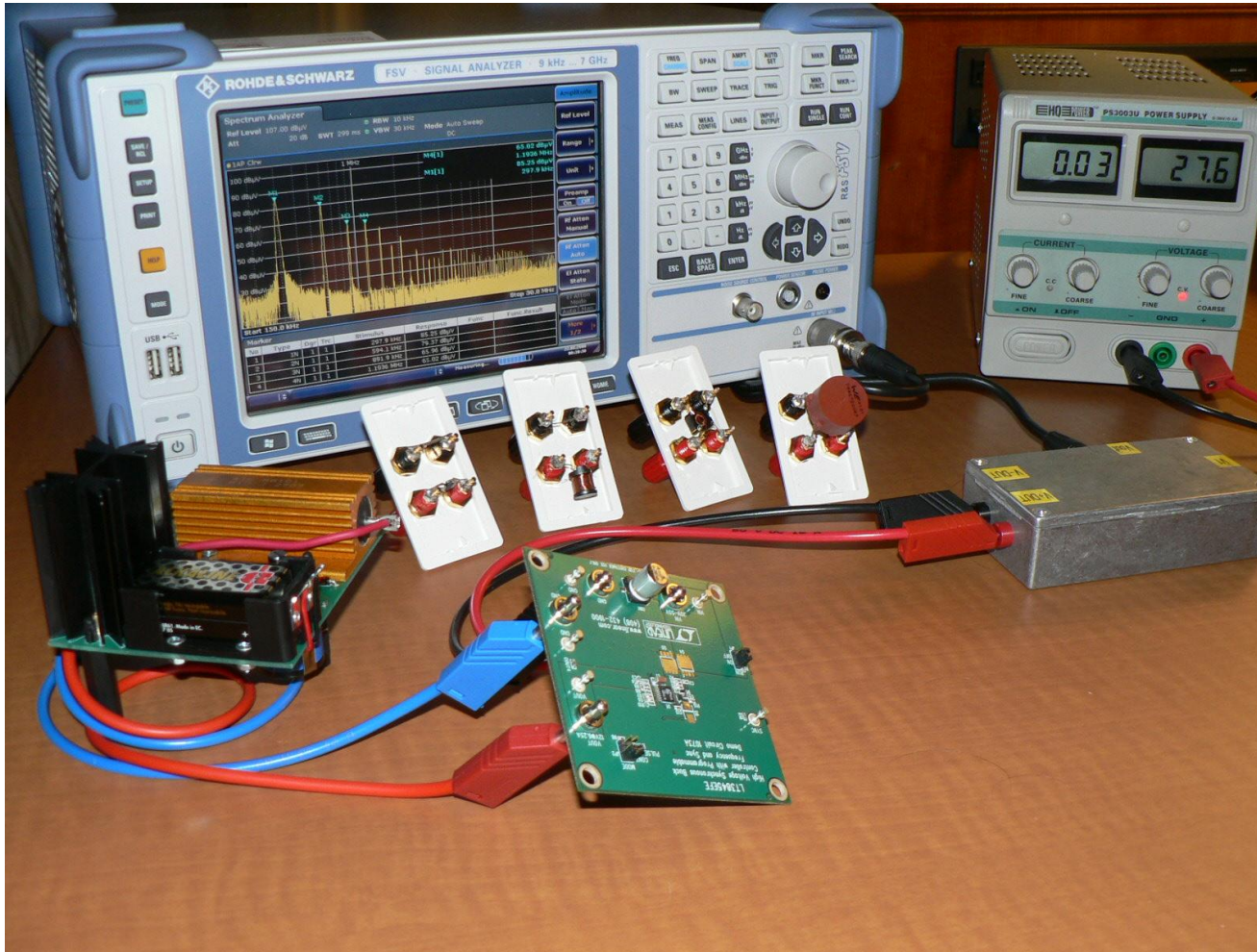


Simulation – Conducted Emissions (Example 2)

- Chip Bead
- Differential Choke
- Bifilar wound CMC
- Sectional wound CMC



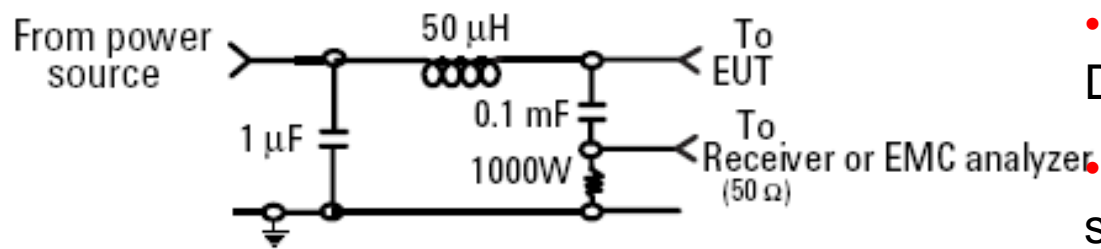
Simulation – Conducted Emissions Test Setup (Example 2)



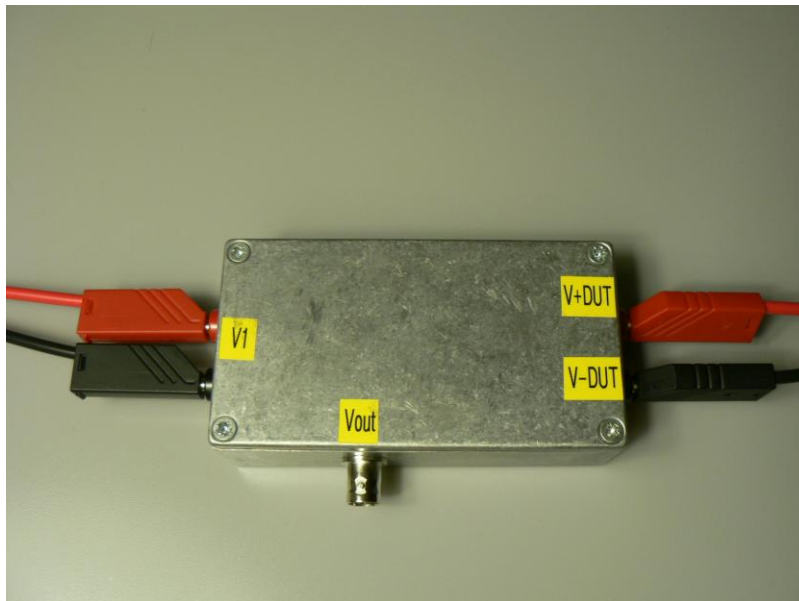
- No load
- 1.5A load at 300KHz fsw

Simulation – Conducted Emissions Test Setup (Example 2)

Line Impedance Stabilization Network (LISN)

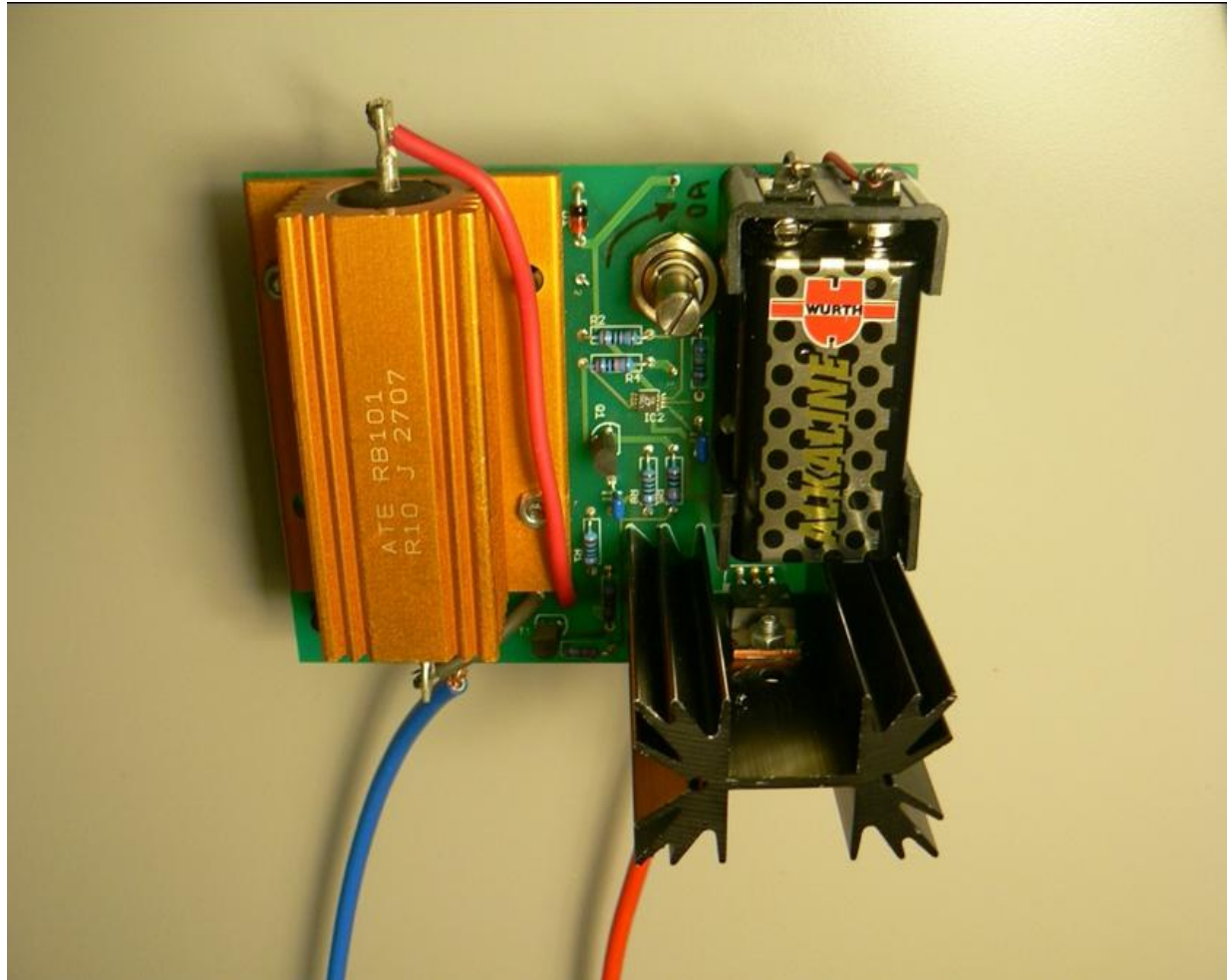


- Isolates DUT (device under test) from Power Source (typically mains) Noise
- Provide characteristic impedance to DUT (50 Ohms in this case)
- Path for conducted noise from DUT to spectrum analyzer



The $1\ \mu\text{F}$ in combination with the $50\ \mu\text{H}$ inductor is the filter that isolates the mains from the EUT. The $50\ \mu\text{H}$ inductor isolates the noise generated by the EUT from the mains. The $0.1\ \mu\text{F}$ couples the noise generated by the EUT to the EMC analyzer or receiver. At frequencies above 150 kHz, the EUT signals are presented with a 50- Ω impedance.

Simulation – Conducted Emissions Test Setup (Example 2)



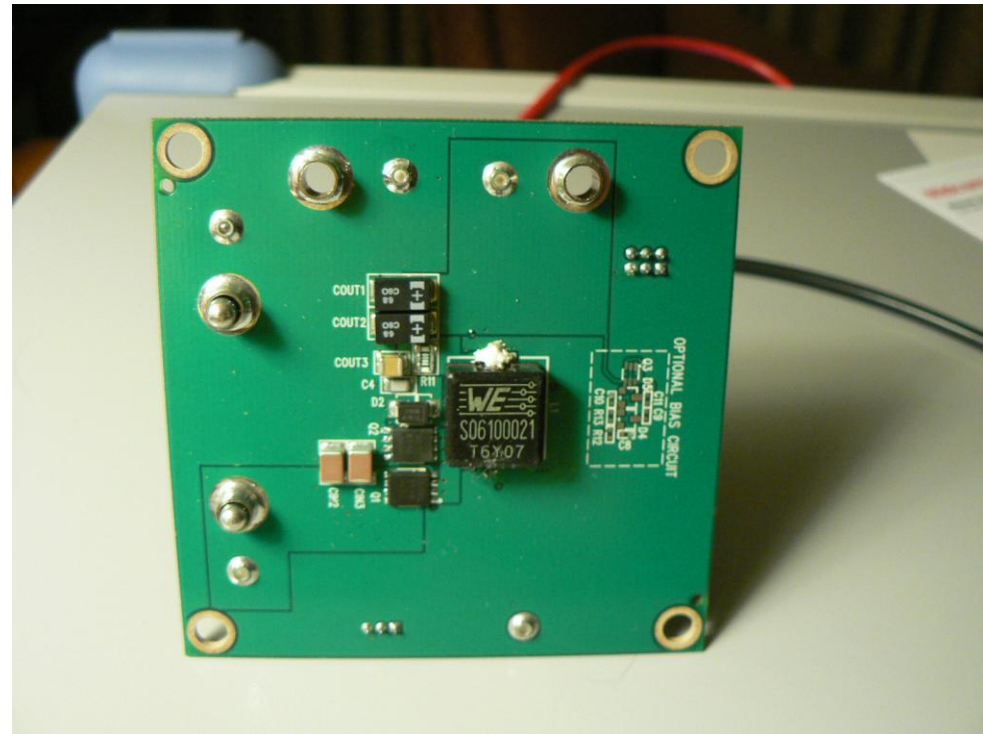
Simulation – Conducted Emissions Test Setup (Example 2)



- DC/DC Converter
- Input Voltage 20V-25V
- Output Voltage 12V/6.25A
- F_{sw} : 300KHz

Testcondition:

- no load
- max. load 1.5A



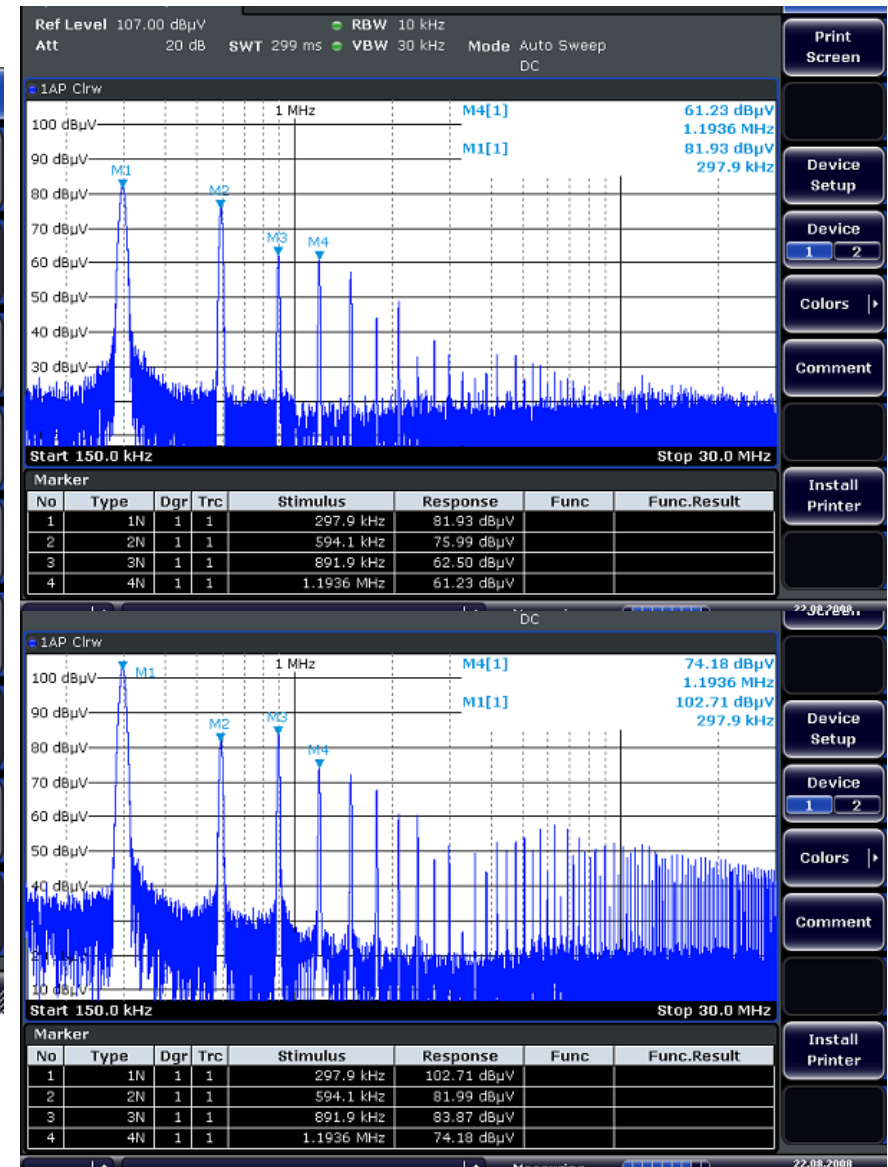
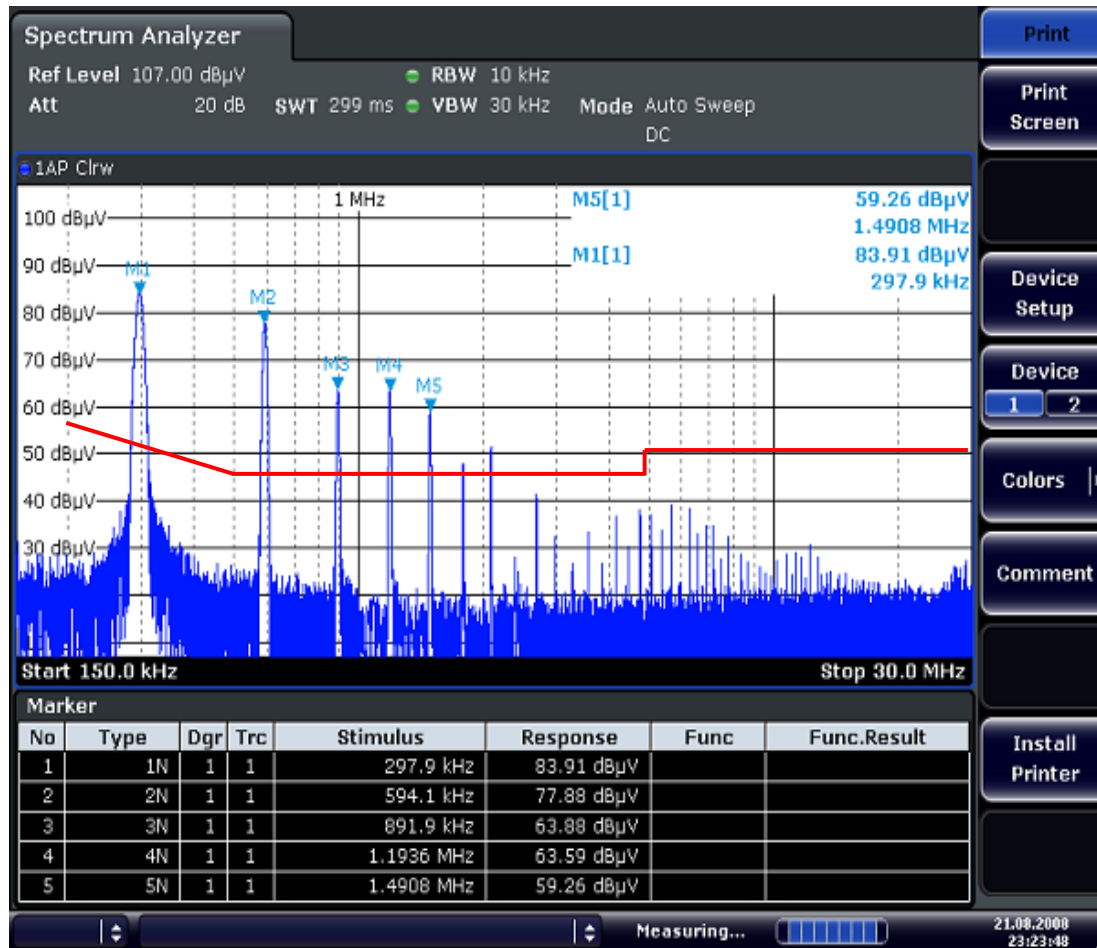
Simulation – Conducted Emissions Example 2 Chip Bead Ferrite



Chip Bead 530 Ω / 3A

Simulation – Conducted Emissions Example 2 Chip Bead Ferrite Result

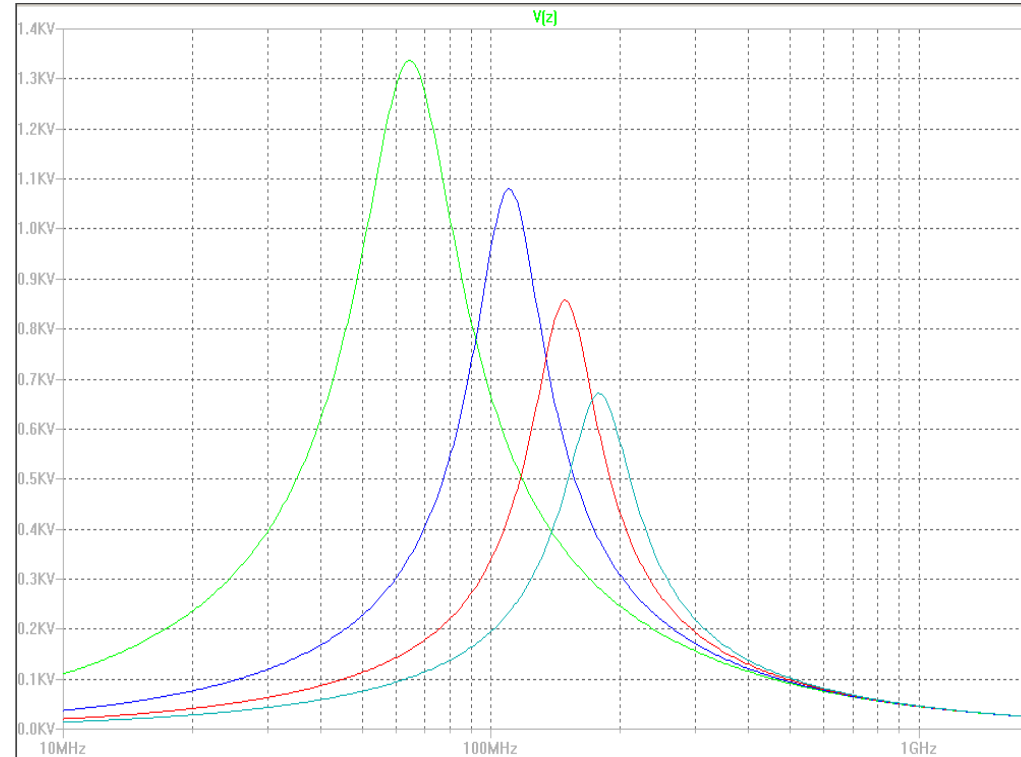
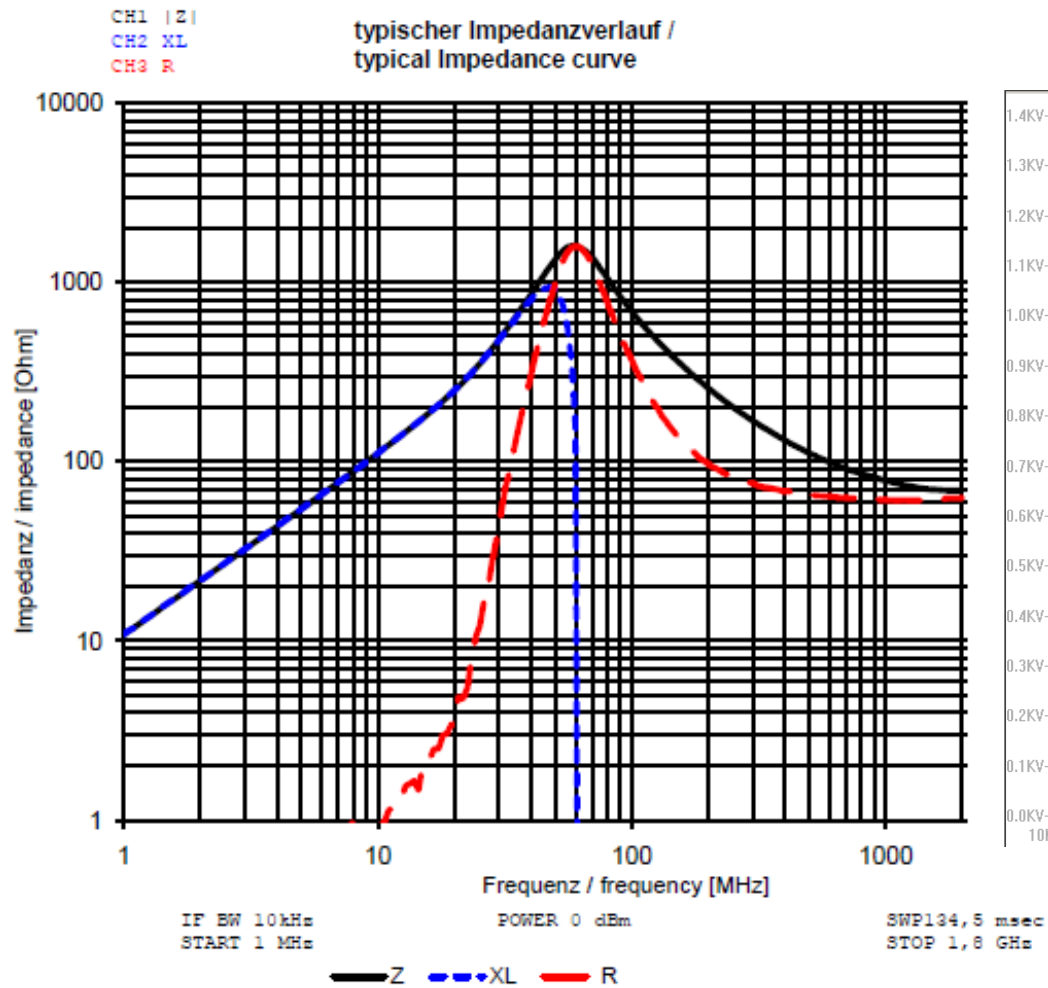
no load >



Chip Bead 670Ω at 3A

load 1.5A>

Simulation – Conducted Emissions Example 2 Chip Bead Ferrite Result



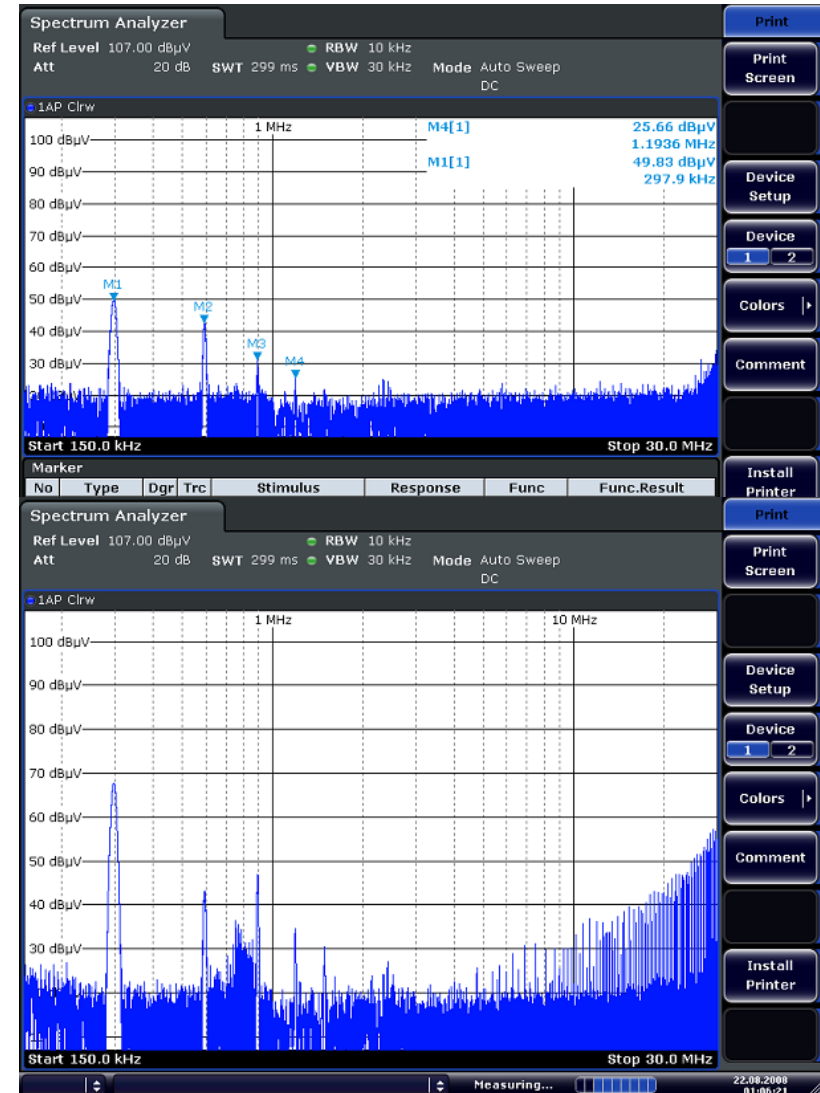
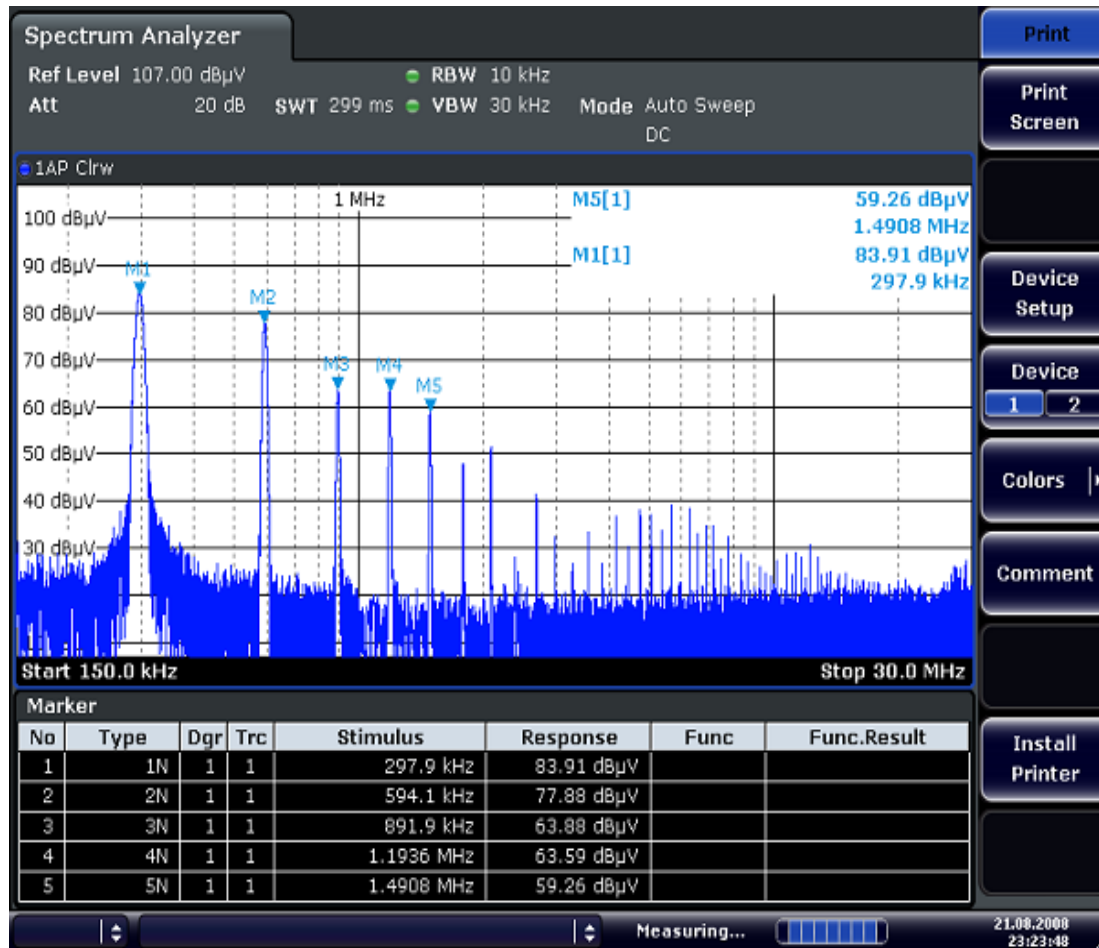
742 792 515

Simulation – Conducted Emissions Example 2 Differential Choke



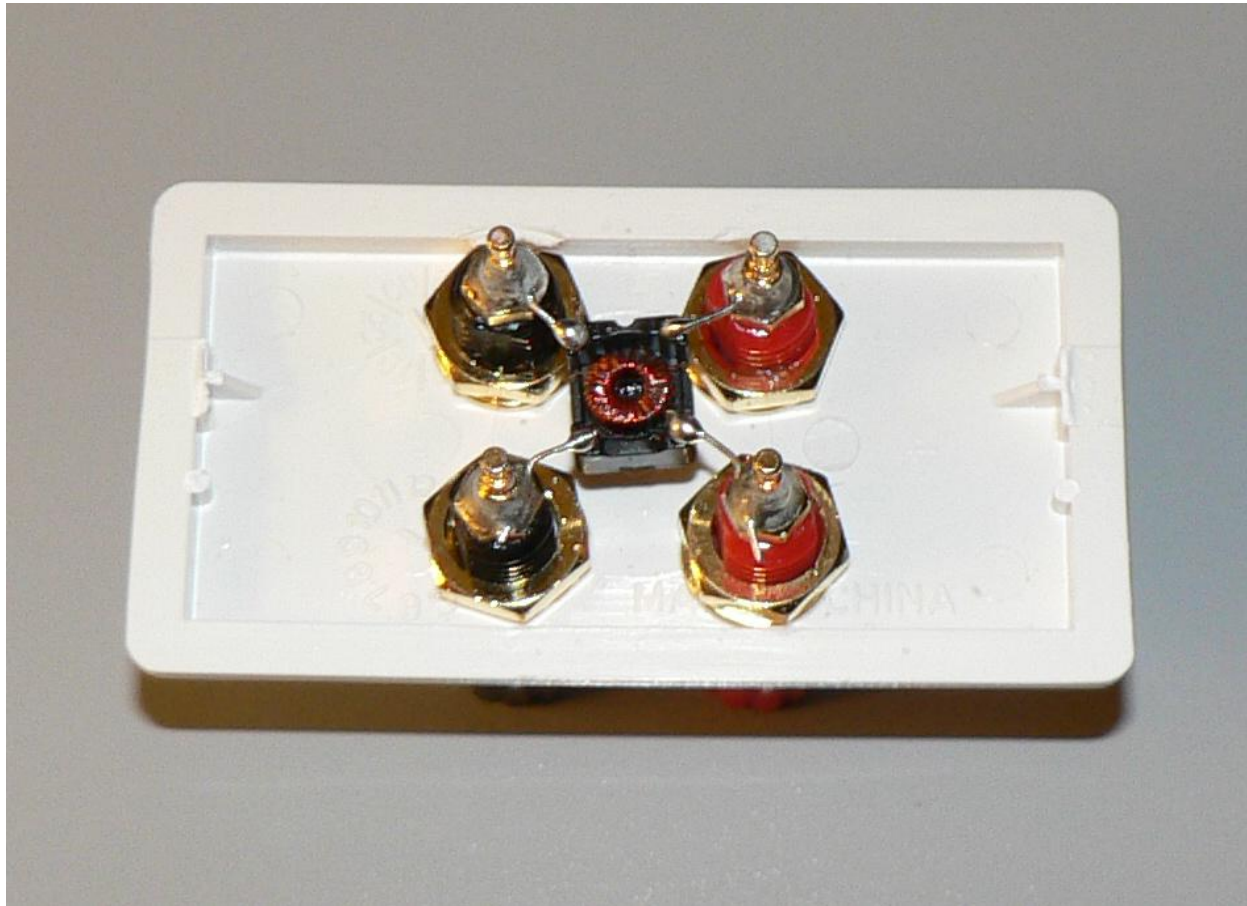
744 743 221 (220uH)

Simulation – Conducted Emissions Example 2 Differential Choke Result



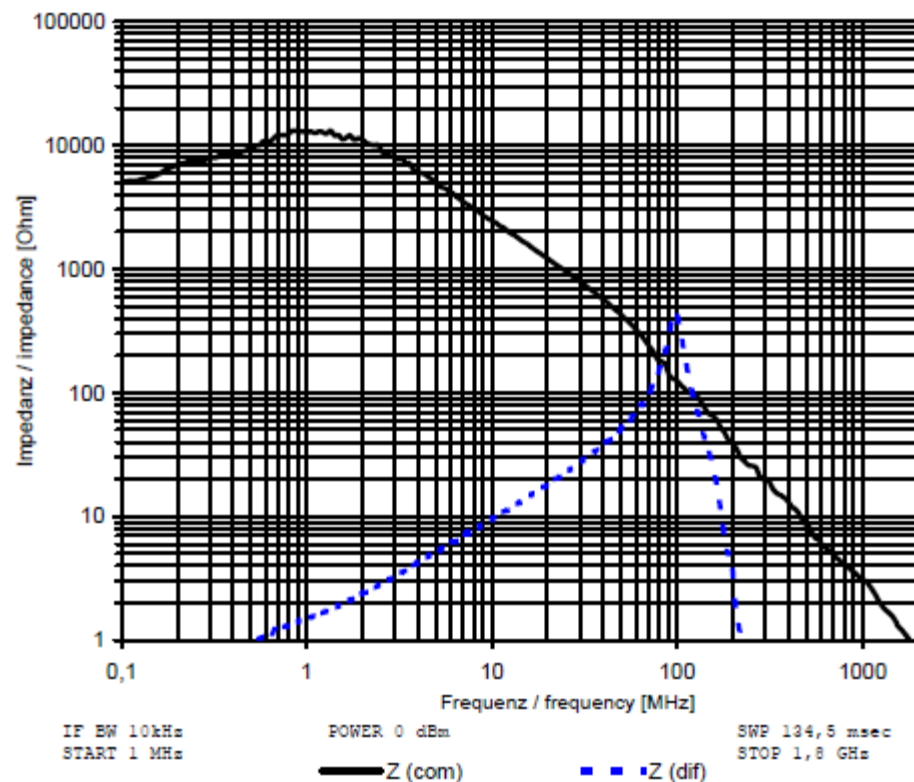
744 743 221 (220uH)

Simulation – Conducted Emissions Example 2 Bifilar CMC



4.7mH Bifilar winding Common Mode Choke

Simulation – Conducted Emissions Example 2 Bifilar CMC Result



**IDC 350mA
Max**



**Load is 1.5A
And...CMC**

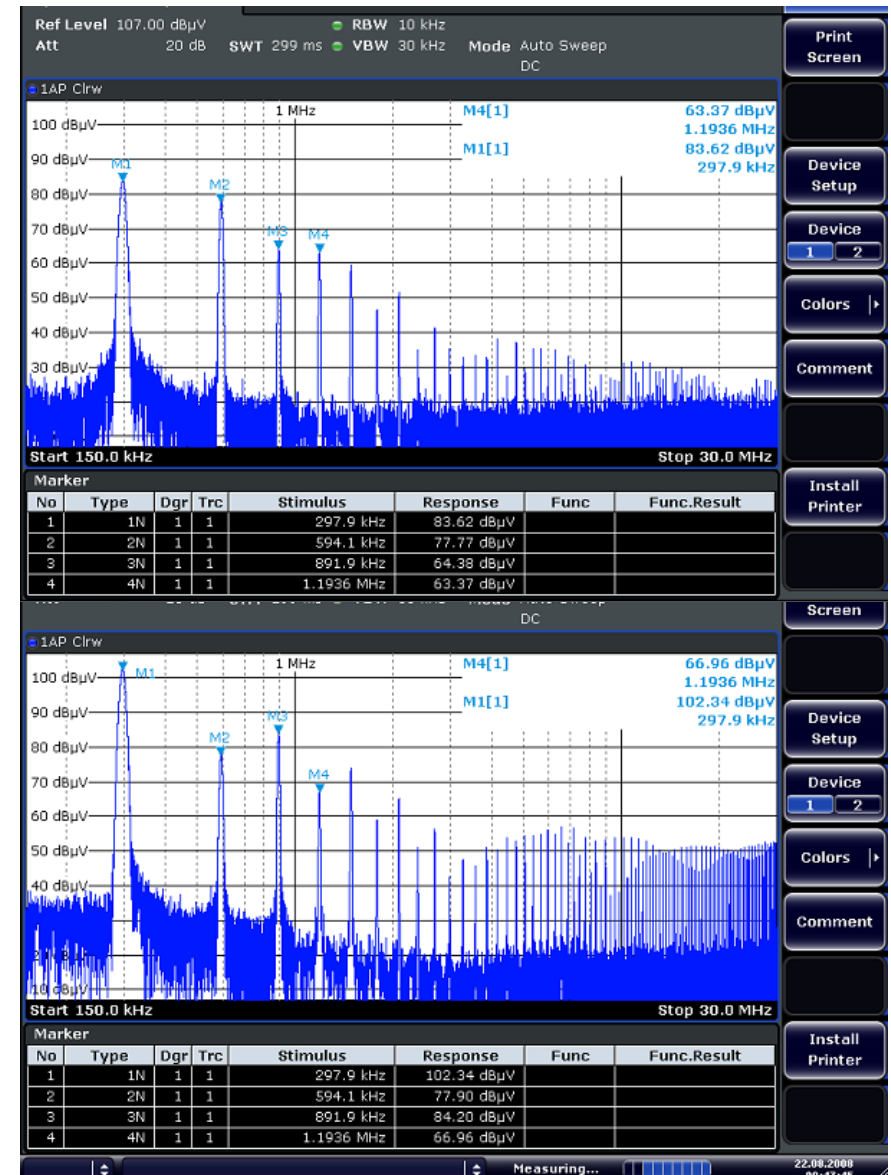
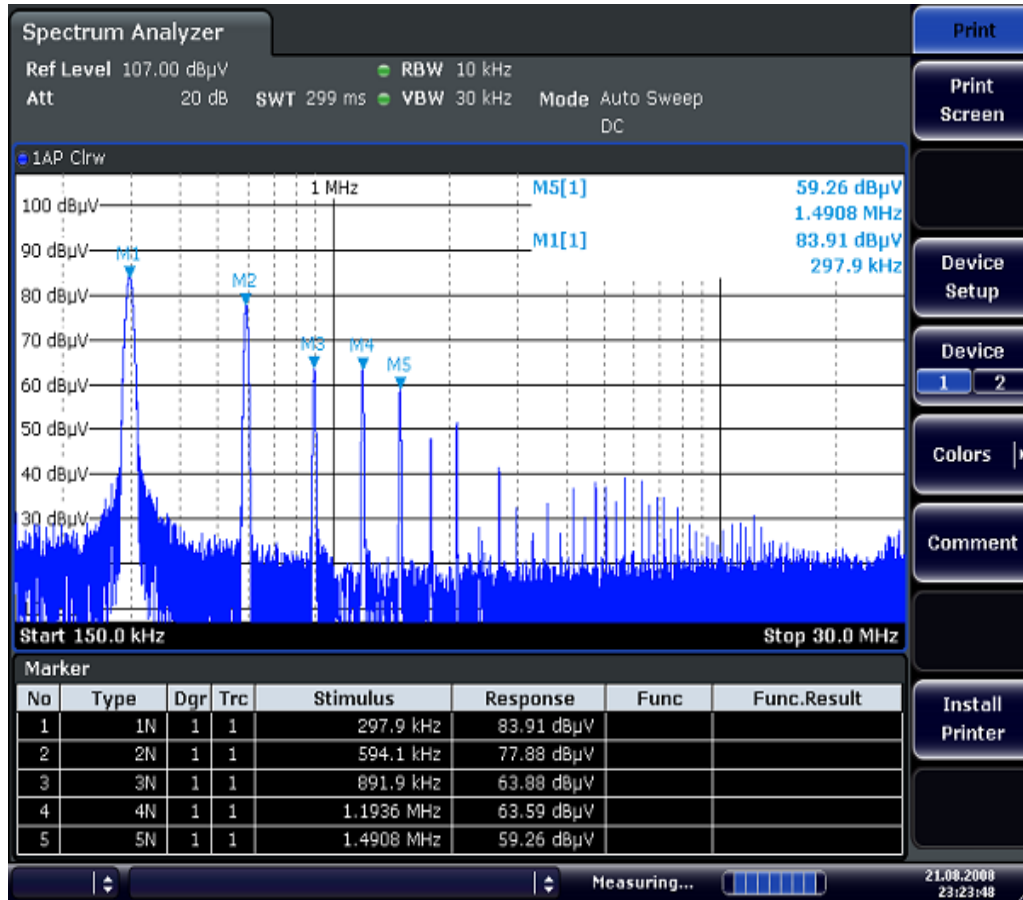
**Warning: Don't try this at home!
For Demonstration Purposes Only!**

4.7mH Bifilar winding Common Mode Choke

744 272 472

Simulation – Conducted Emissions Example 2 Bifilar CMC Result

no load >



load 1.5A>

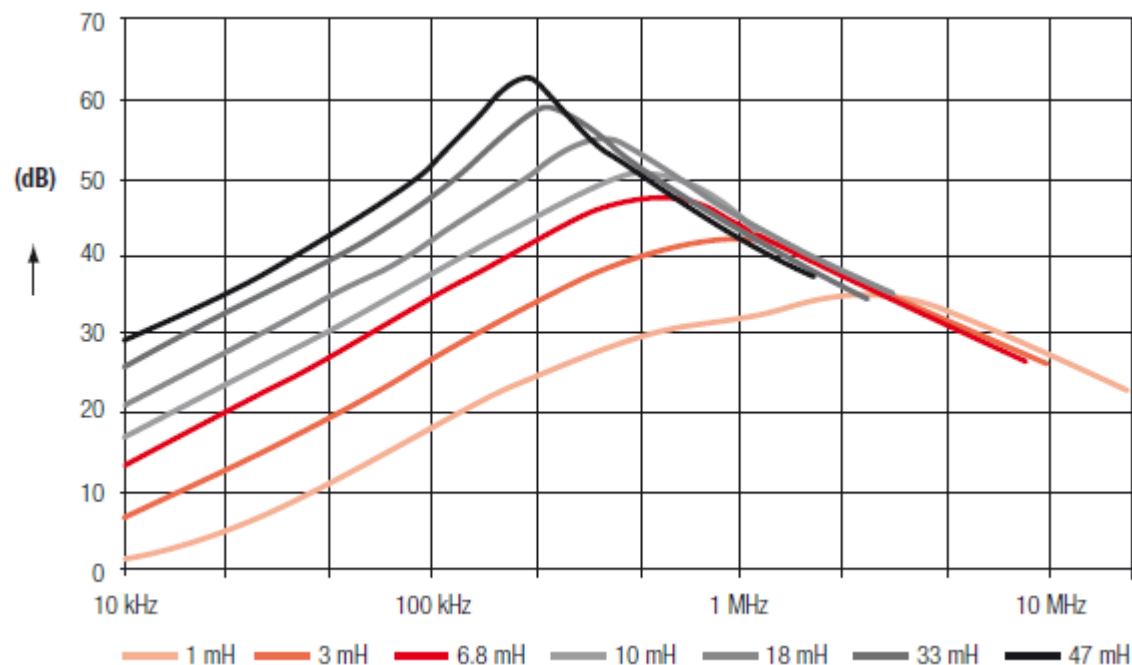
4.7mH Bifilar winding Common Mode Choke

Simulation – Conducted Emissions Example 2 CMC Sectional



Sectional common mode choke 47mH

Simulation – Conducted Emissions Example 3 CMC Sectional

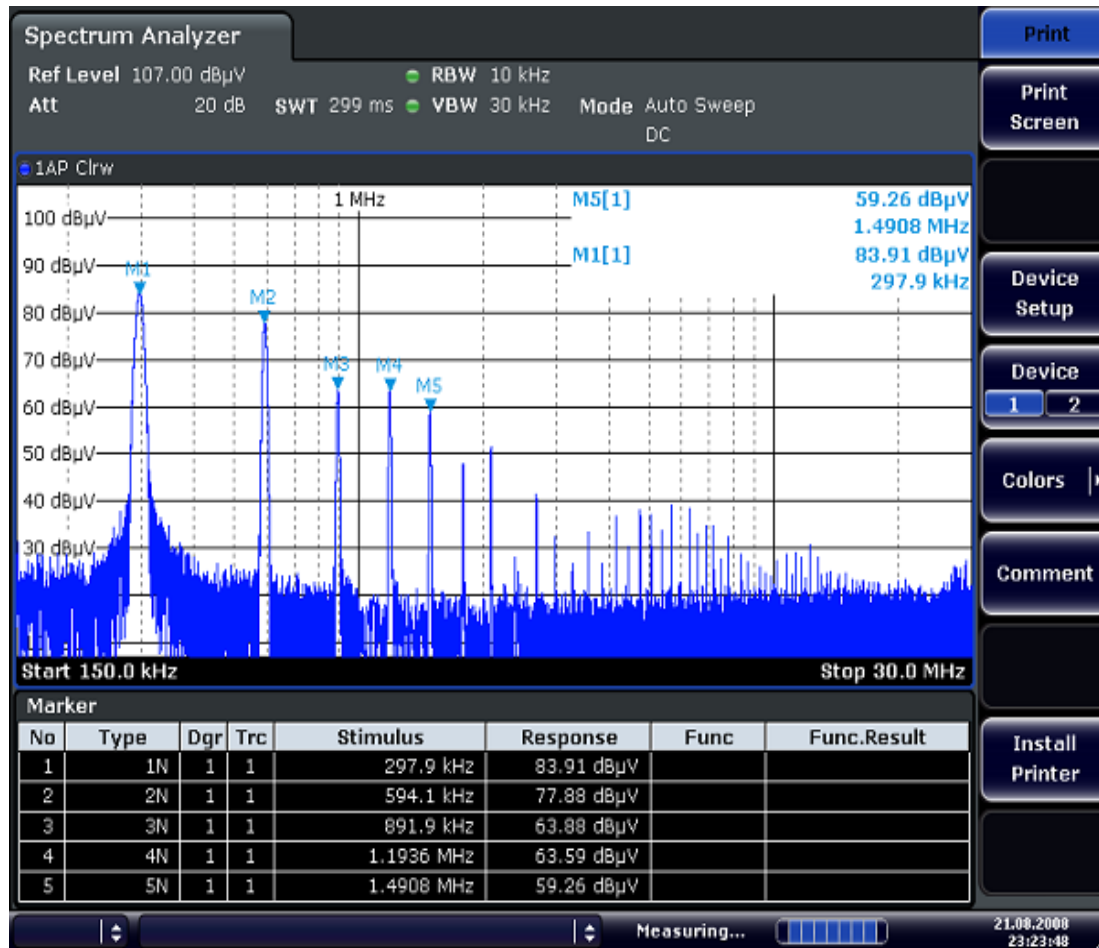


CMC 47mH Sectional Winding
Leakage Inductance $L_s \sim 250\mu\text{H}$ (5% of L)

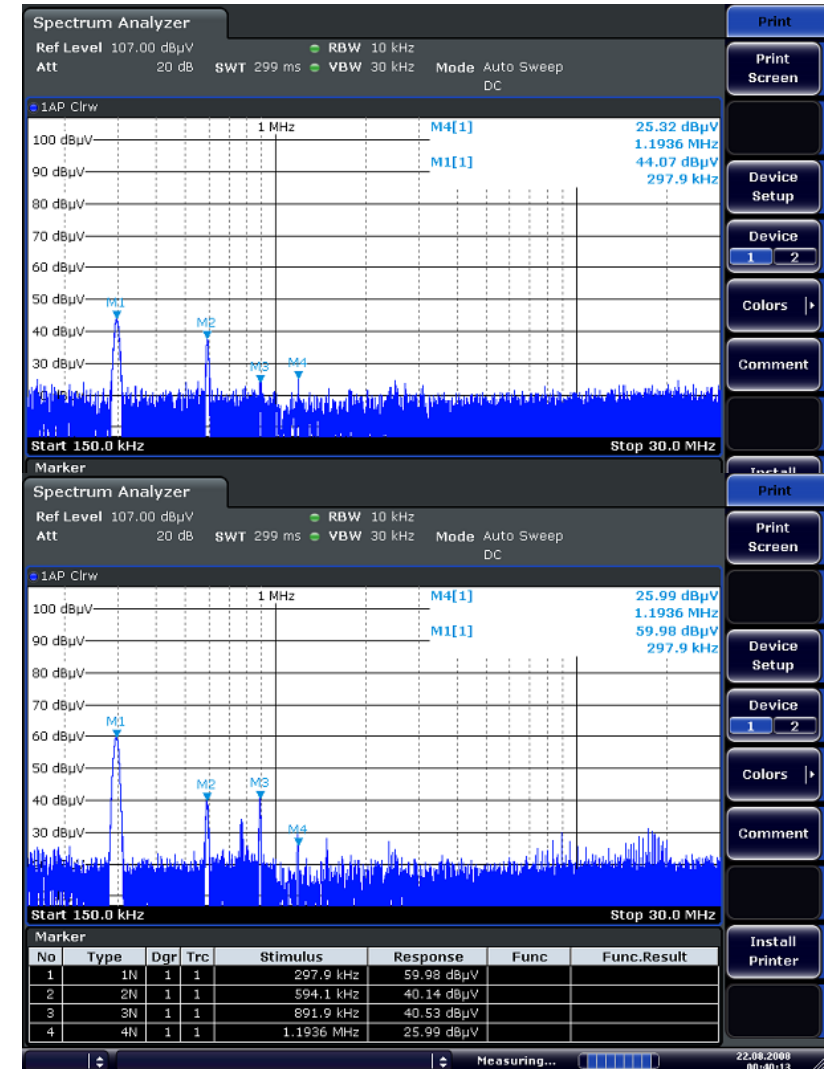
Sectional common mode choke 47mH

Simulation – Conducted Emissions Example 2 CMC Sectional Result

no load >



load 1.5A>

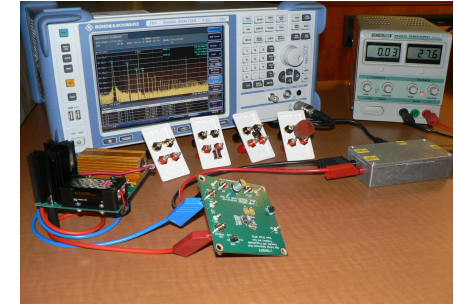


Sectional common mode choke 47mH

Simulation – Conducted Emissions Example 2 Conclusion

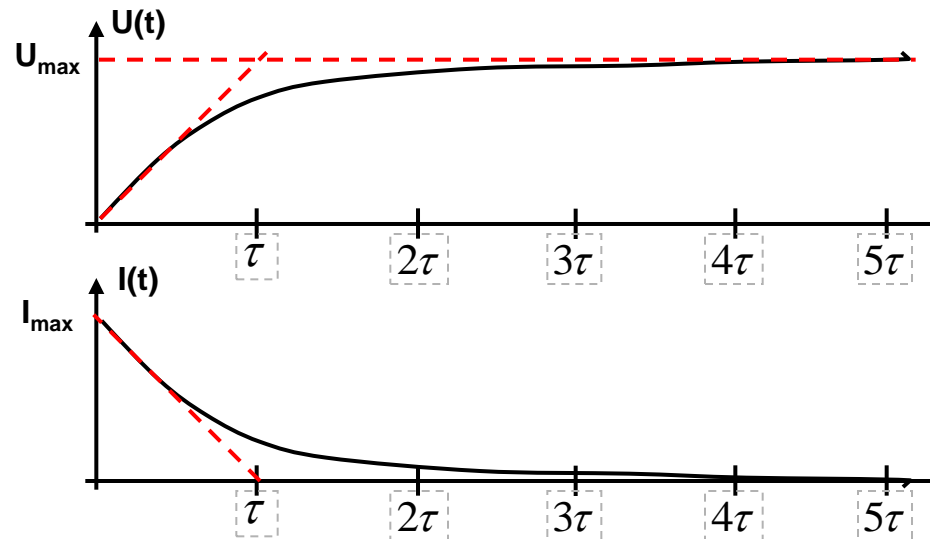
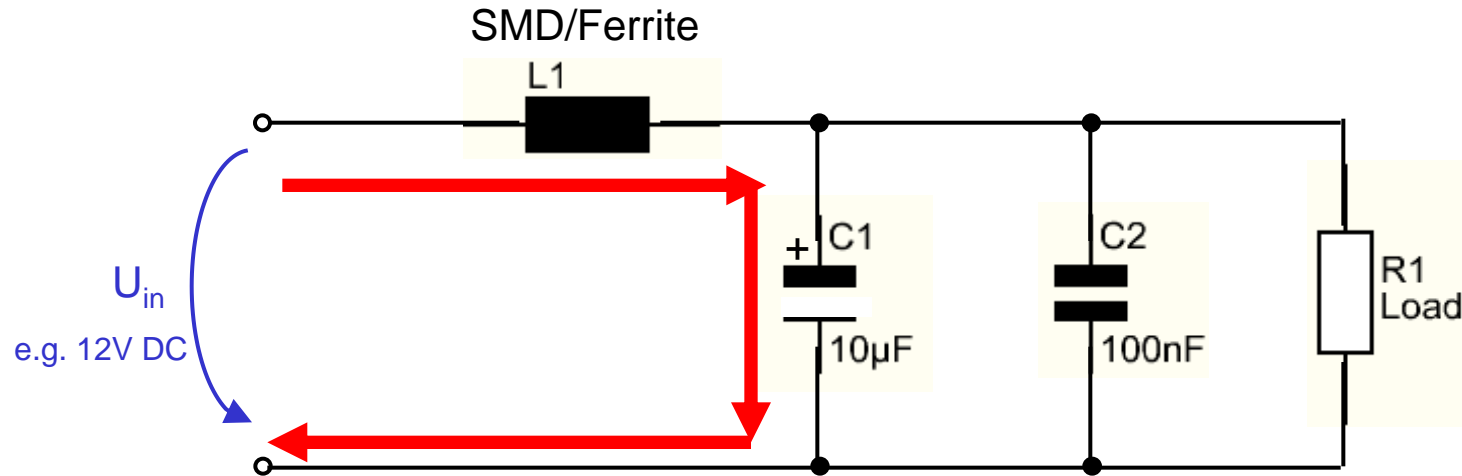


- High frequency noise appears under load (Noise is differential mode)
- **Chip Bead Ferrite**
 - Without a load there is some affect at high frequencies, but with a load the bead pre-magnetizes and there is no effect at all.
- **Differential Choke**
 - Attenuates low frequency noise because of SRF
- **Bifilar common mode choke**
 - Does not attenuate because of very low leakage inductance
- **Sectional common mode choke**
 - Attenuates both high and low frequencies because of leakage inductance and high SRF



Filter topologies – LC-Filter

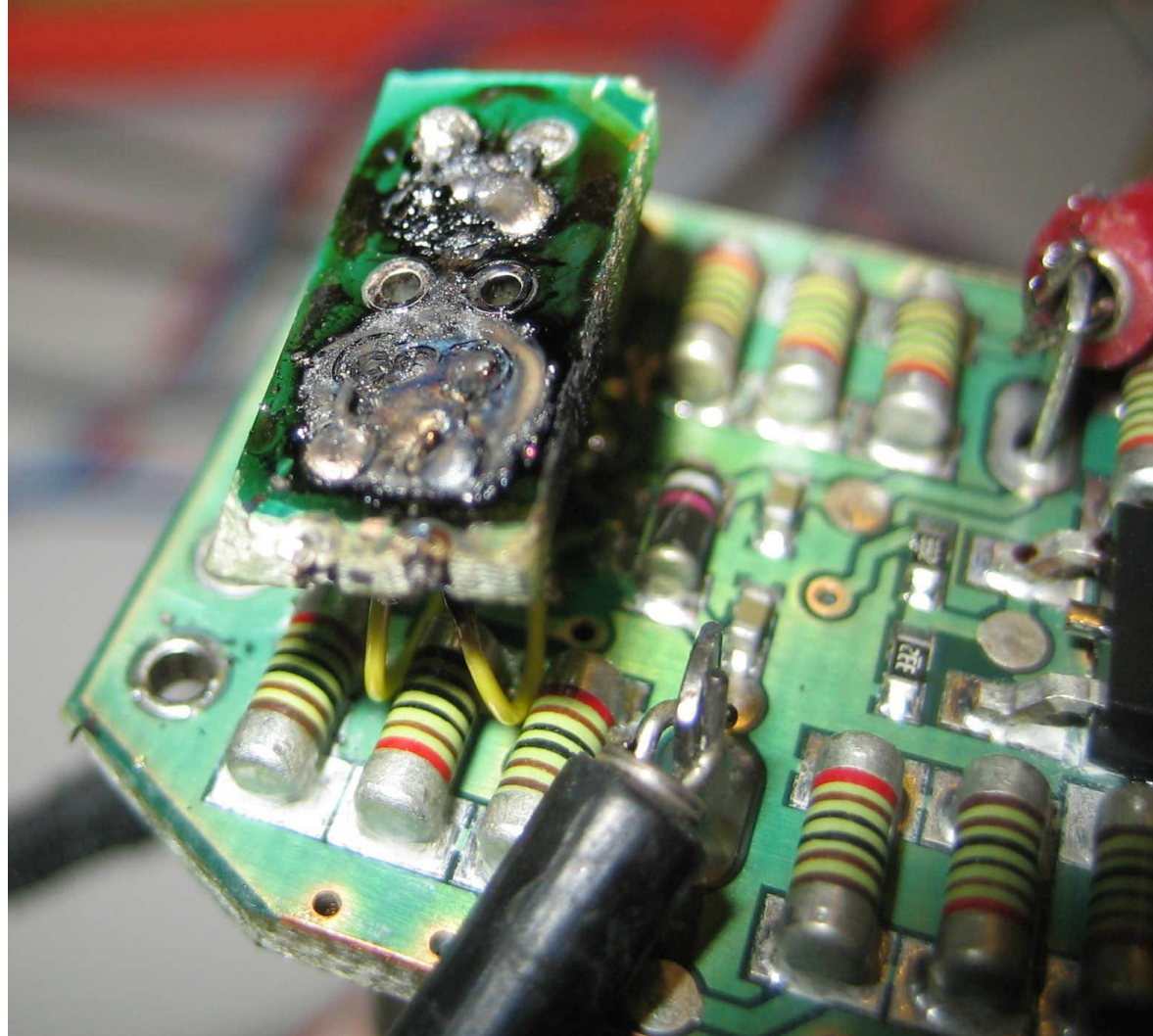
design tip: avoid over current (low dump)



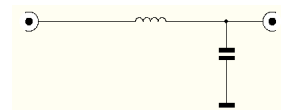
at 15A.....



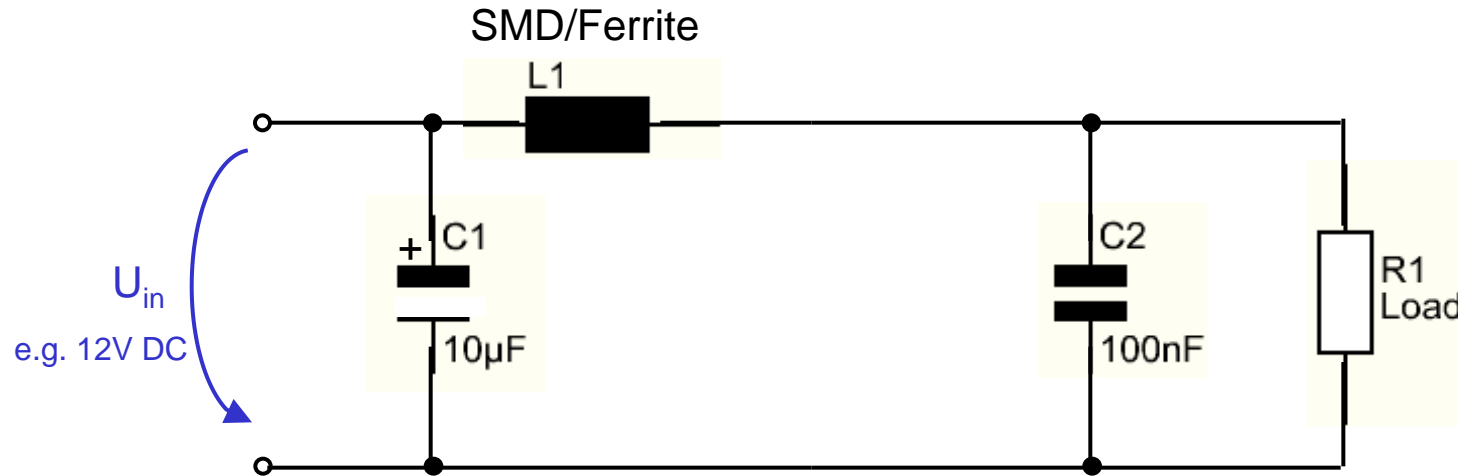
at 15A.....



Filter topologies – LC-Filter



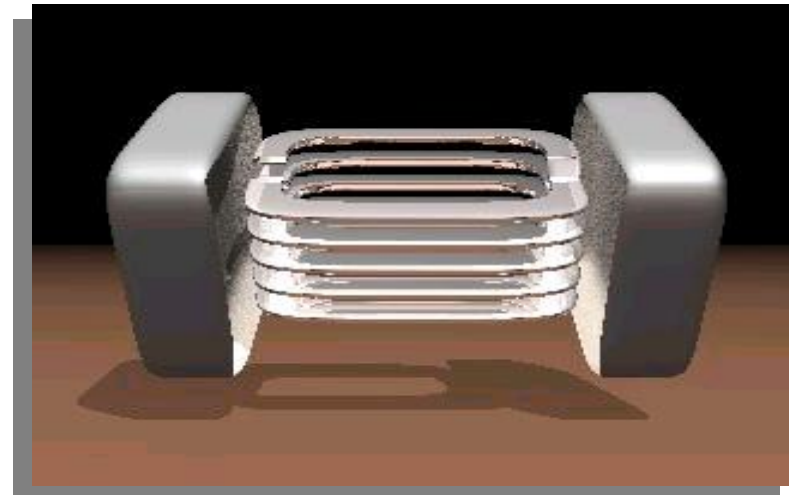
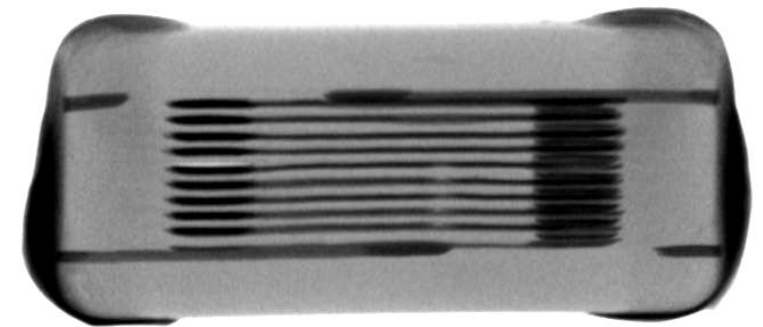
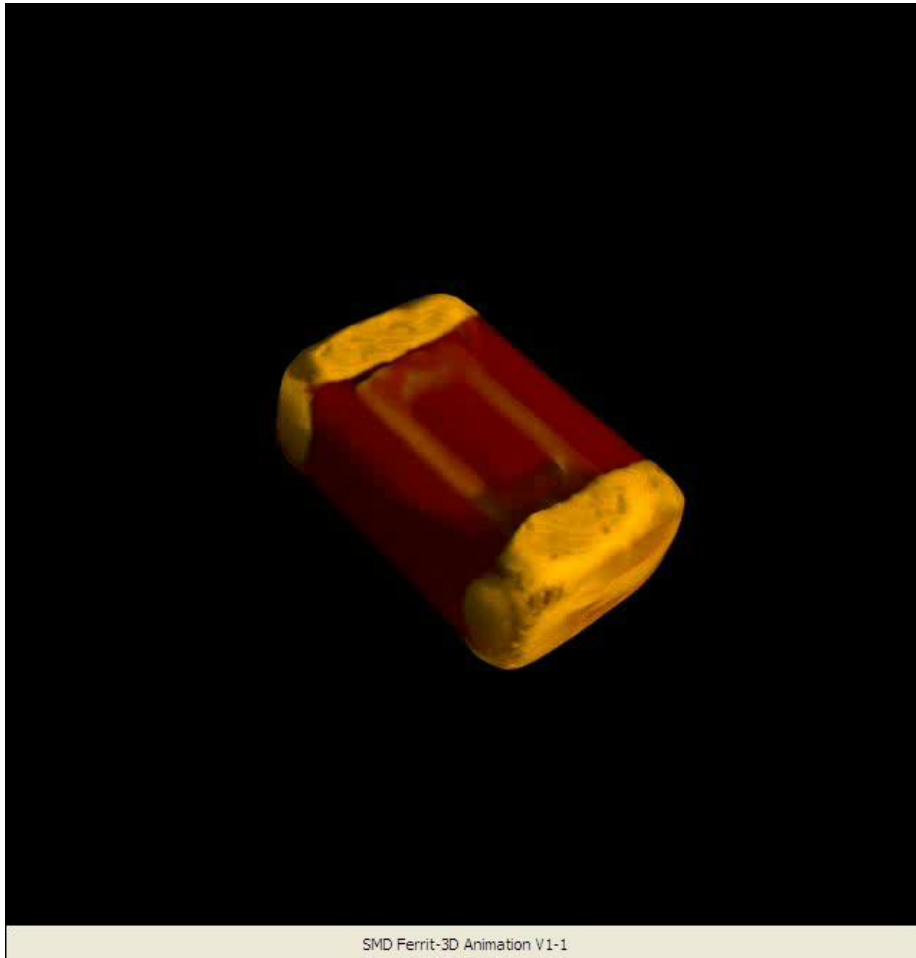
design tip: avoid over current (low dump)



- SMD-Ferrite safe from low dump current
 - not a PI-Filter
- capacitor $C1$ is just for stabilization

L Filter SMD-Ferrite WE-CBF

- Using the core losses $R=f(f)$
- Transform differential noise energy into heat

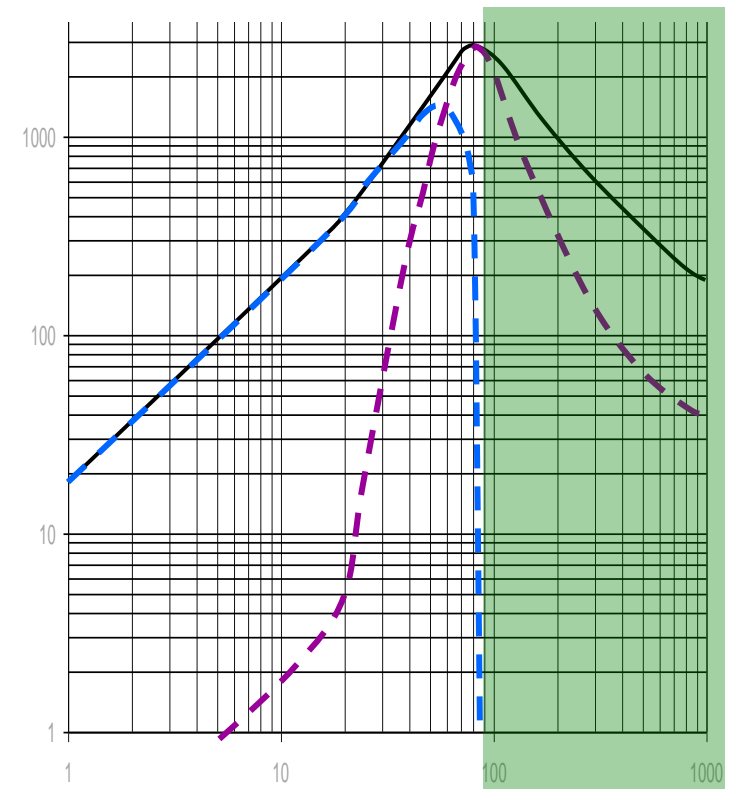
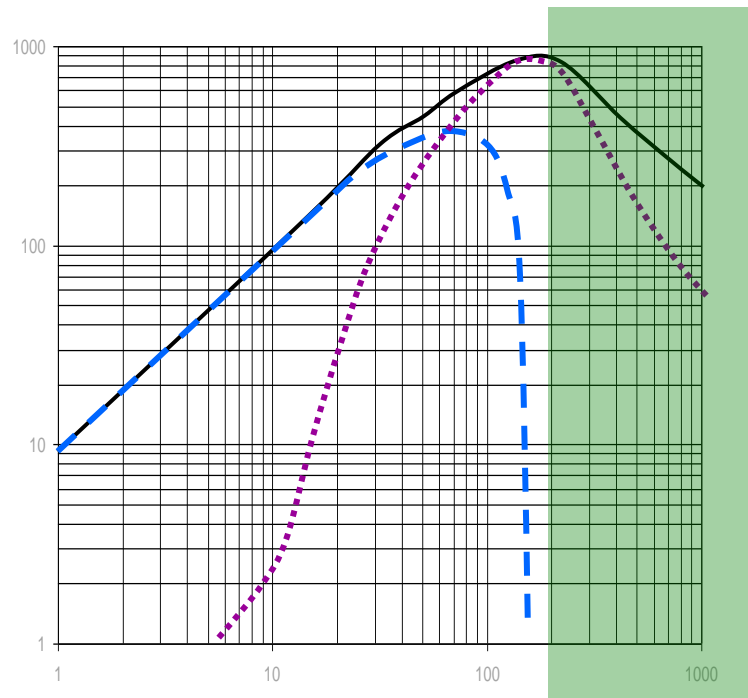
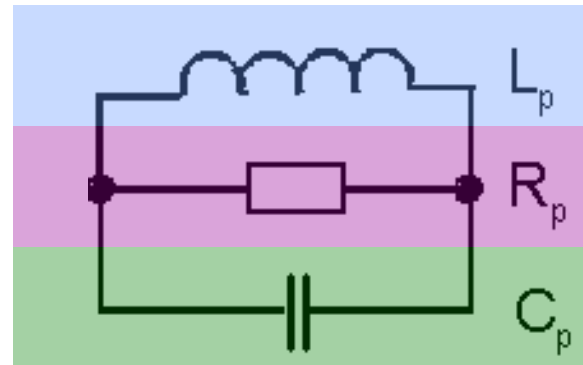


L Filter SMD-Ferrite WE-CBF



IMPORTANT:

- Check equivalent circuit

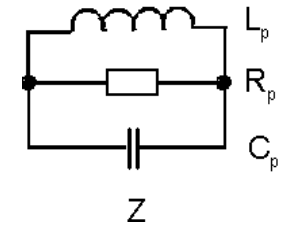


Filter topologies – Inductor / SMD-Ferrite

- Parasitic capacities

→ Inductors: 10 pF ... 500 pF

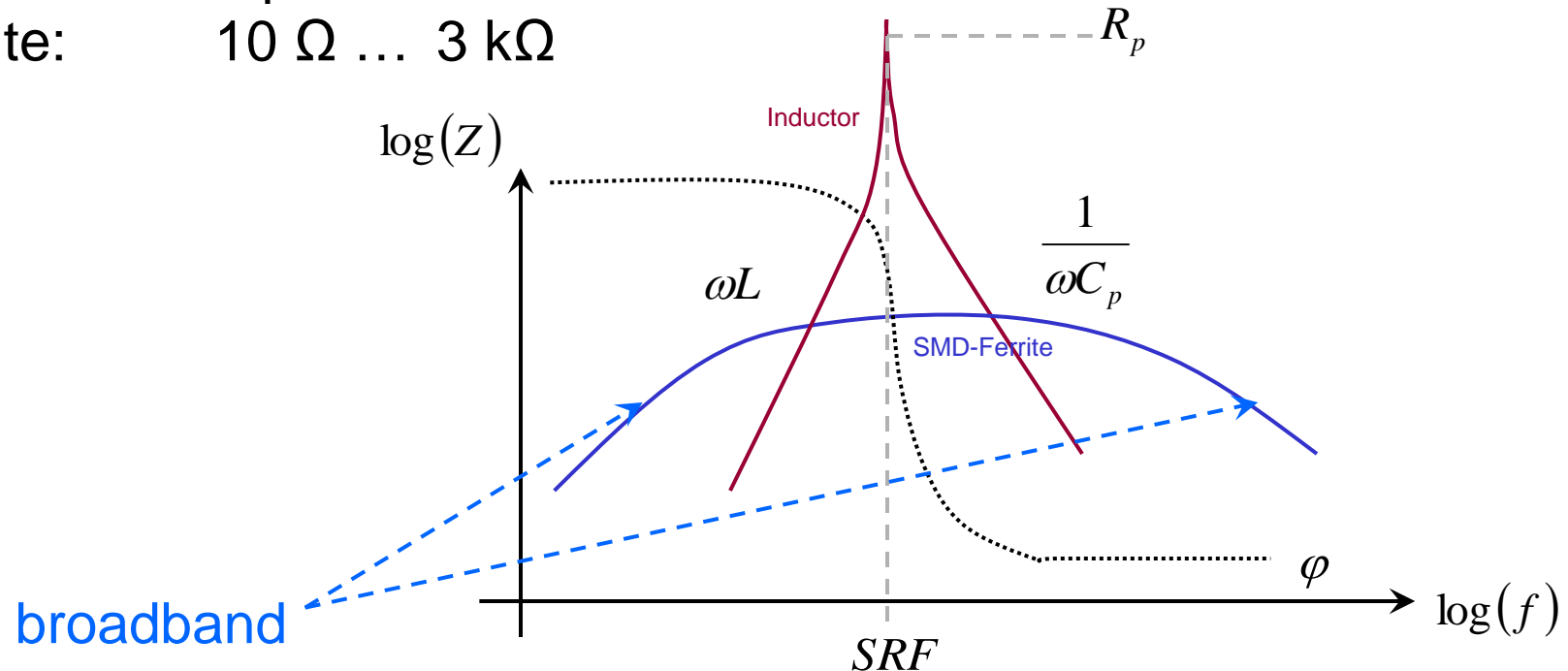
→ SMD-Ferrite: 5 fF ... 5 pF



- Losses

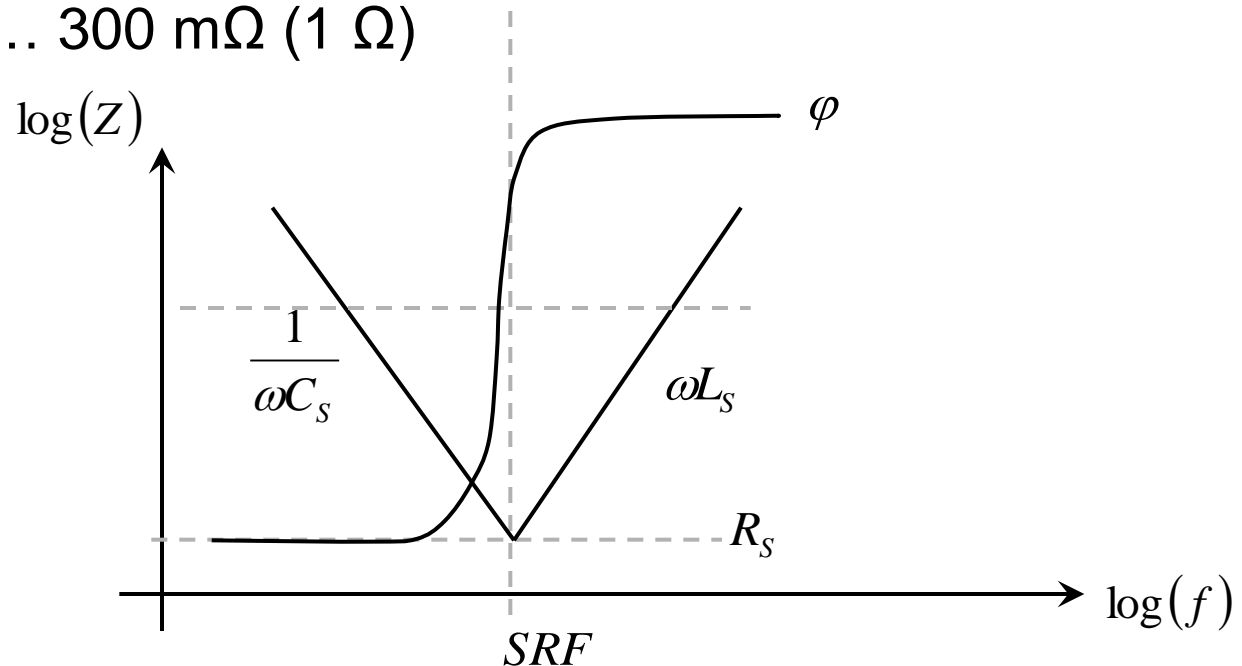
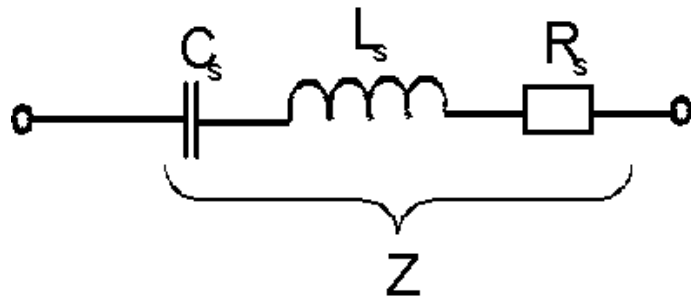
→ Inductors: up to 30 kΩ

→ SMD-Ferrite: 10 Ω ... 3 kΩ

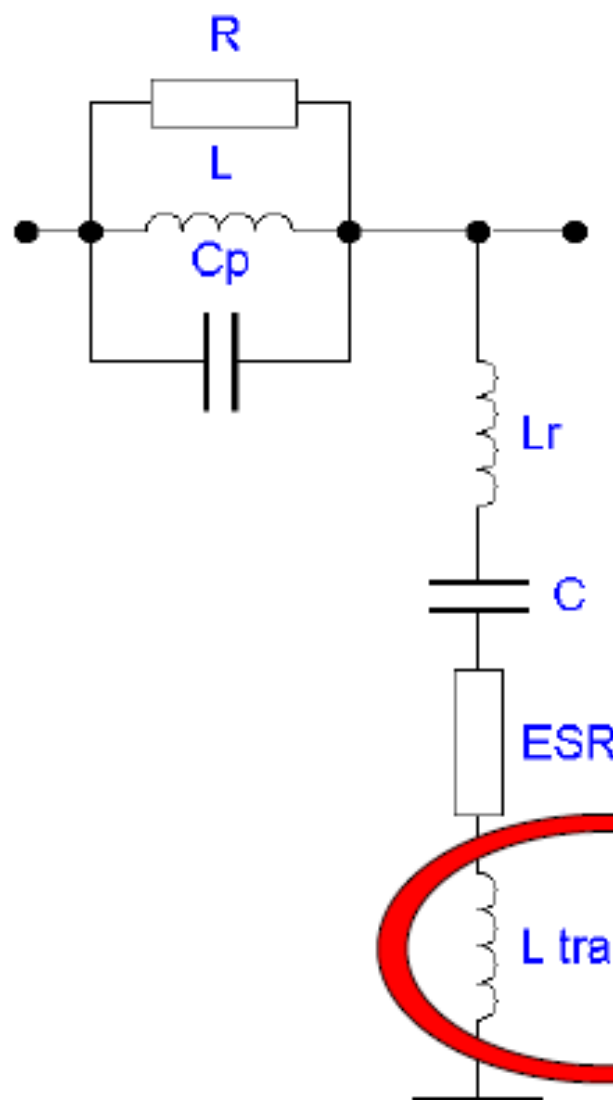


Filter topologies C-Filter

- Expand the filter with an additional frequency dependent component
→ with a Capacitor
- Series inductance L_s
→ SMD-typical: 1 nH ... 5nH
- Losses (ESR) R_s
→ SMD-typical: 20 mΩ ... 300 mΩ (1 Ω)



Filter topologies – LC-Filter



1 mm ~ 1 nH

1 via ~ 0.5 nH

0.5 nH @ 100 MHz = 0.314 Ω

0.5 nH @ 1 GHz = 3.14 Ω !



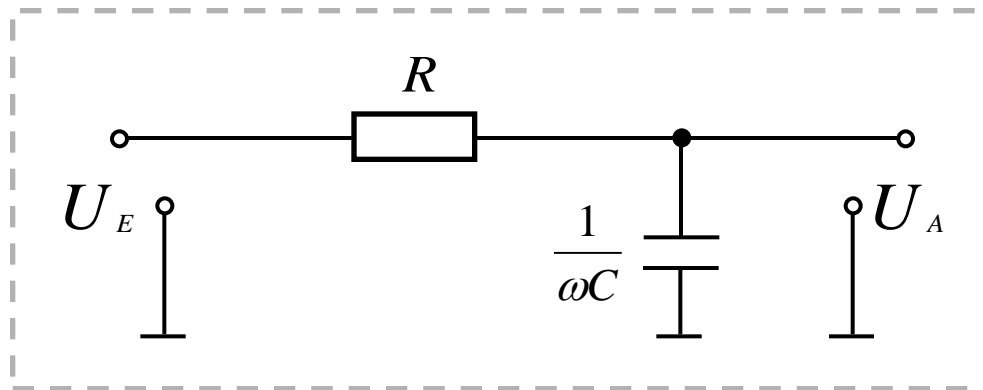
$Z_{ges}: ESR + L_r (2\text{nH}) + L_{via}(0,5\text{nH})$

100nF = 0,2Ω + 1,2Ω + 0,3Ω = 1,7Ω @ 100MHz
 Size: 0603/X7R

Filter and Signal - Low pass filter

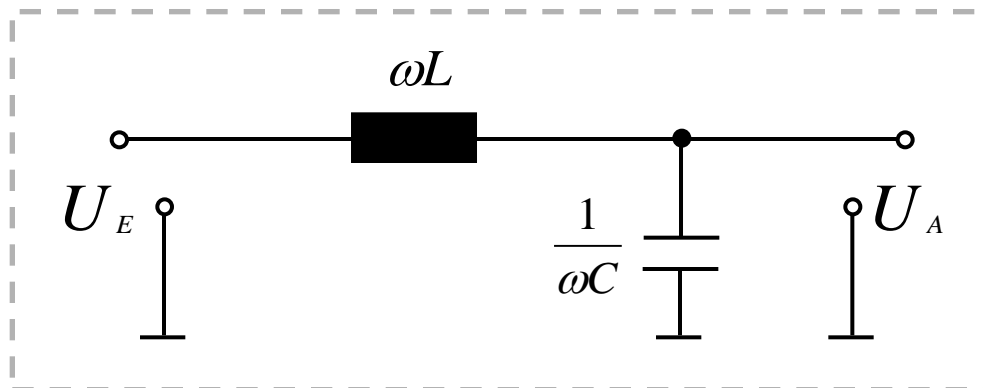
...are most popular used filter for EMI

LPF 1.order



$$f \uparrow \rightsquigarrow Z_C \downarrow$$

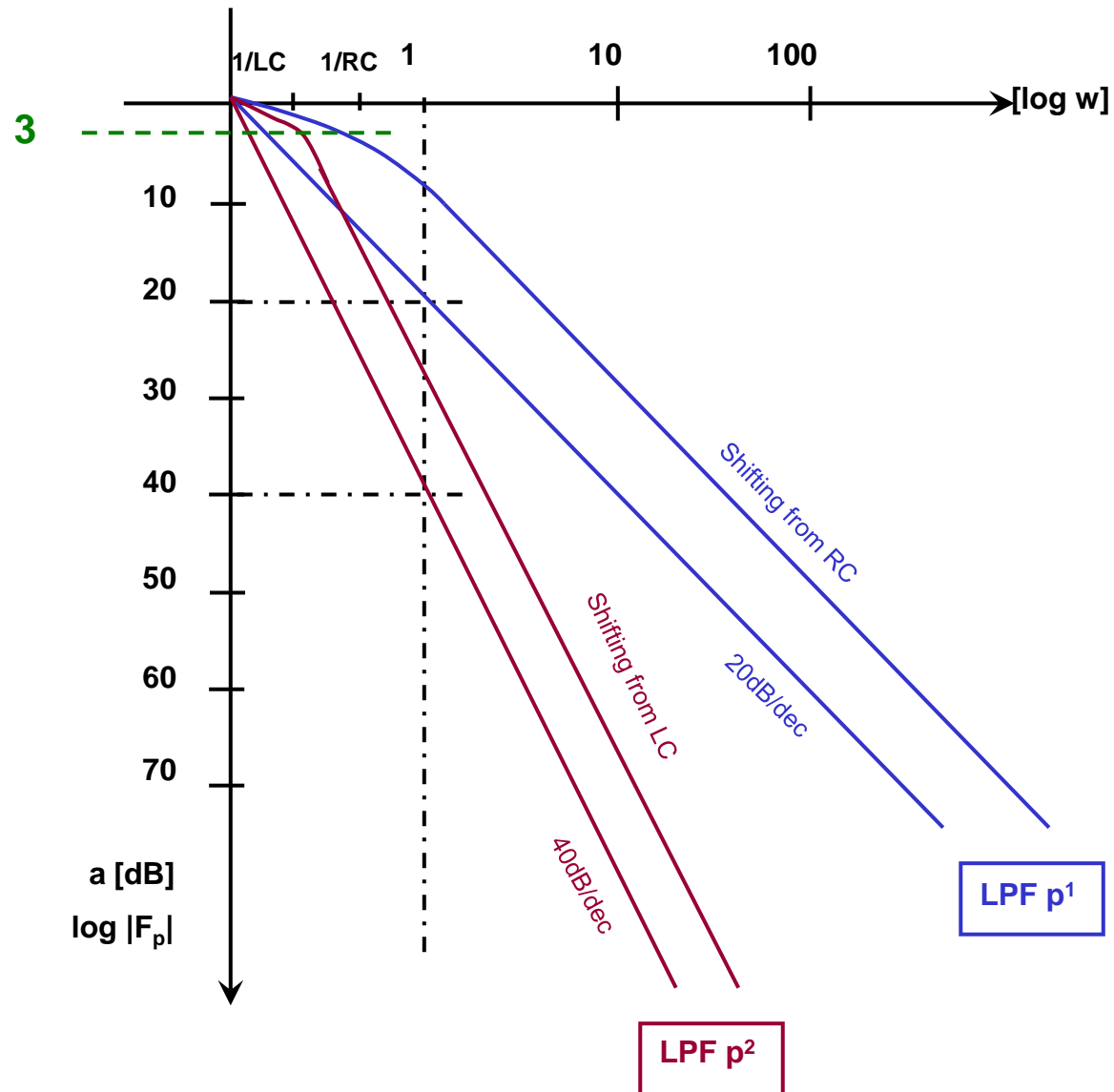
LPF 2. order



$$f \uparrow \rightsquigarrow Z_L \uparrow$$

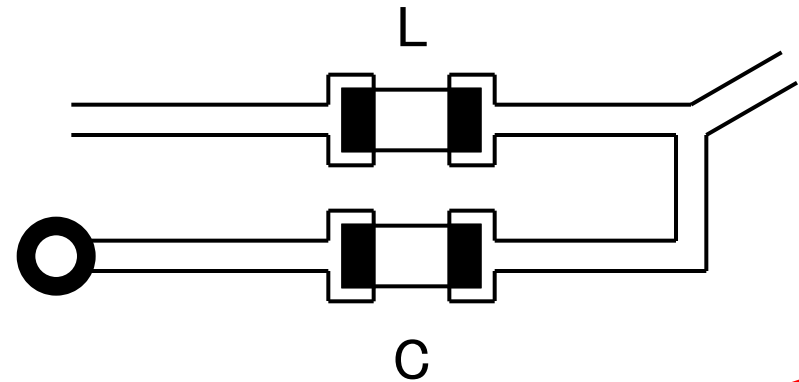
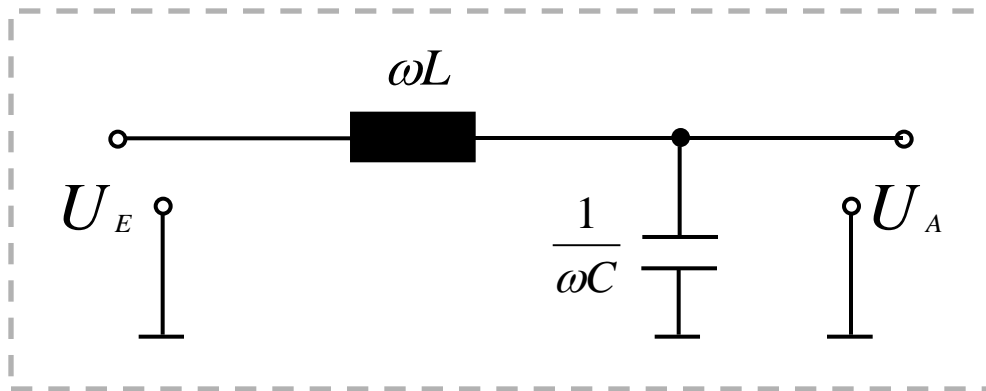
$$f \uparrow \rightsquigarrow Z_C \downarrow$$

Low pass filter - insertion loss



Grounded filter

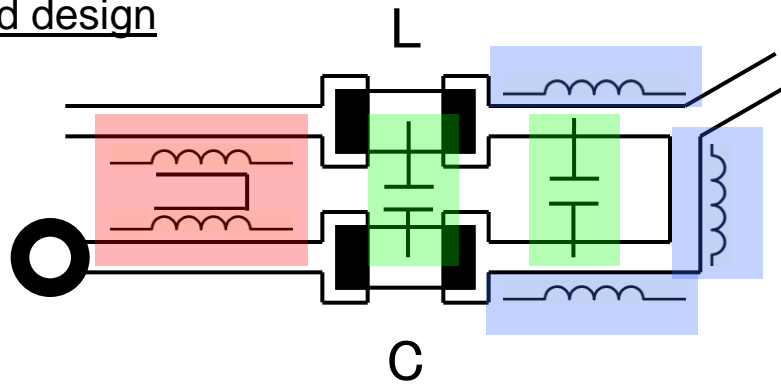
- most important condition for an LC filter
 - extremely good connection from capacitor to ground
- the filter efficiency will be depreciated from additional impedances
 - parasitic from capacitor connection (long legs)
 - layout design (to long trace)
 - from construction (ground pins, or bolts for PCB mounting)



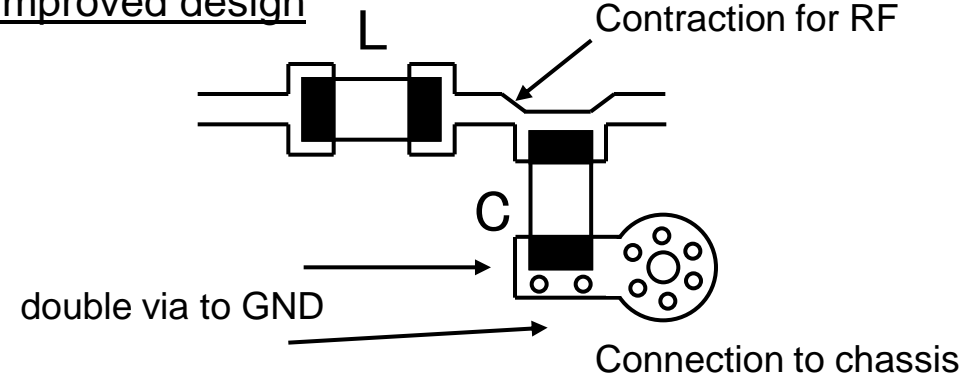
- low space required
- good grounding
- well arranged

Grounded filter

bad design

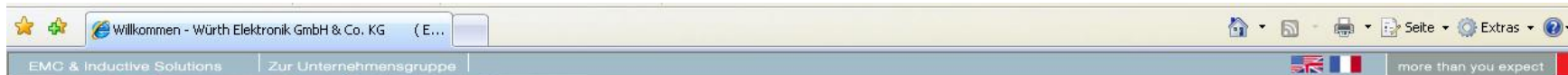


Improved design



- inductive coupling from filter input to capacitor ground
- capacitive coupling – will increase for higher frequencies
- parasitic inductance from too long traces
 - 1mm trace means approx. 1nH
 - per via 0,5 nH
- no connection to chassis/case
- bad position of filter output

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Unexpected effects – who smile more?

- ...if the application is not EMC conform ?



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