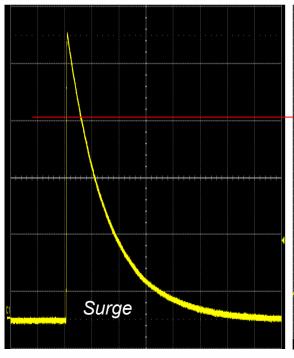
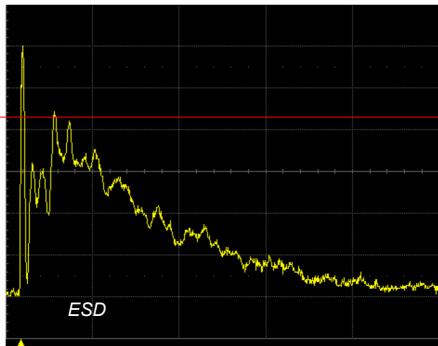
EMC Pulse Measurements

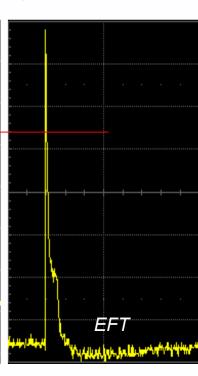
and Custom Thresholding

LeCroy

Presented to the Long Island/NY IEEE Electromagnetic Compatibility and Instrumentation & Measurement Societies - May 13, 2008







Contents

- EMC measurement requirements
- How thresholds affect pulse measurement definitions and why standard pulse parameters will not work for EMC pulses
- Measurement thresholds for ESD pulses
- Sequenced acquisition for EFT (Electrical Fast Transient) pulses
- Parameter limiters applied to filter EMC pulse statistics
- Custom measurements

EMC Measurement Requirements

4 Quadrants of EMC/ESD Testing

Radiated Emissions

Will the EUT create emissions that interfere with the operation of other products?

Radiated Immunity

Will the EUT be susceptible to emissions from other devices, either through the air or via cables?

Conducted Emissions

How much noise voltage is injected back into the mains by the EUT?

Conducted Immunity

Will the EUT be susceptible to transients generated by switching of capacitive or inductive components?

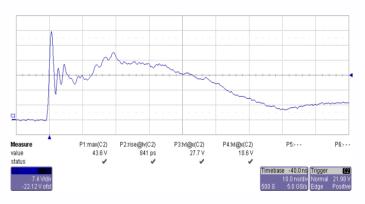
- Oscilloscopes used for
 - Radiated Immunity
 - Conducted Immunity
- "Pulsed EMI tests:
 - ESD (Electrostatic Discharge)
 - EFT (Electrical Fast Transient)
 - Surge

EUT = Equipment Under Test

Test Requirements

- Generate a Burst, Surge, or ESD pulse (for example, with an ESD gun)
- Verify the pulse shape(s) from the generator with an oscilloscope before each test
 - Rise Time
 - Fall Time
 - Width
- Ensure that the DUT still operates correctly during test, for example:
 - Automotive engine control unit still transmits proper messages
 - Telecom board serial data messages are uncorrupted
 - Consumer electronics item still functions
- ESD Standards:
 - IEC 61000-4, EN 61000-4, ITU, UL,
 FCC, Telcordia, ANSI, Bellcore,
 Proprietary (Military, Automotive), etc.
 - The majority of Immunity Testing follows the IEC 61000 (CE Mark)



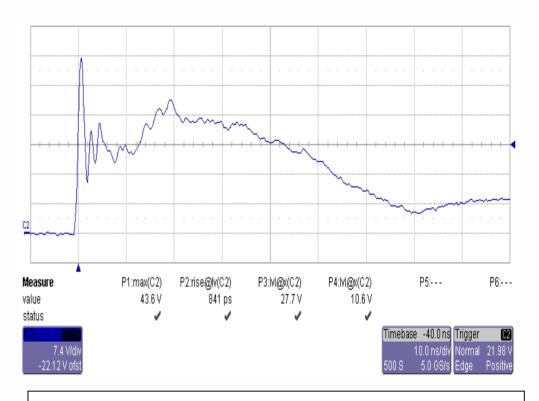




ESD Testing – Electrostatic Discharge Measurement Steps

Pulse Characteristics

- T_{rise} = 0.7 to 1.0 ns
- $T_{fall} = 0.7 \text{ to } 1.0 \text{ ns}$
- Measurement Needs
 - Capture a Single Pulse
 - Measure one pulse, verify rise time for positive pulses, verify fall time for negative pulses
 - 1 GHz, 2 GHz, or 3
 GHz+ scope depending on standard specification

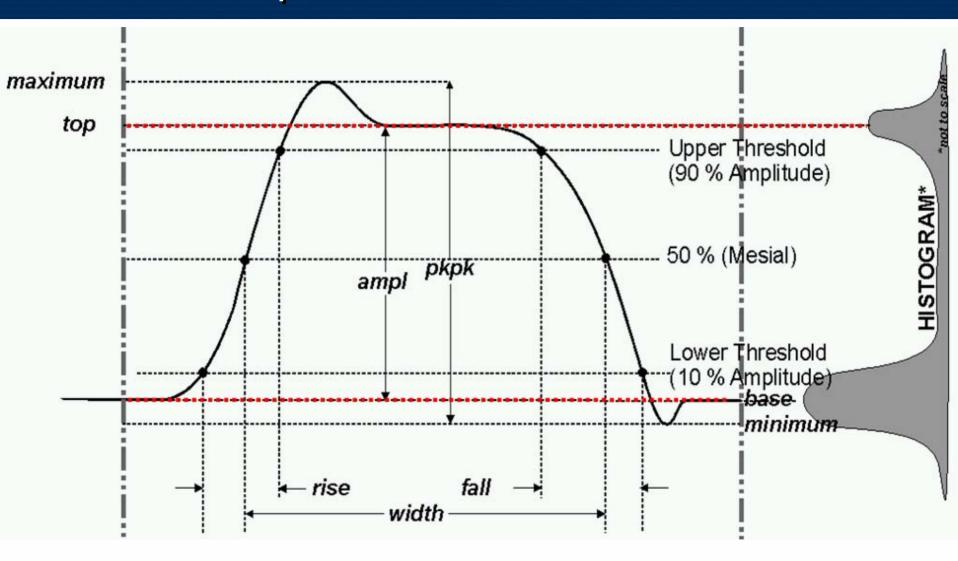


How is risetime defined on this ESD pulse? 10%-90% risetime is only meaningful if 0% and 100% levels exist and have been defined on the pulse.

Pulse Measurement Definitions

IEEE Standard Pulse Definitions

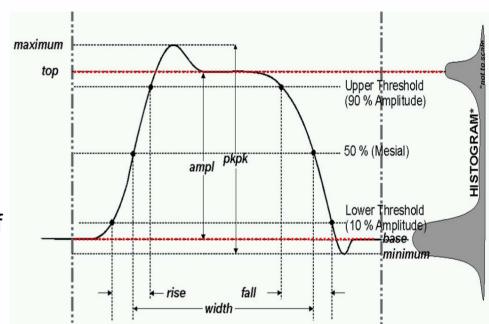
How Oscilloscopes Measure Pulse Parameters



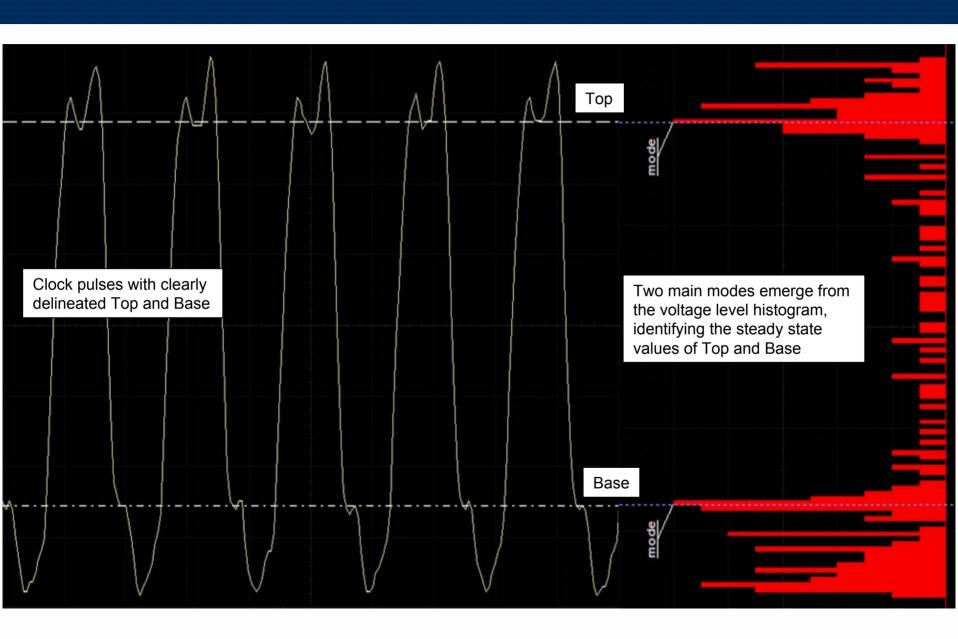
Oscilloscopes determine pulse parameters from Top and Base values

IEEE Pulse DefinitionsHow Pulse Measurements Are Determined

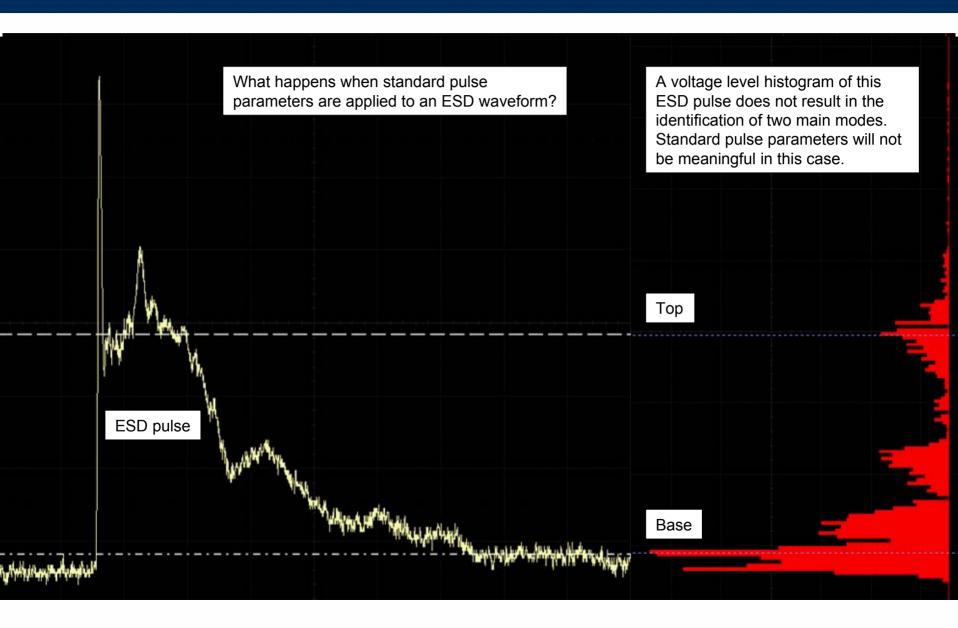
- Pulse measurement definitions are defined by the IEEE Std 181-2003 "IEEE standard on transitions, pulses, and related waveforms"
- Oscilloscopes conform to the IEEE pulse measurement definitions, and Top and Base are determined statistically based on the two modes of a voltage level histogram.
- Top and Base form the 100% and 0% reference levels which are used for measurements such as amplitude, risetime, falltime, period, frequency, width, duty cycle, overshoot, and virtually every timing measurement.
- Top and Base must first be calculated correctly in order for timing and amplitude measurements to produce the correct measurement result.



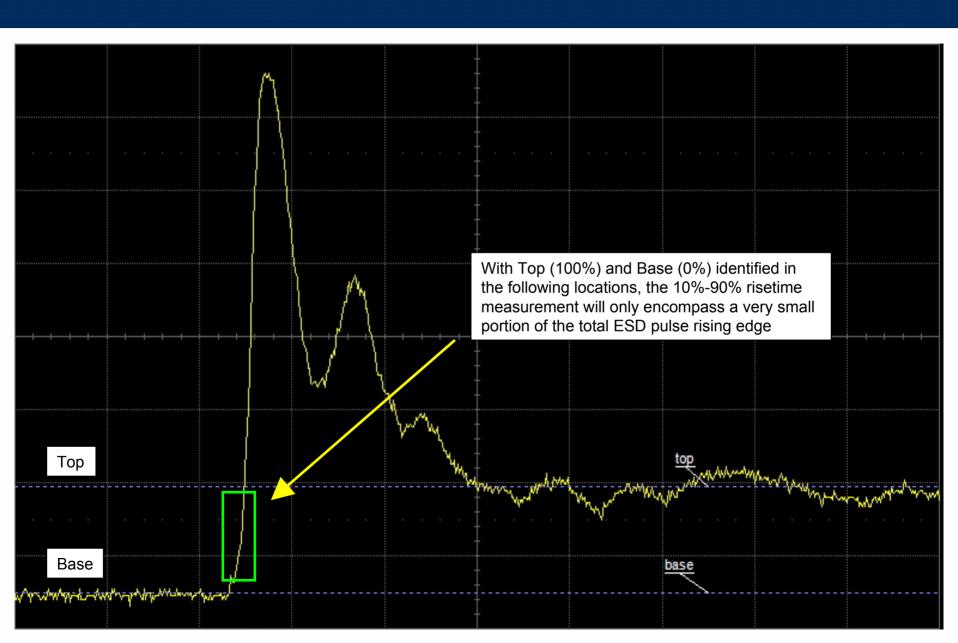
Clock Top and Base correctly determined from voltage histogram



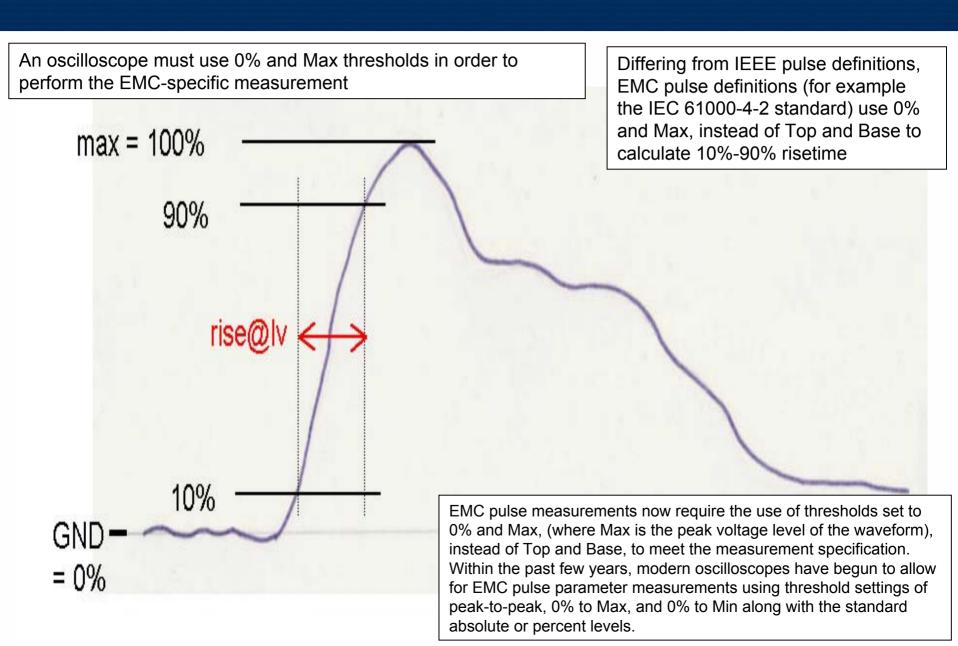
ESD Top and Base are not meaningful for pulse measurements



ESD Top and Base are not meaningful for pulse measurements



EMC Risetime Definitions use 0% and Max



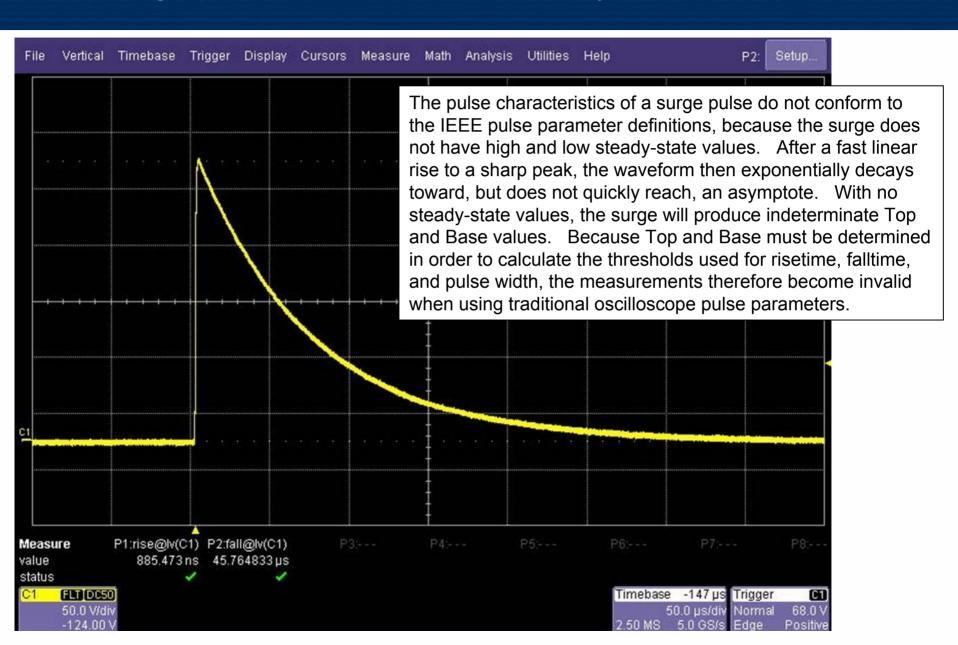
Risetime calculated using standard IEEE pulse parameter definitions



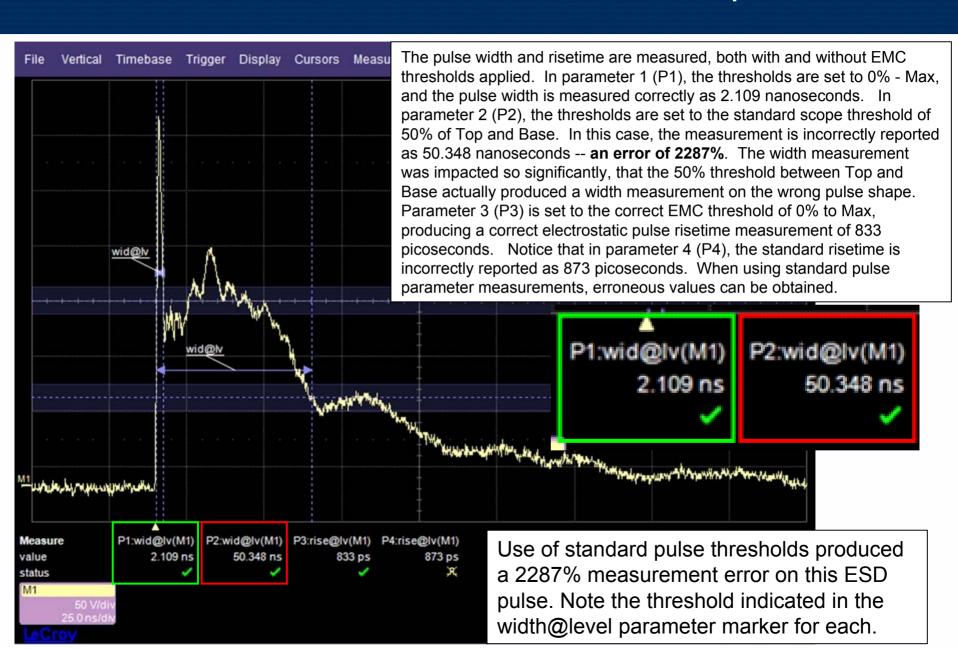
Risetime calculated using EMC thresholds



A Surge pulse does not have a clearly-defined Top and Base



Standard and EMC thresholds for ESD pulse width



EFT Testing – Electrical Fast Transient

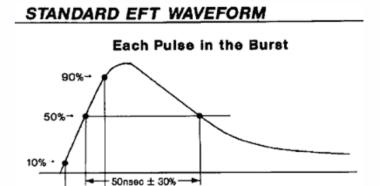
Measurement Steps

Pulse Characteristics

- $-T_{rise} = 5ns$
- $-T_{fall} = 50$ ns
- Burst of many 5x50 pulses

Measurement Needs

- Capture 2ms of burst
- Measure one pulse, verify shape (rise, fall, width)
- Measure burst frequency (10-100 kHz)
- Measure Capture time of burst packet (2ms)
- Measure burst packet rate (300ms)

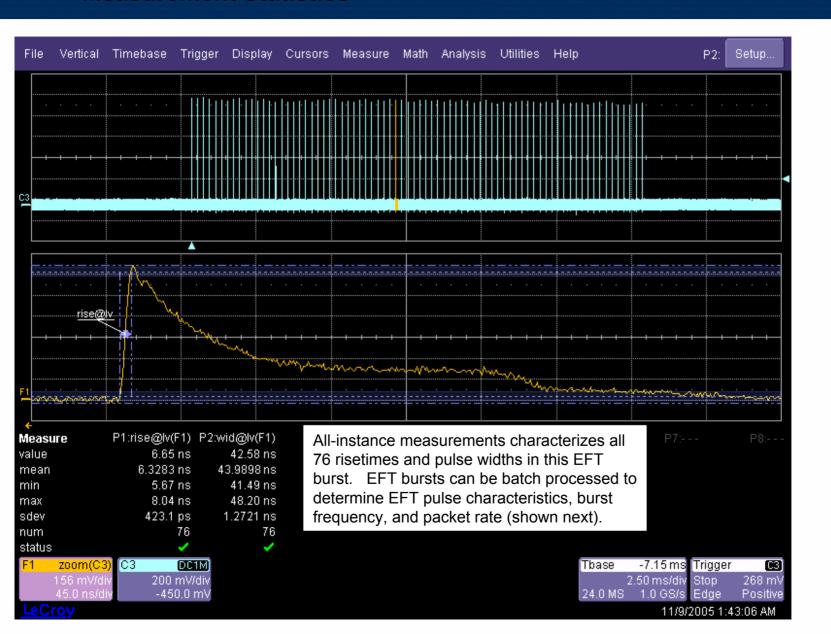


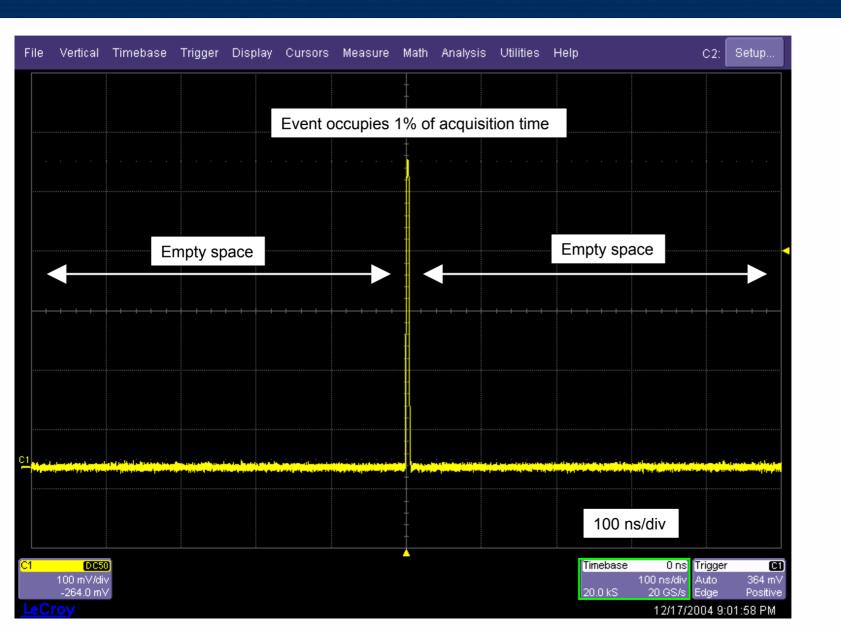
5nsec ± 30%

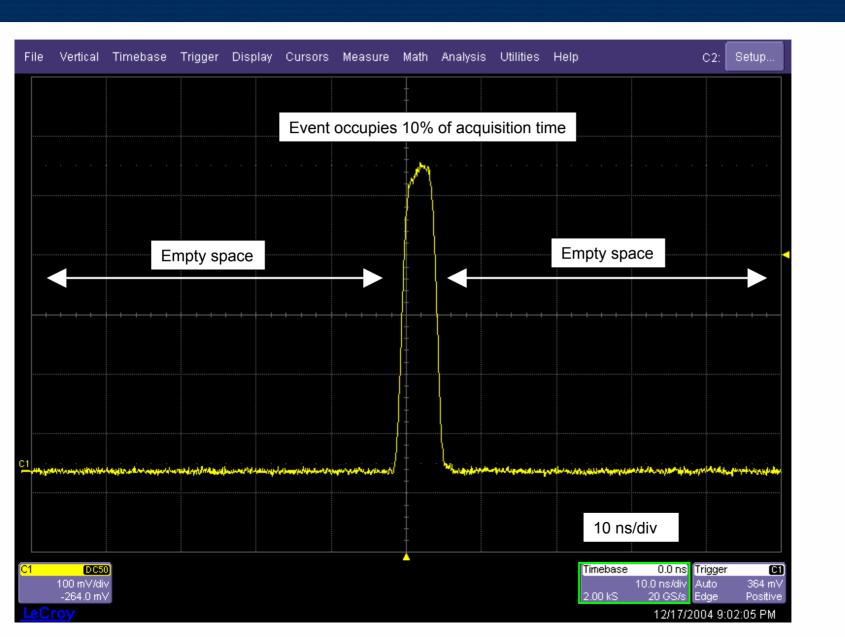


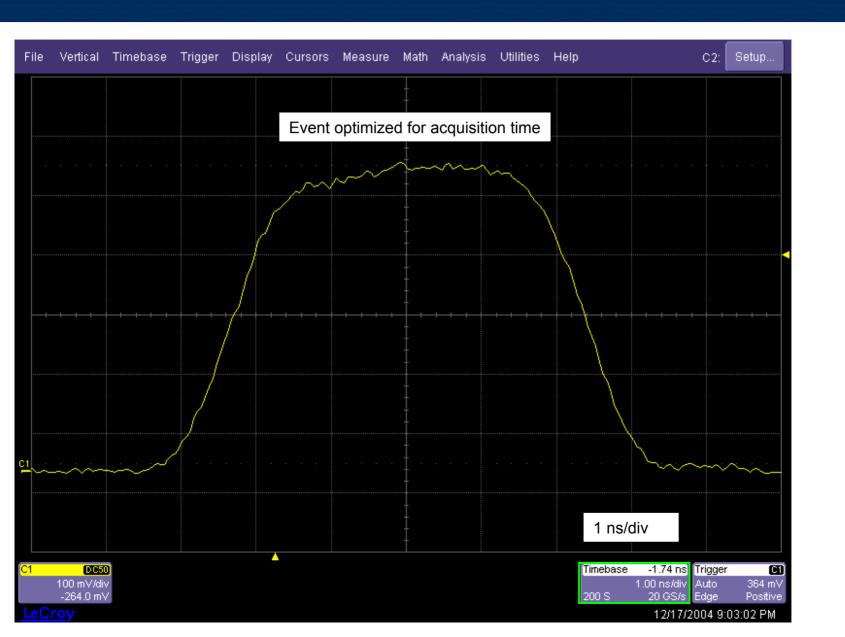
EFT Testing – Electrical Fast Transient

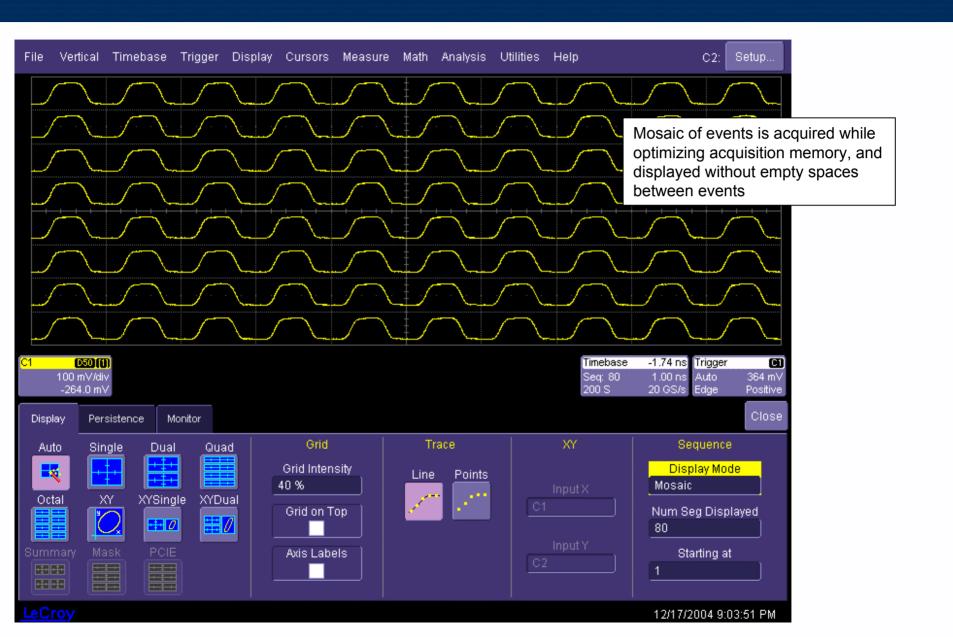
Measurement Statistics

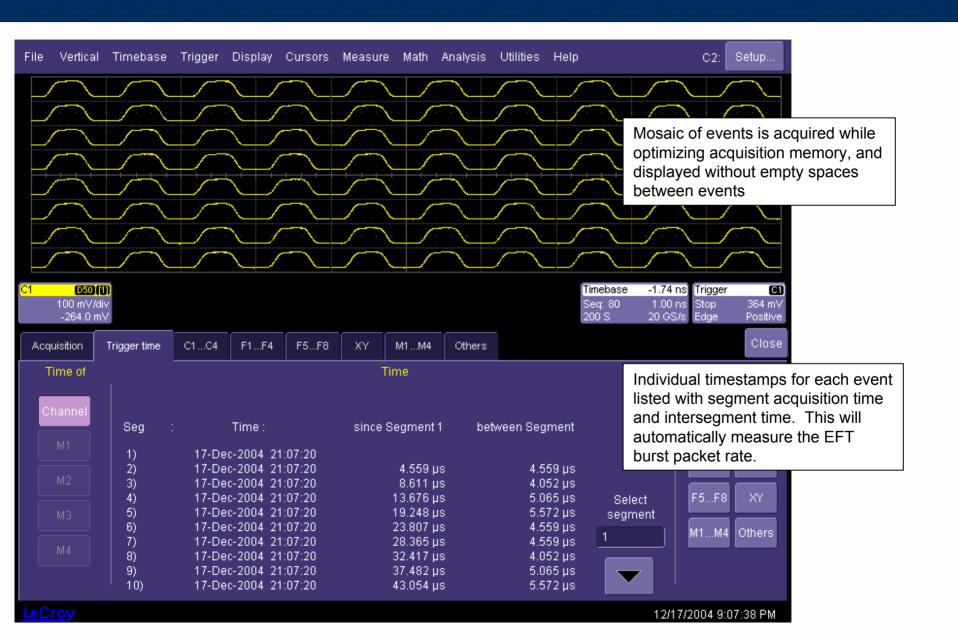


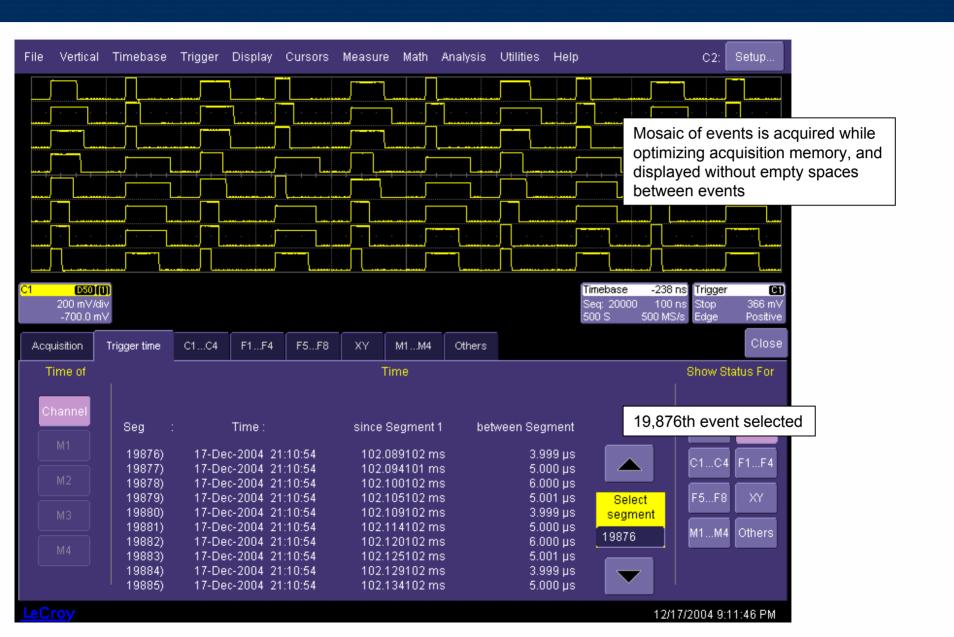




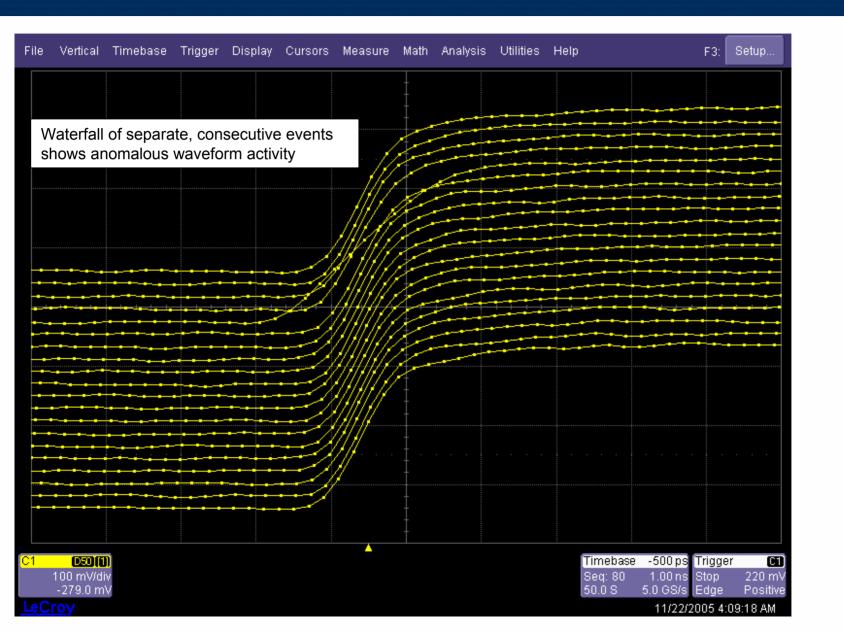




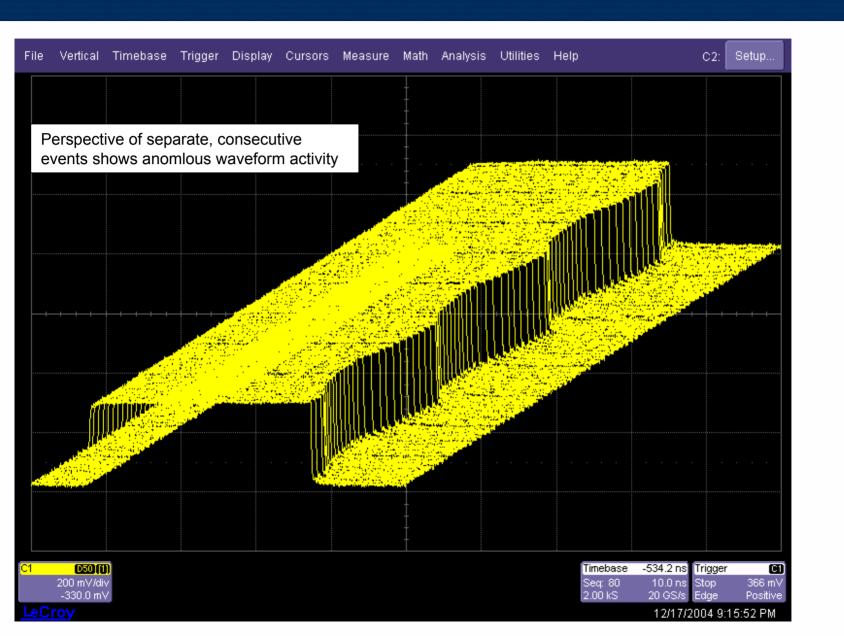




Sequence Waterfall shows anomaly

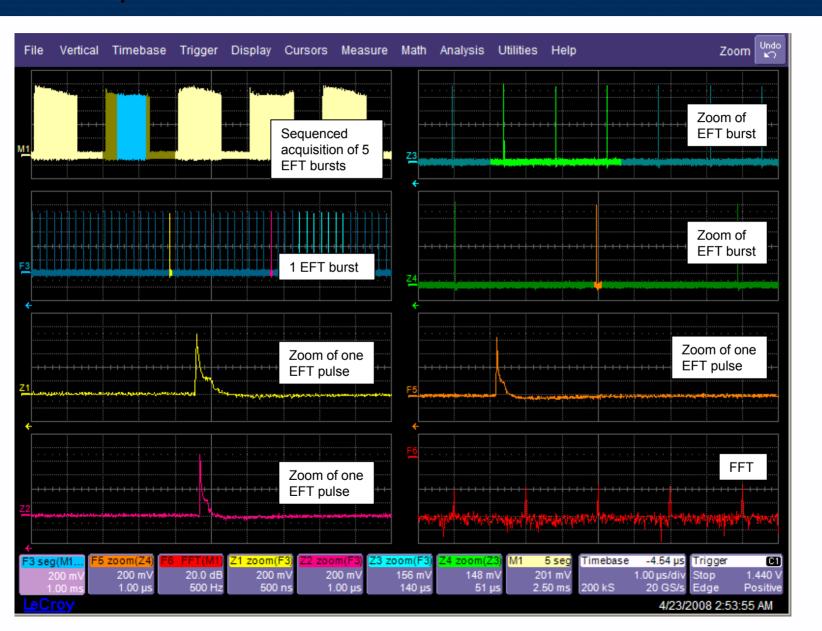


Sequence Perspective shows contour of acquired pulses



EFT Testing – Electrical Fast Transient

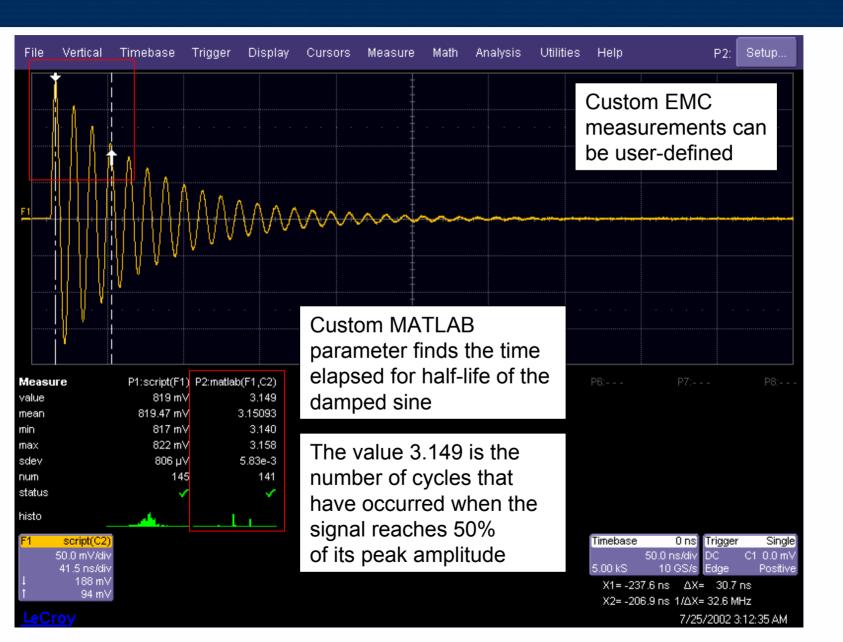
Sequence Mode and Octal Grid



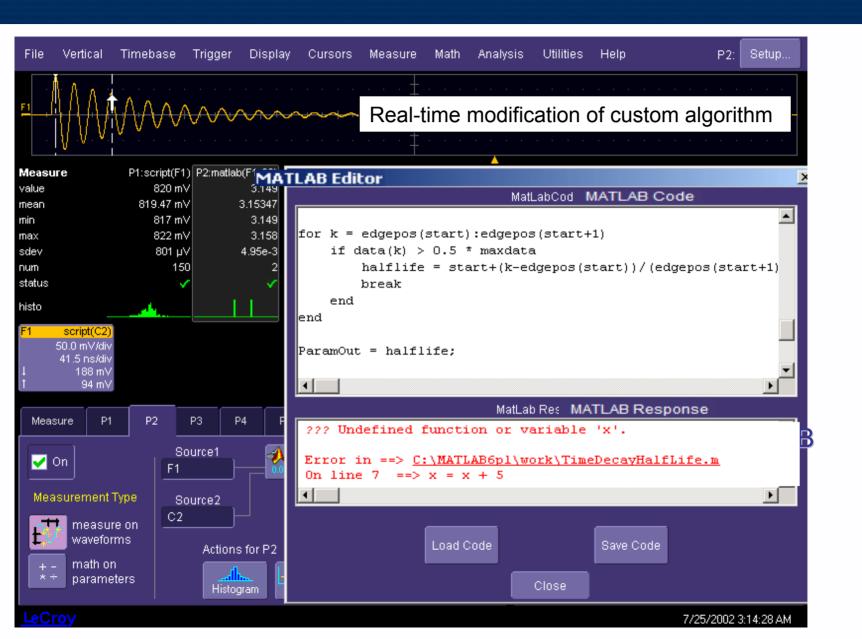
Parameter limiting technique for ESD width measurement



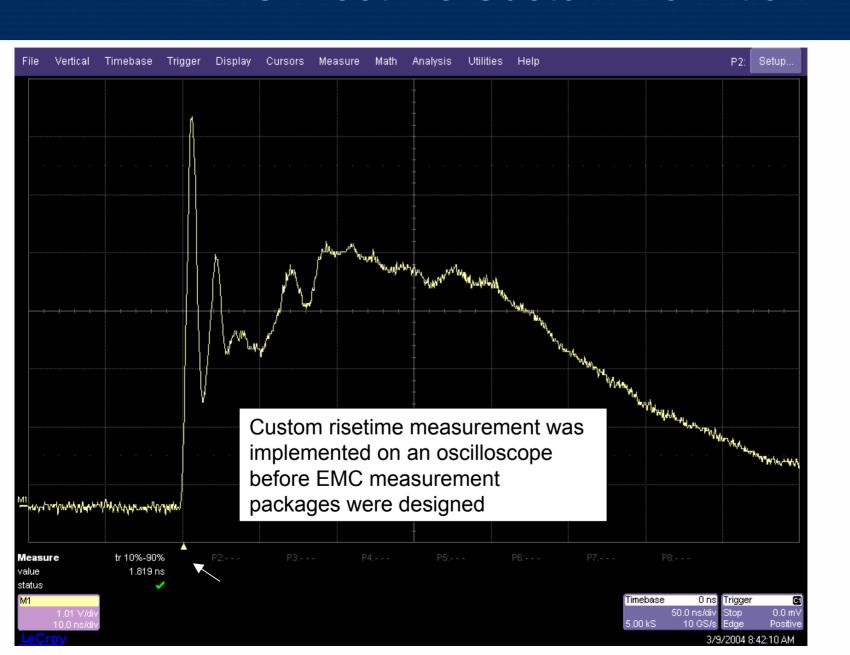
Inline Custom Measurement



Real-Time Modification of Custom Measurement



EMC Risetime Custom Definition

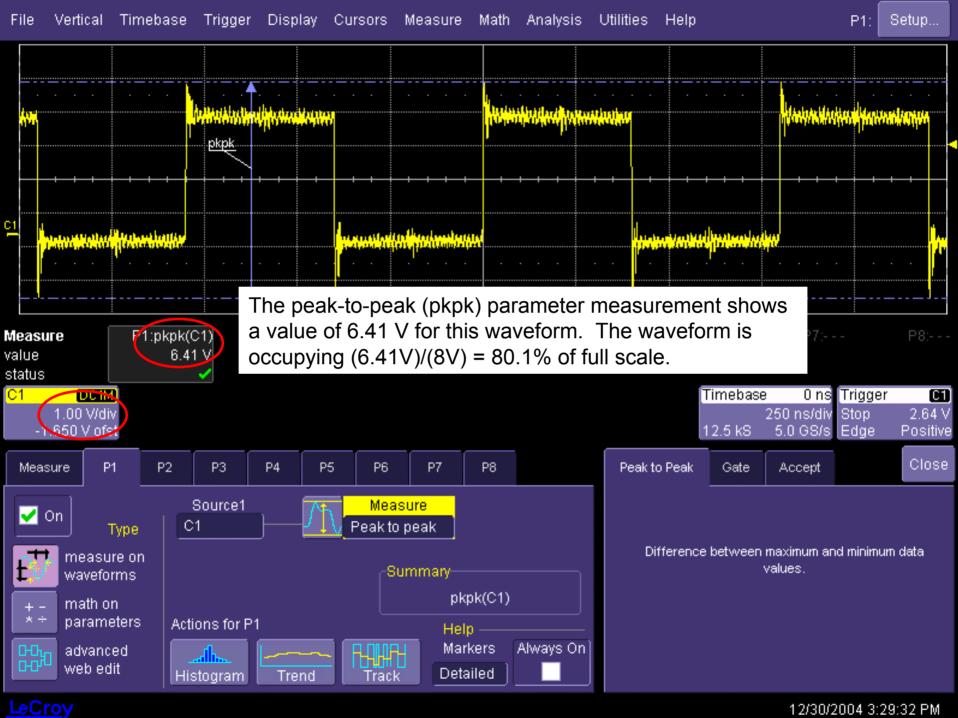


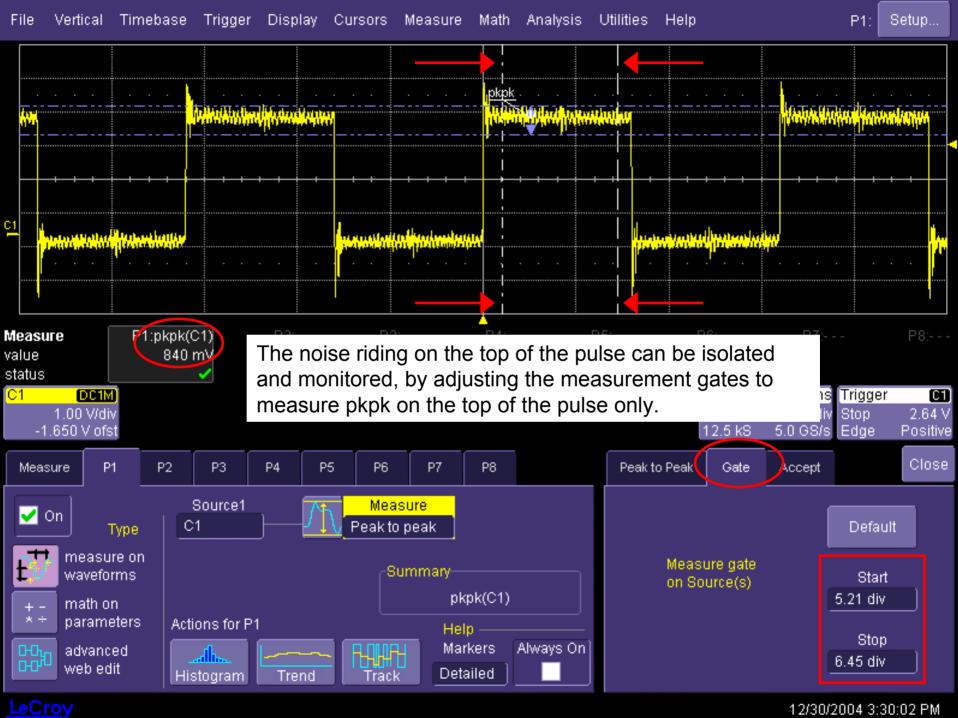
Summary

- EMC/ESD test specifications require verification of rise times, fall times, pulse widths and pulse shapes
- Standard oscilloscope pulse parameter measurements are based on IEEE pulse definitions
- EMC engineers use different pulse definitions which oscilloscopes are not designed to use
- Non-standard measurement setups are required to perform accurate pulse parameter measurements of electrostatic discharge, electrical fast transients, and surges.
- Selecting the correct measurement threshold can make a significant difference in the measurement accuracy of these signals.
- During acquisition of EMC pulses, vertical channel scaling affects signal integrity (shown next)

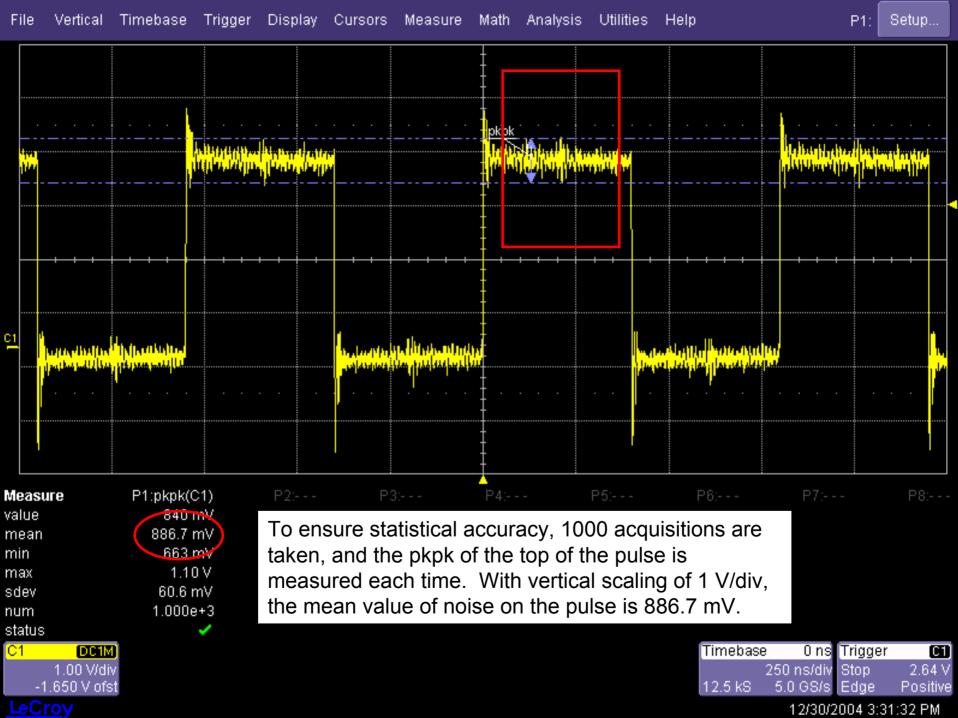
Vertical Scaling of EMC pulses affects Signal Integrity

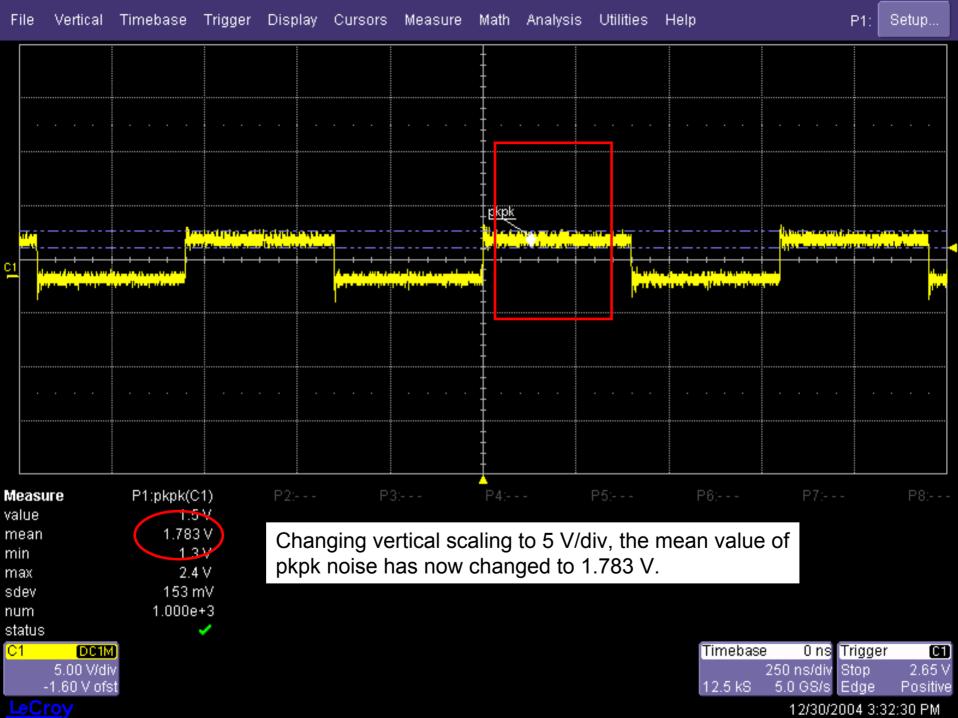




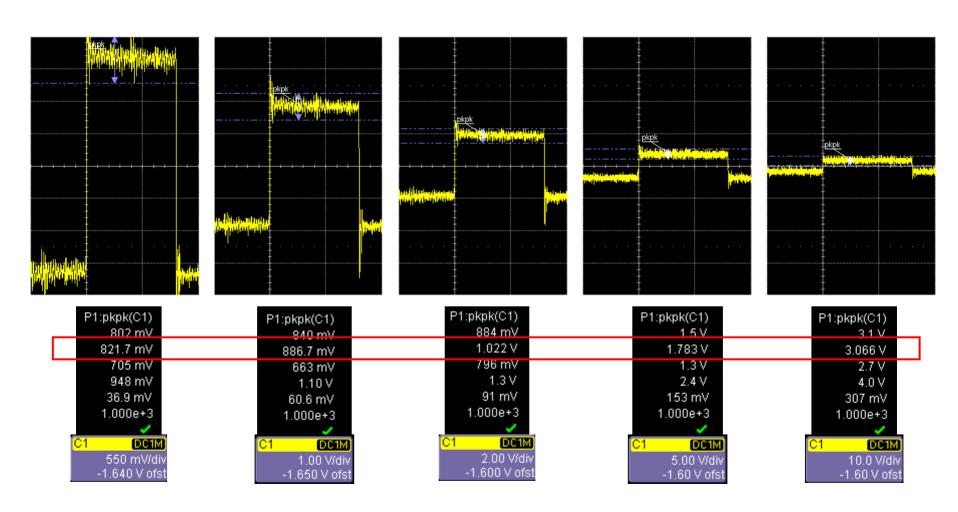


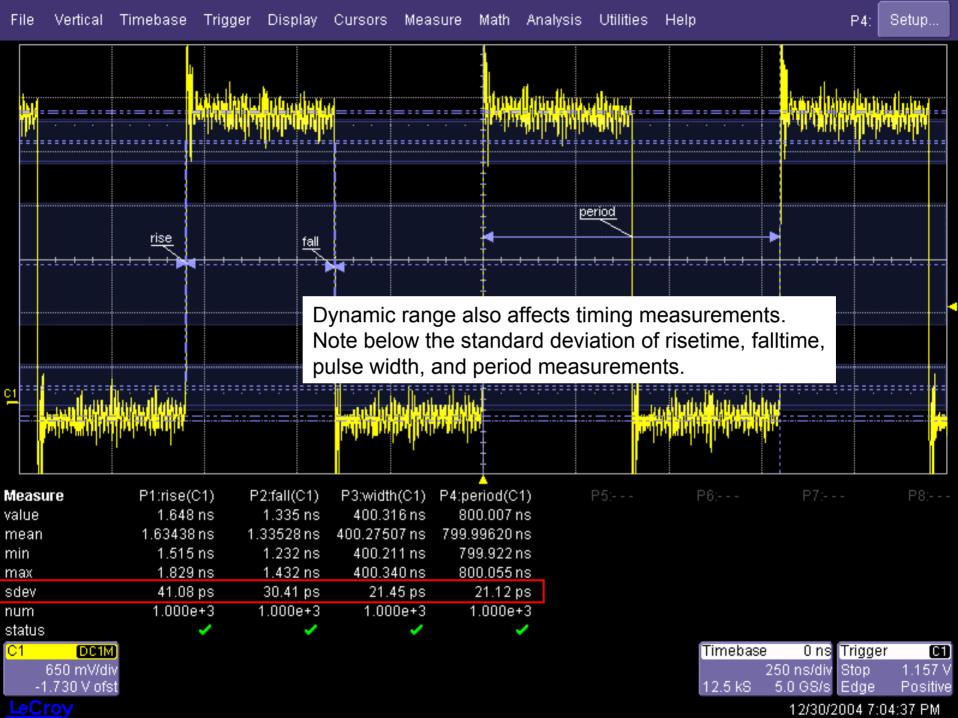






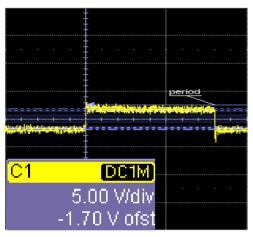
Further adjusting vertical scaling shows that channel vertical scaling significantly affects the measured noise level. At 550 mV/div, the mean noise level is **821 mV**; at 2 V/div, the noise level is **1.02 V**, and at 10 V/div, the noise level is **3.06V**. Between 2 V/div and 10 V/div, the measured noise level tripled (factor of 3x) when changing the V/div by a factor of 5x.



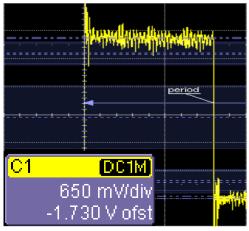




Summary: Adjusting the vertical scaling of waveforms also affects the accuracy of timing measurements.

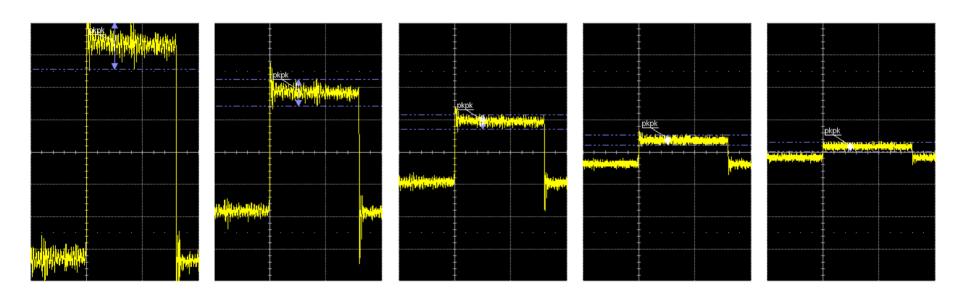


Measure	P1:rise(C1)	P2:fall(C1)	P3:width(C1)	P4:period(C1)
value	1.713 ns	1.496 ns	400.192 ns	800.121 ns
mean	1.62728 ns	1.42165 ns	400.20818 ns	799.99405ns
min	978 ps	872 ps	399.781 ns	799.552 ns
max	2.442 ns	2.181 ns	400.688 ns	800.417 ns
sdev	214.79 ps	205.31 ps	145.57 ps	134.51 ps
num	1.000e+3	1.000e+3	1.000e+3	1.000e+3



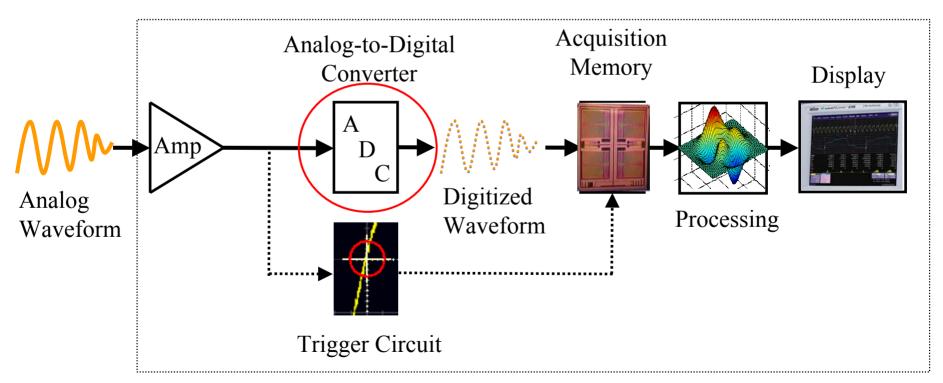
	Measure	P1:rise(C1)	P2:fall(C1)	P3:width(C1)	P4:period(C1)
The state of the s	value	1.648 ns	1.335 ns	400.316 ns	800.007 ns
	mean	1.63438 ns	1.33528 ns	400.27507 ns	799.99620 ns
-	min	1.515 ns	1.232 ns	400.211 ns	799.922 ns
	max	1.829 ns	1.432 ns	400.340 ns	800.055 ns
T. T.	sdev	41.08 ps	30,41 ps	21.45 ps	21.12 ps
	num	1.000e+3	1.000e+3	1.000e+3	1.000e+3

Why does measurement accuracy vary with changes in V/div setting?



Dynamic Range

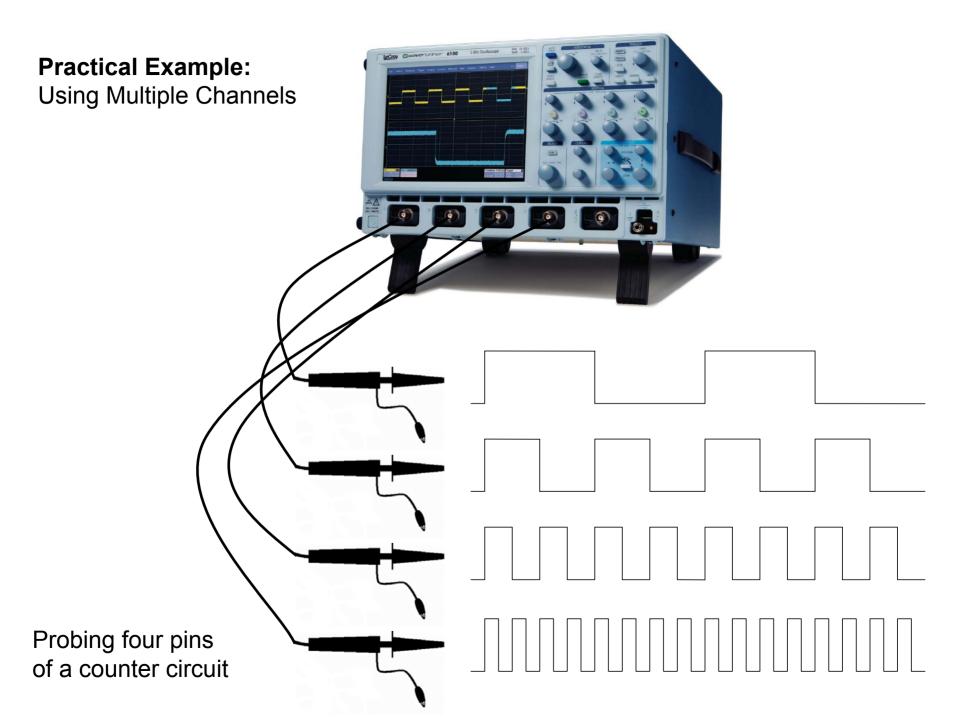
When high-performance oscilloscopes acquire an input signal, the output of the amplifier is digitized by an **8-bit** analog-to-digital converter (ADC). The dynamic range of the oscilloscope is the range of signal amplitudes that the ADC can process effectively. The minimum of the range occurs where signal power equals noise power. The maximum of the range occurs at or near full scale where maximum counts of the ADC are used while digitizing the waveform, while distortion is minimized.



Oscilloscope Block Diagram

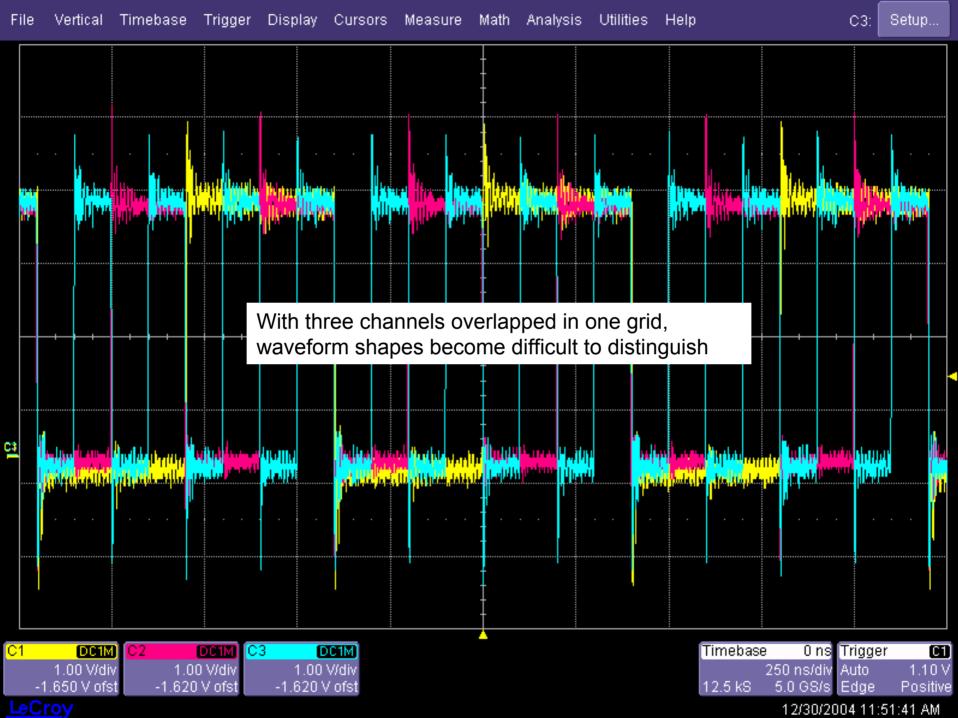
Practical Application:

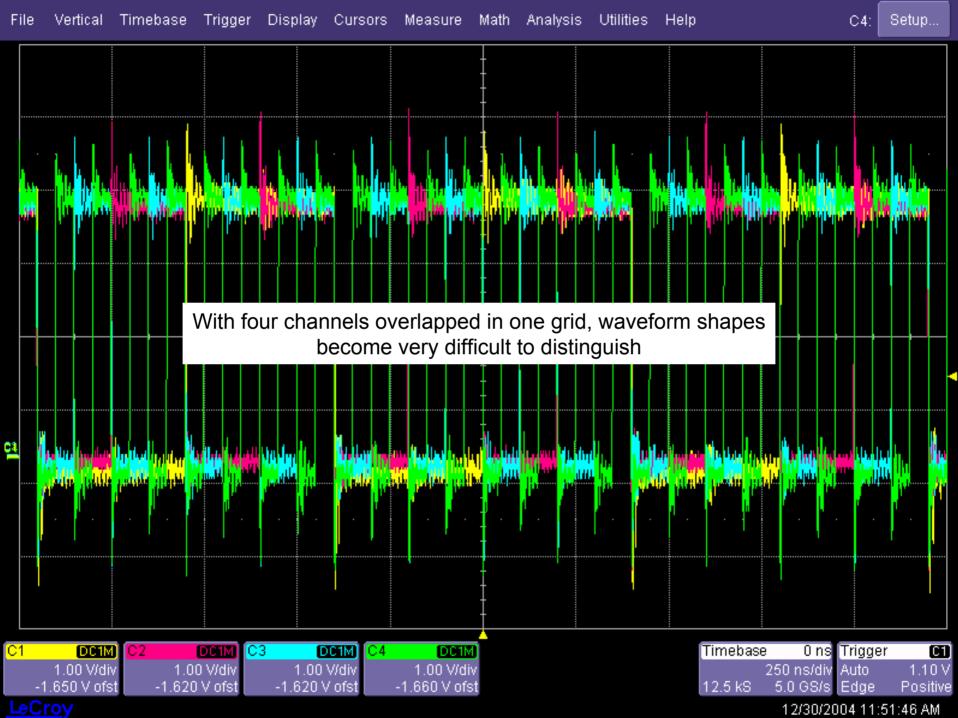
Using Multiple Channels
While Maximizing Dynamic Range



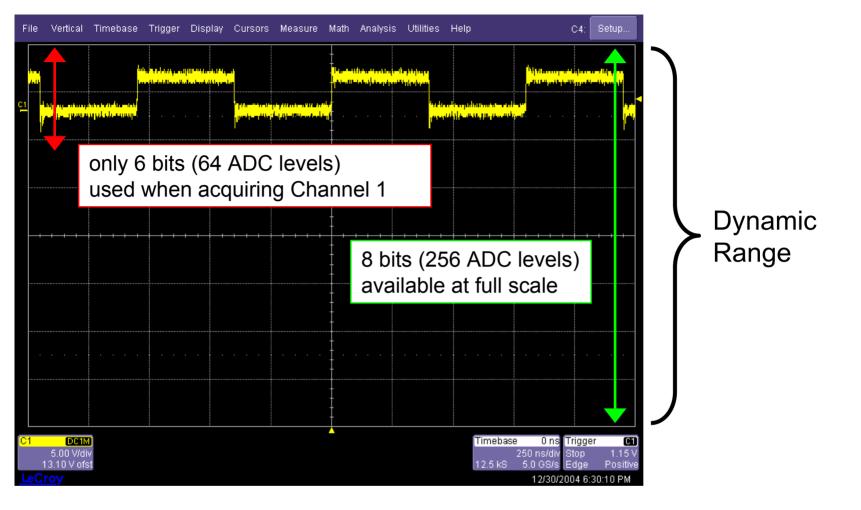






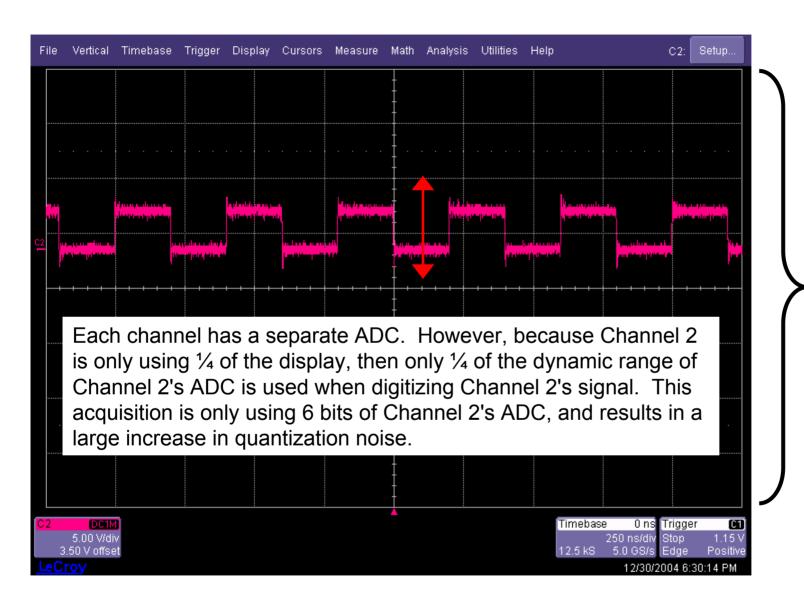






Because Channel 1 is only using ¼ of the display, only ¼ of the dynamic range of Channel 1's ADC is used when digitizing Channel 1's signal.

An 8-bit ADC has $2^8 = 256$ quantization levels. When using $\frac{1}{4}$ of the dynamic range, only a maximum of 64 of the 256 quantization levels are used for acquiring Channel 1. Using $\frac{1}{4}$ of quantization levels results in a maximum of 6-bit resolution on the acquired channel ($2^6 = 64$). This loss of resolution causes an increase in quantization noise.

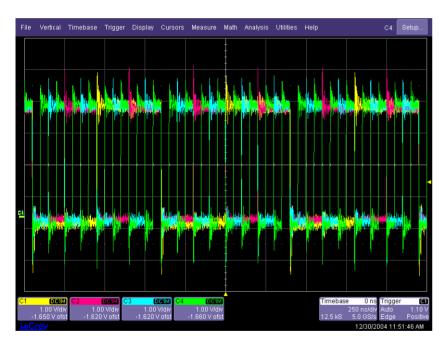


Dynamic Range

The same applies to Channels 3 and 4. When vertical scaling is reduced to fit four signals into the same grid, then each channel is only using $\frac{1}{4}$ of its dynamic range, which results in loss of vertical resolution and the addition of significant quantization noise.



How can the compromise between maximizing dynamic range, and clearly viewing multiple signals, be resolved?

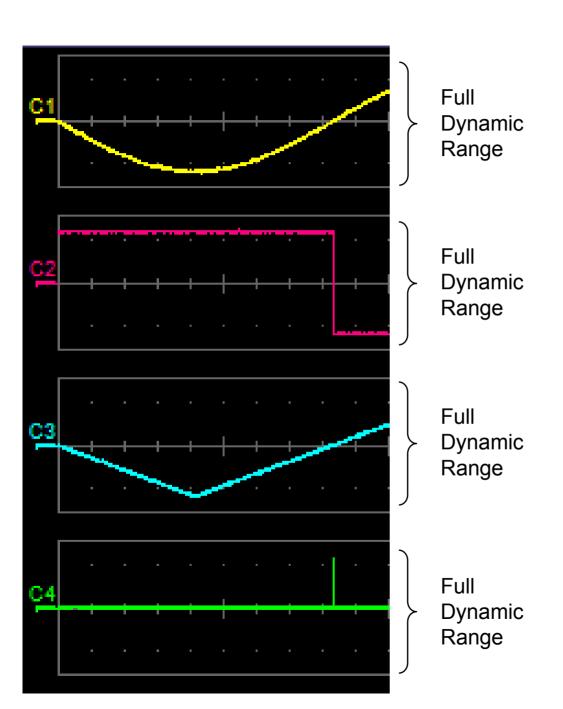


Maximize dynamic range



Clearly view all signals

Solution: Multigrid Displays

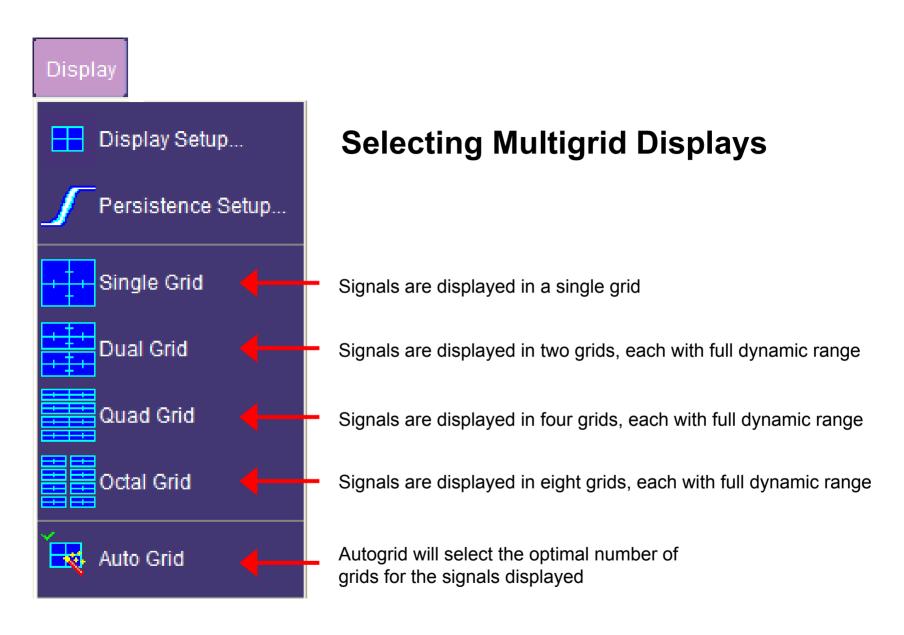


Multigrid Displays

Using Multigrid displays, each channel is contained within a separate grid.

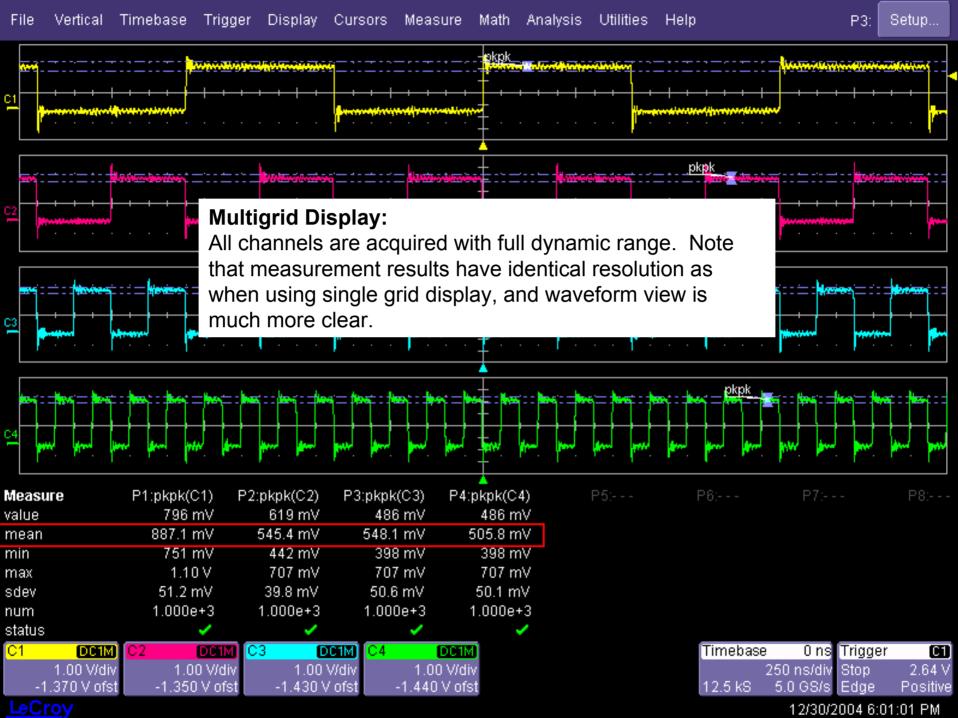
Note that Channel 1 is fully contained within an independent grid that contains the full dynamic range of Channel 1's ADC.

The same is true for Channels 2, 3, and 4. Using Multigrid, dynamic range can be optimized while all signals are clearly viewed.



Multigrid displays eliminate the compromise between clearly viewing multiple channels and maximizing dynamic range.

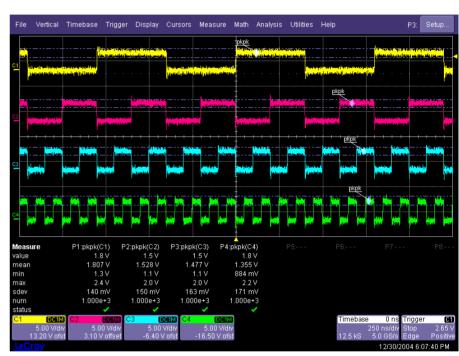




Comparing Single Grid and Multigrid:

Shown below, the difference in noise level is apparent when acquiring the identical signal using Single Grid and Multigrid settings. The dynamic range improvement of Multigrid significantly reduces quantization noise.





Multigrid Display

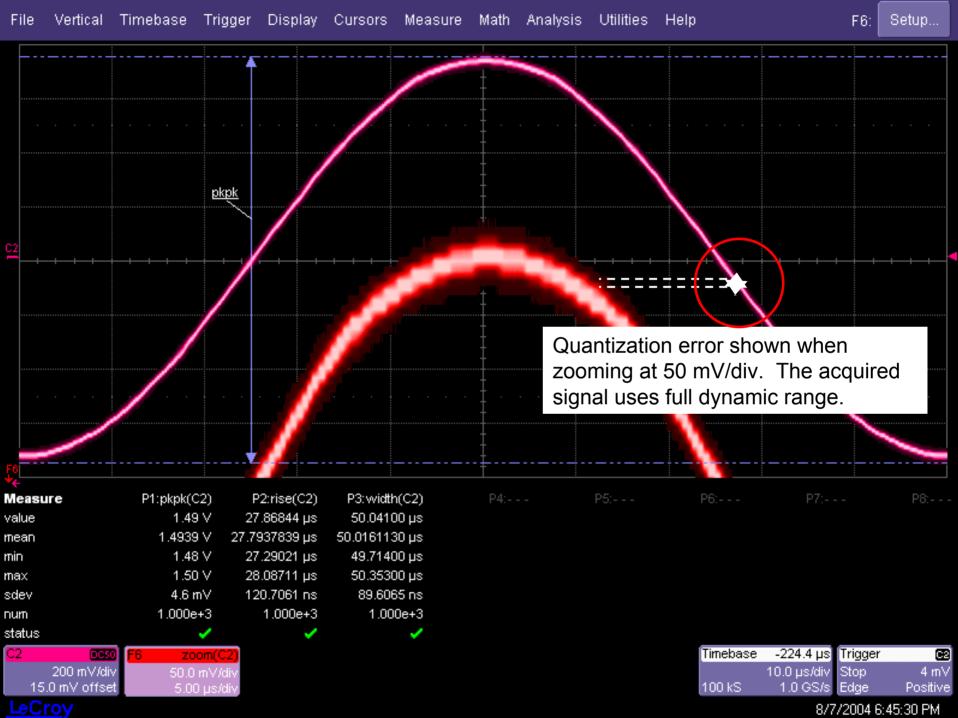
Measure	P1:pkpk(C1)	P2:pkpk(C2)	P3:pkpk(C3)	P4:pkpk(C4)
value	796 mV	619 mV	486 mV	486 mV
mean	887.1 mV	545.4 mV	548.1 mV	505.8 mV
min	751 mV	442 mV	398 mV	398 mV
max	1.10 V	707 mV	707 mV	707 mV
sdev	51.2 mV	39.8 mV	50.6 mV	50.1 mV
num	1.000e+3	1.000e+3	1.000e+3	1.000e+3
status	/	V	>	V

Single grid display

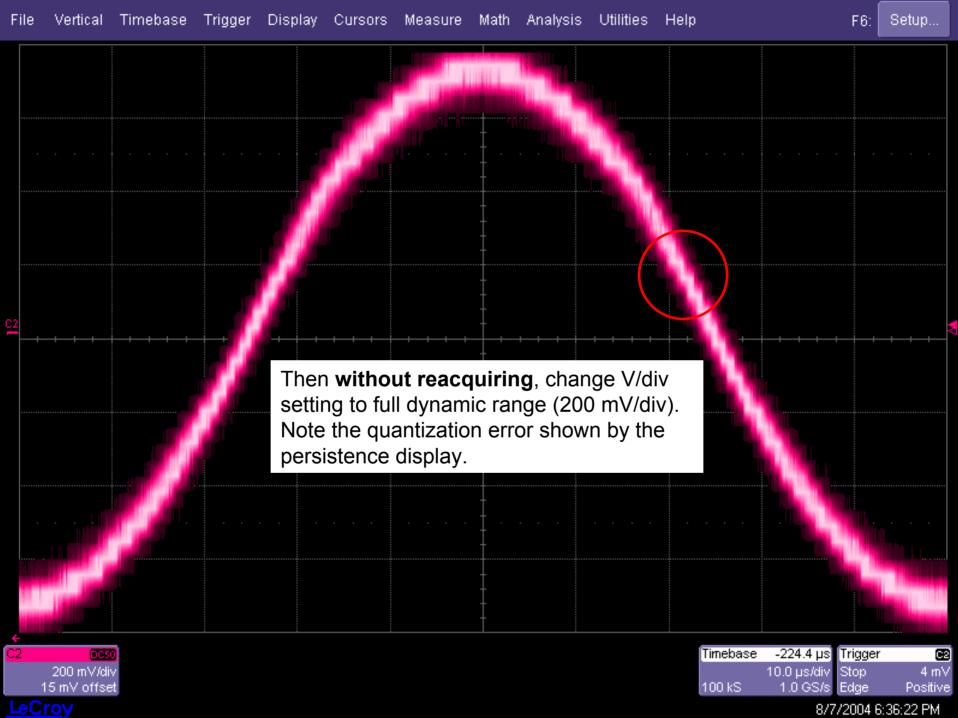
Measure value	P1:pkpk(C1) 1.8 V	P2:pkpk(C2) 1.5 V	P3:pkpk(C3) 1.5 V	P4:pkpk(C4) 1.8 V
mean	1.807 V	1.528 V	1.477 V	1.355 V
min	1.3 V	1.1 V	1.1 V	884 mV
max	2.4 V	2.0 V	2.0 V	2.2 V
sdev	140 mV	150 mV	163 mV	171 mV
num	1.000e+3	1.000e+3	1.000e+3	1.000e+3
status	✓	~	~	~

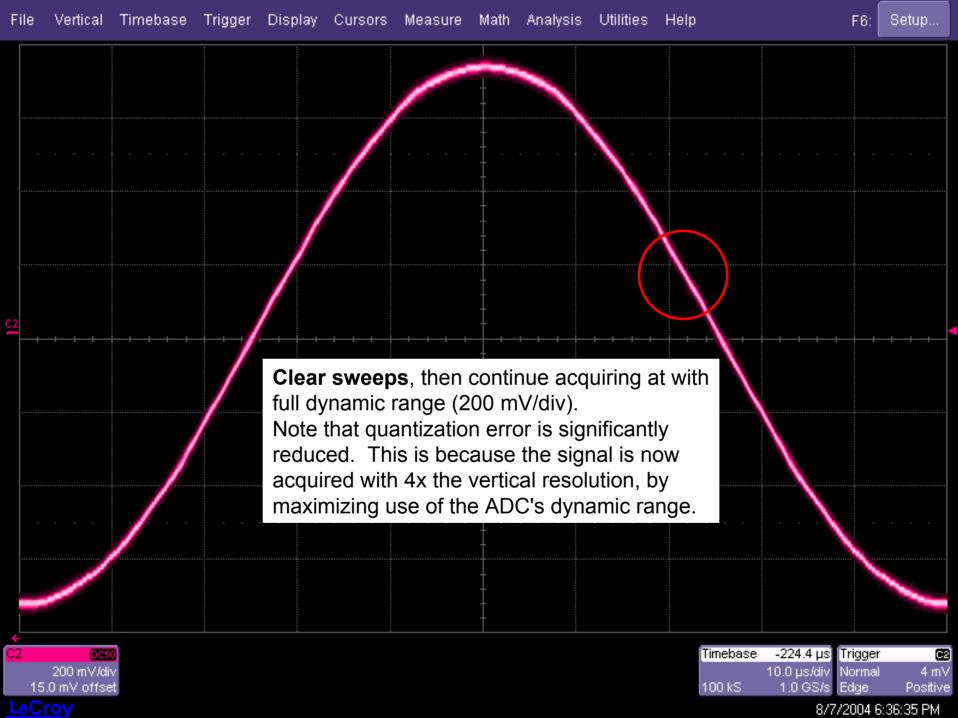
Viewing Quantization Noise

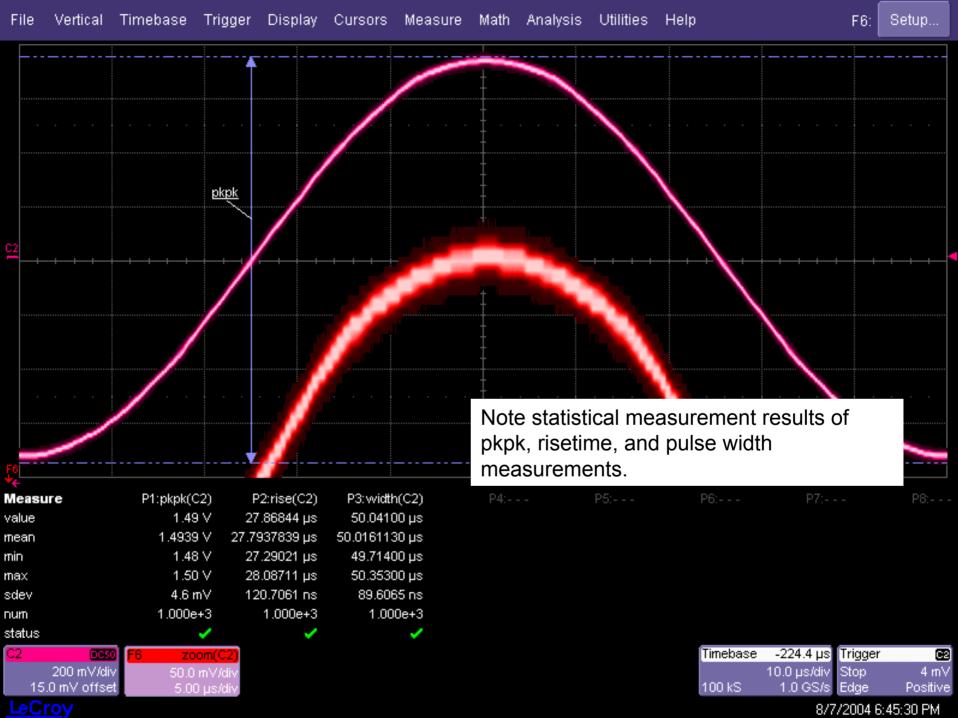


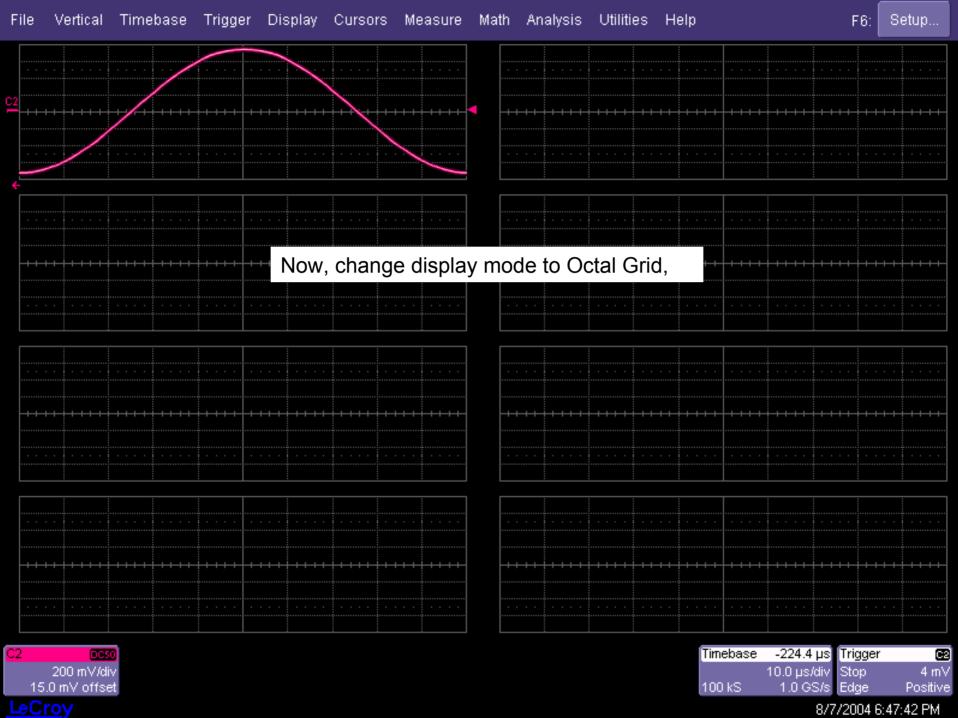




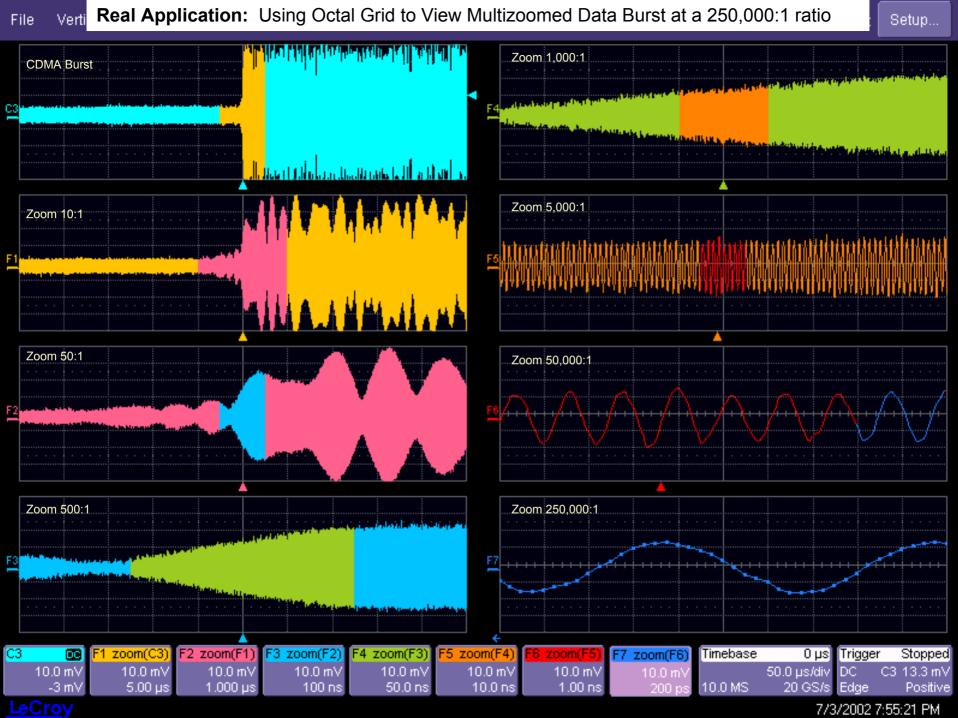








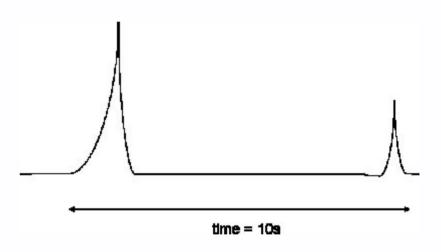




Reference Slides

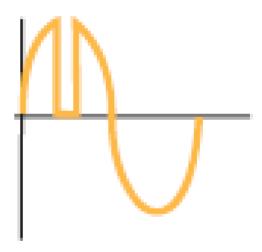
Transient Testing (Automotive)

- Pulse Characteristics
 - Capacitive load dump
 - Inductive kickback/spike (back EMF from motor turn off)
- Measurement Needs
 - Capture Time longer the better:
 - Relay bounce (μs to ms)
 - Transient time
 - μs (motor)
 - ns (FET switch)
 - Measure 50-100 MHz transient
 - 10s capture = 2Mpts at 100 MHz Sample Rate



Dropout and Interrupt Testing

- Monitor AC or DC voltage line with oscilloscope during EMC testing
- Verify that dropout or interrupt occurred, and that device under test was unaffected



Surge Testing

Pulse Characteristics

- T_{rise} = typically 1.2 to 10 μ s
- $-T_{fall}$ = typically 20 to 10,000µs
- Measurement Needs
 - Capture a Single Pulse
 - Measure one pulse, verify rise and fall time



















