

Stability Analysis of Linear and PWM Power Amplifiers

Presented By:
Aniruddha Kolarkar
520-690-7742
AK@apexanalog.com



Topics of Discussion

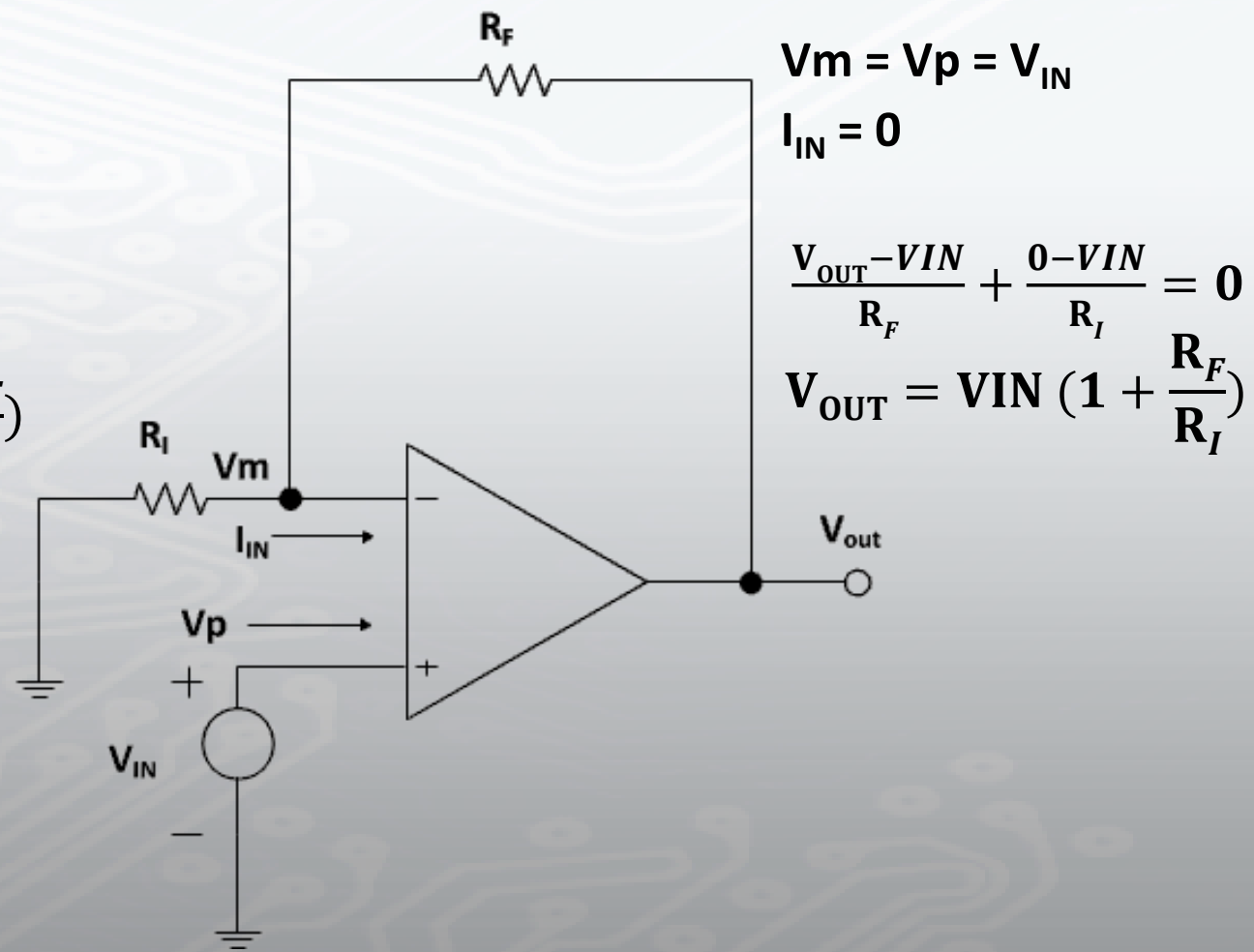
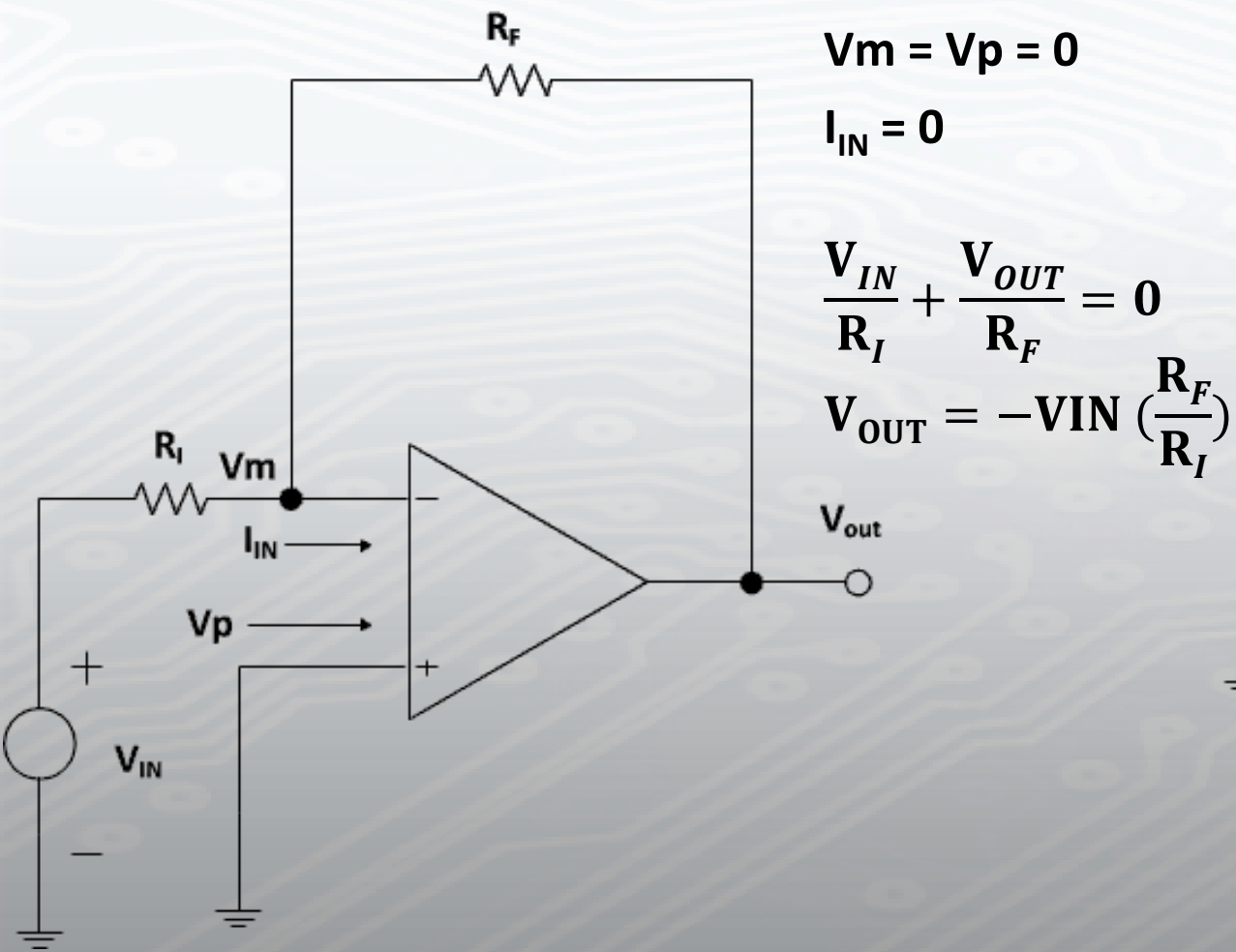
- Operational Amplifiers
- PWM Amplifiers
- Stability Analysis
- Real Life Stability Tests
- Conclusion



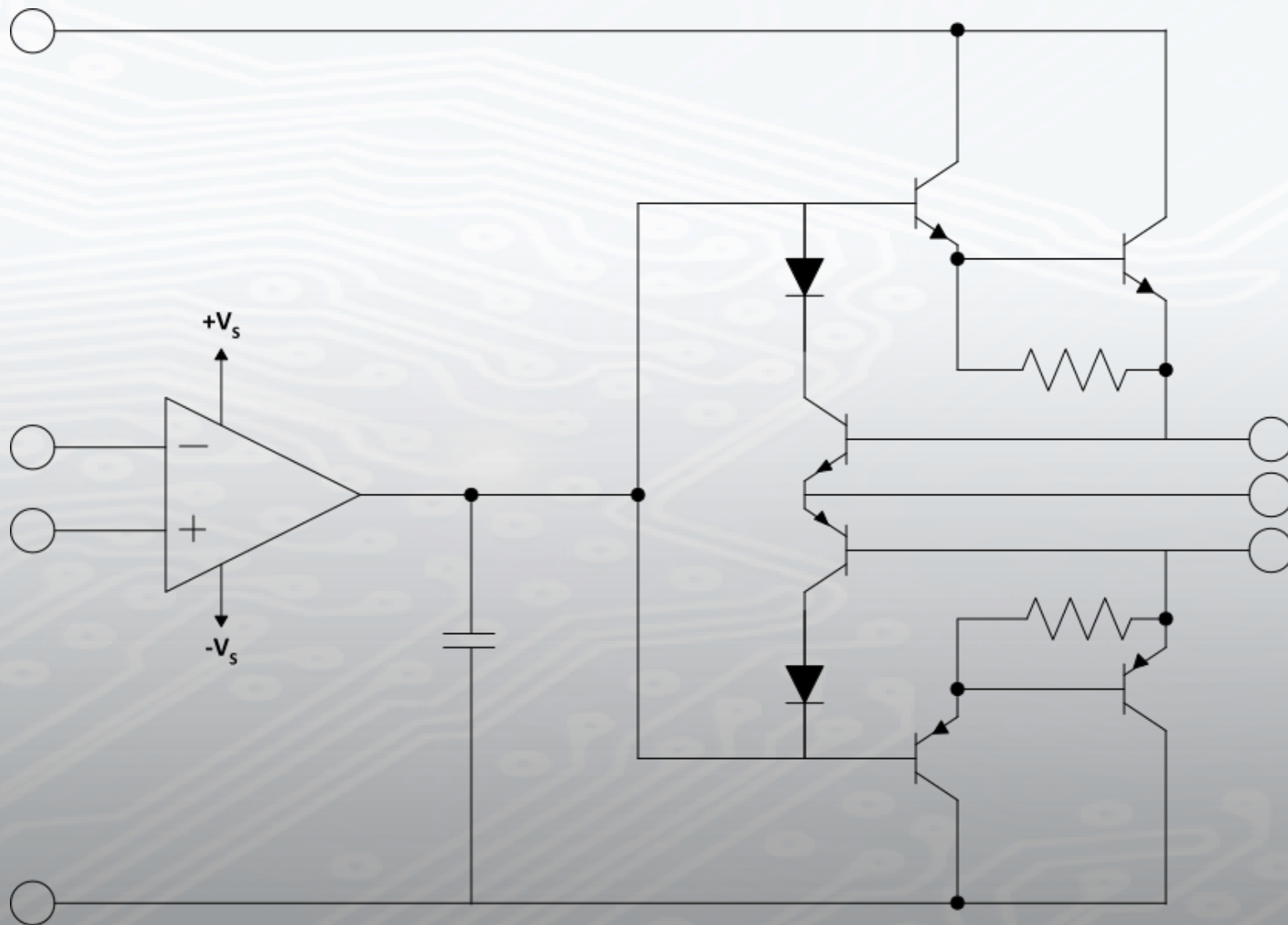
Op Amps



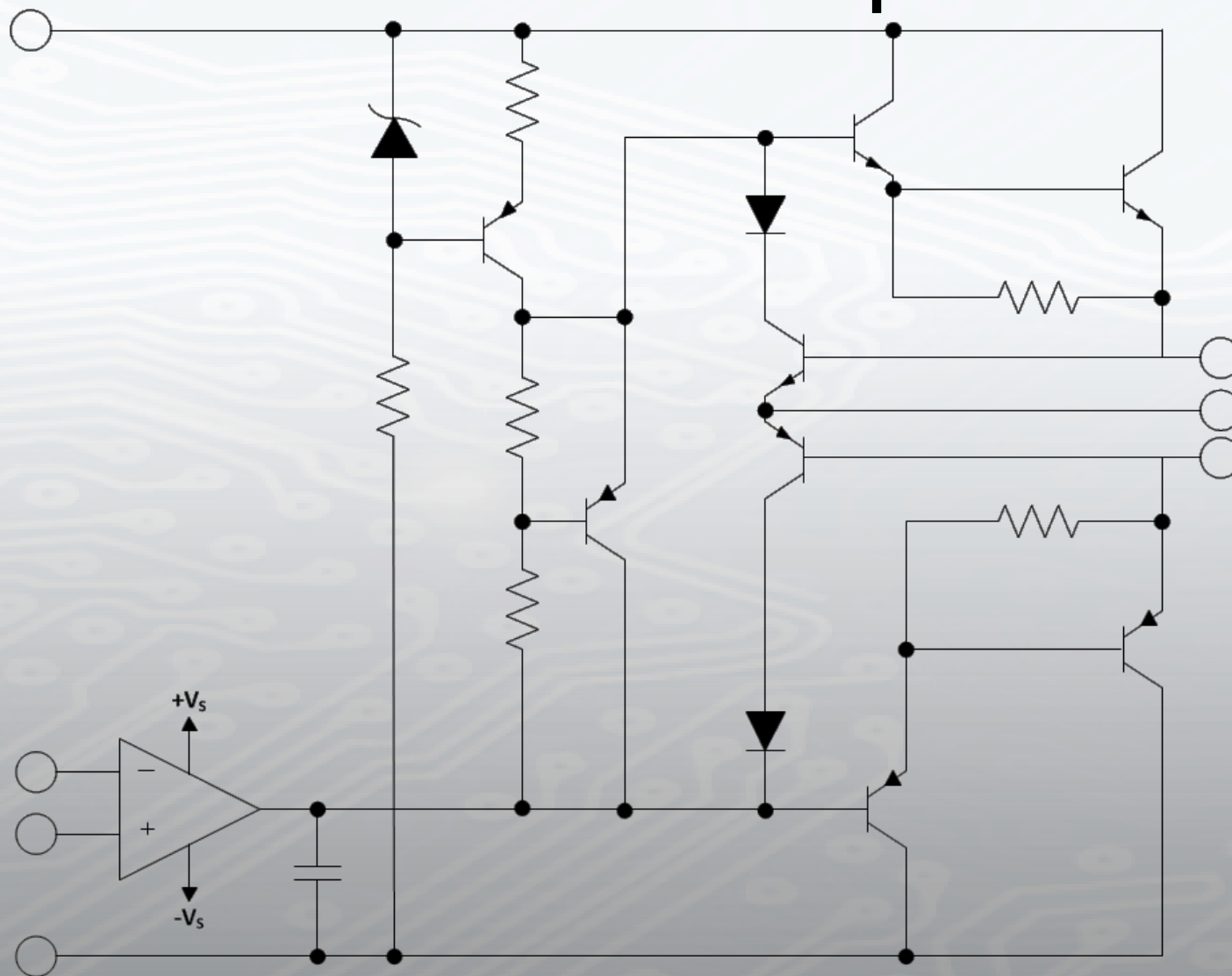
Linear Operation Basics



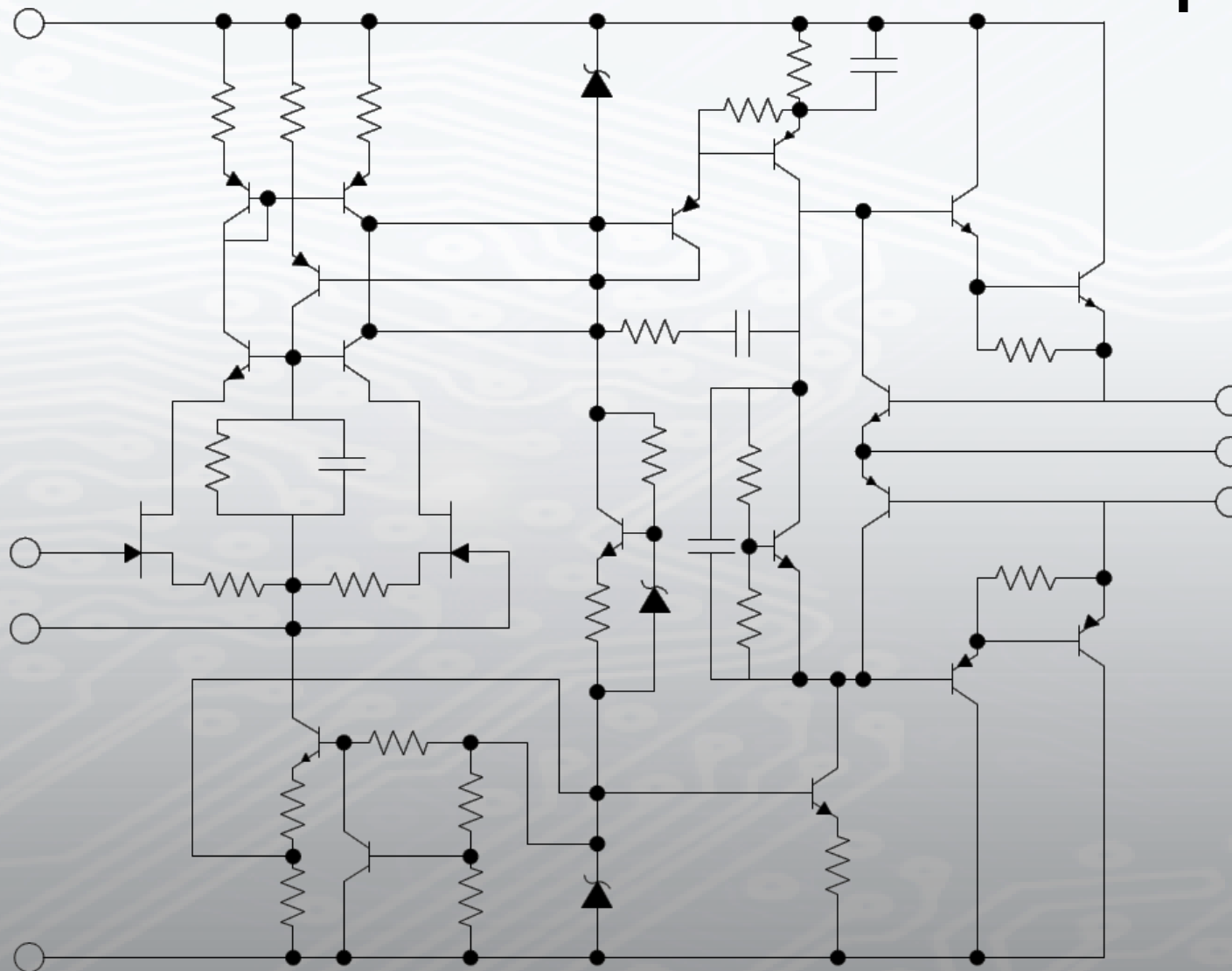
Class C Output Stages



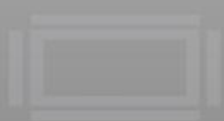
Simple Class AB Outputs



Complex Amplifier

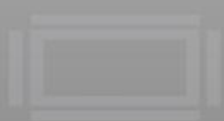


PWM Amplifiers

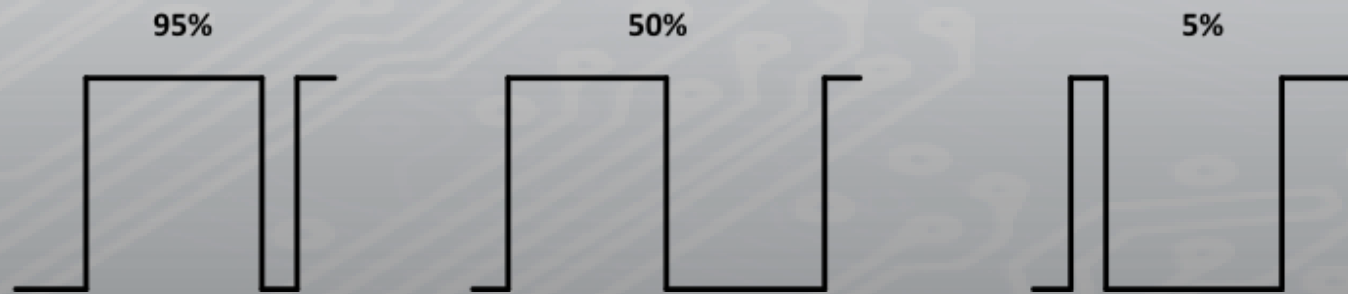
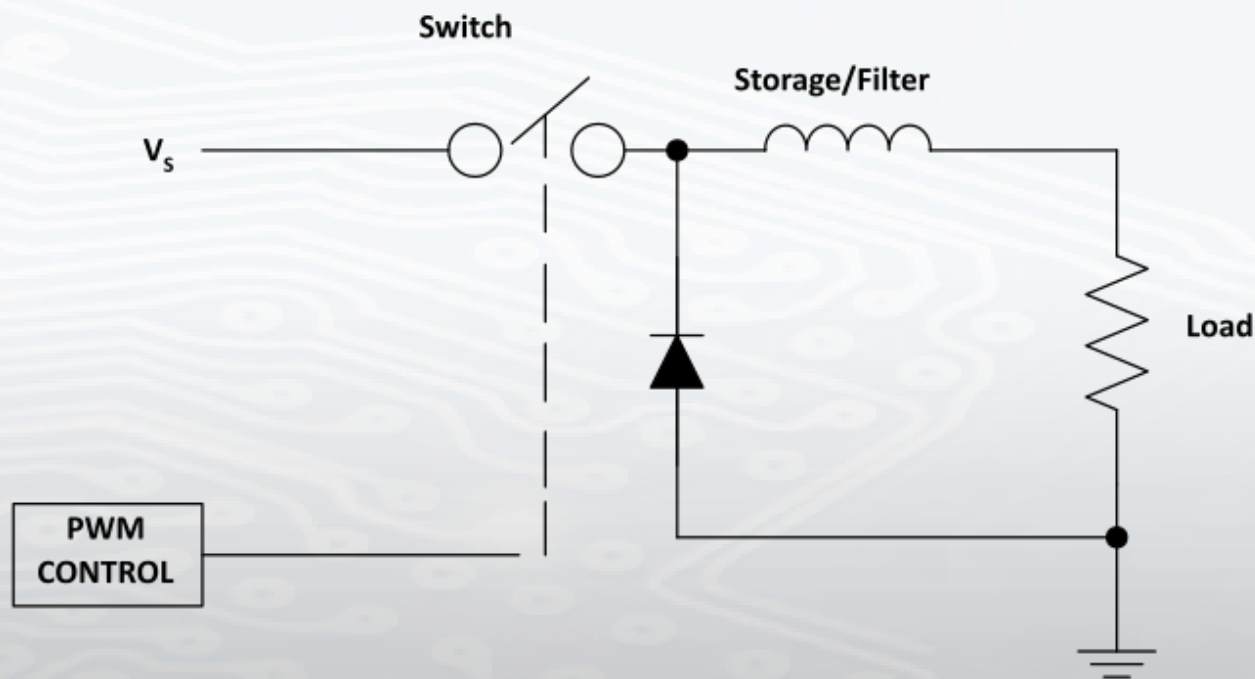


Pulse Width Modulation

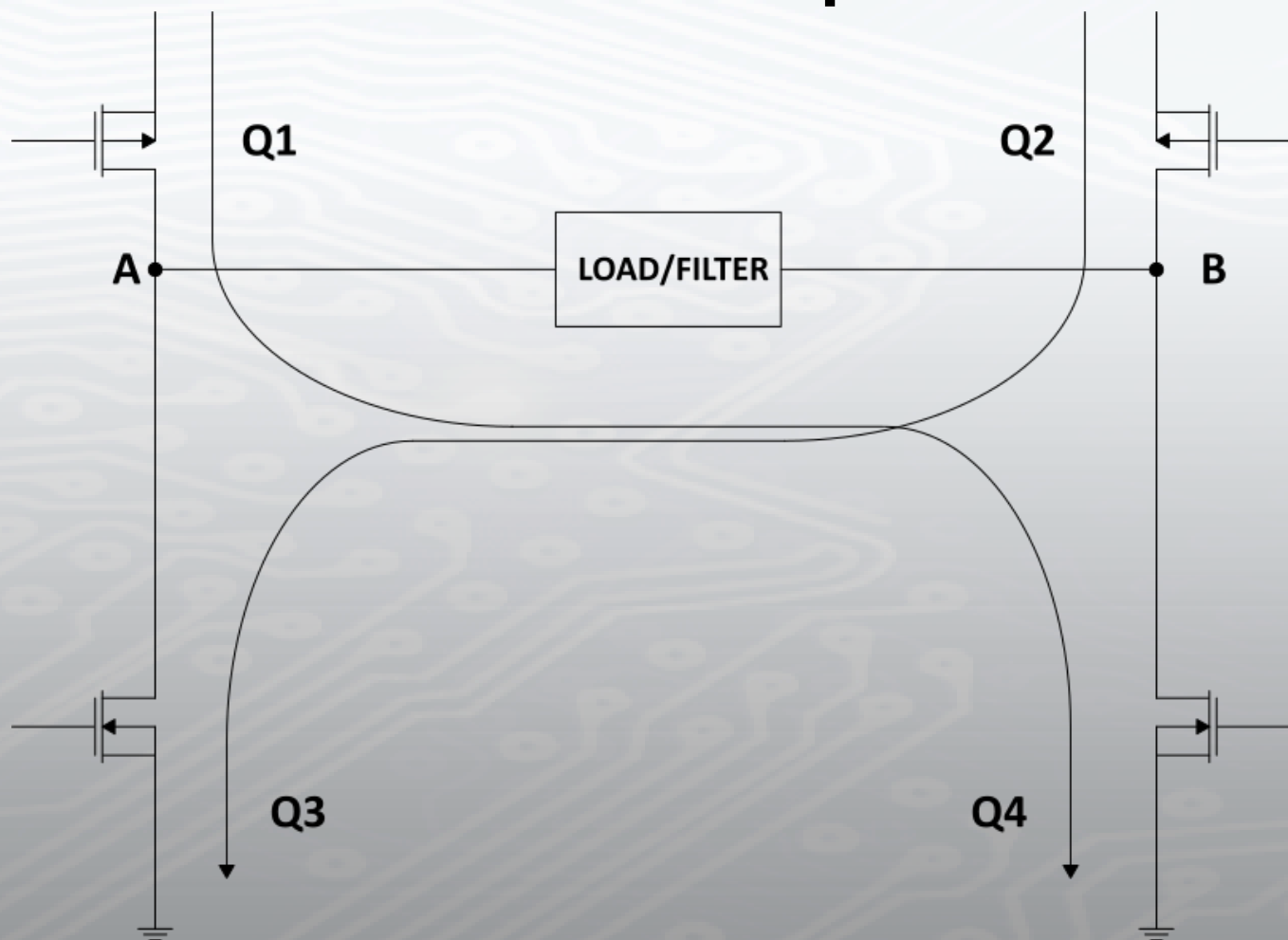
- More Work
- Less Waste



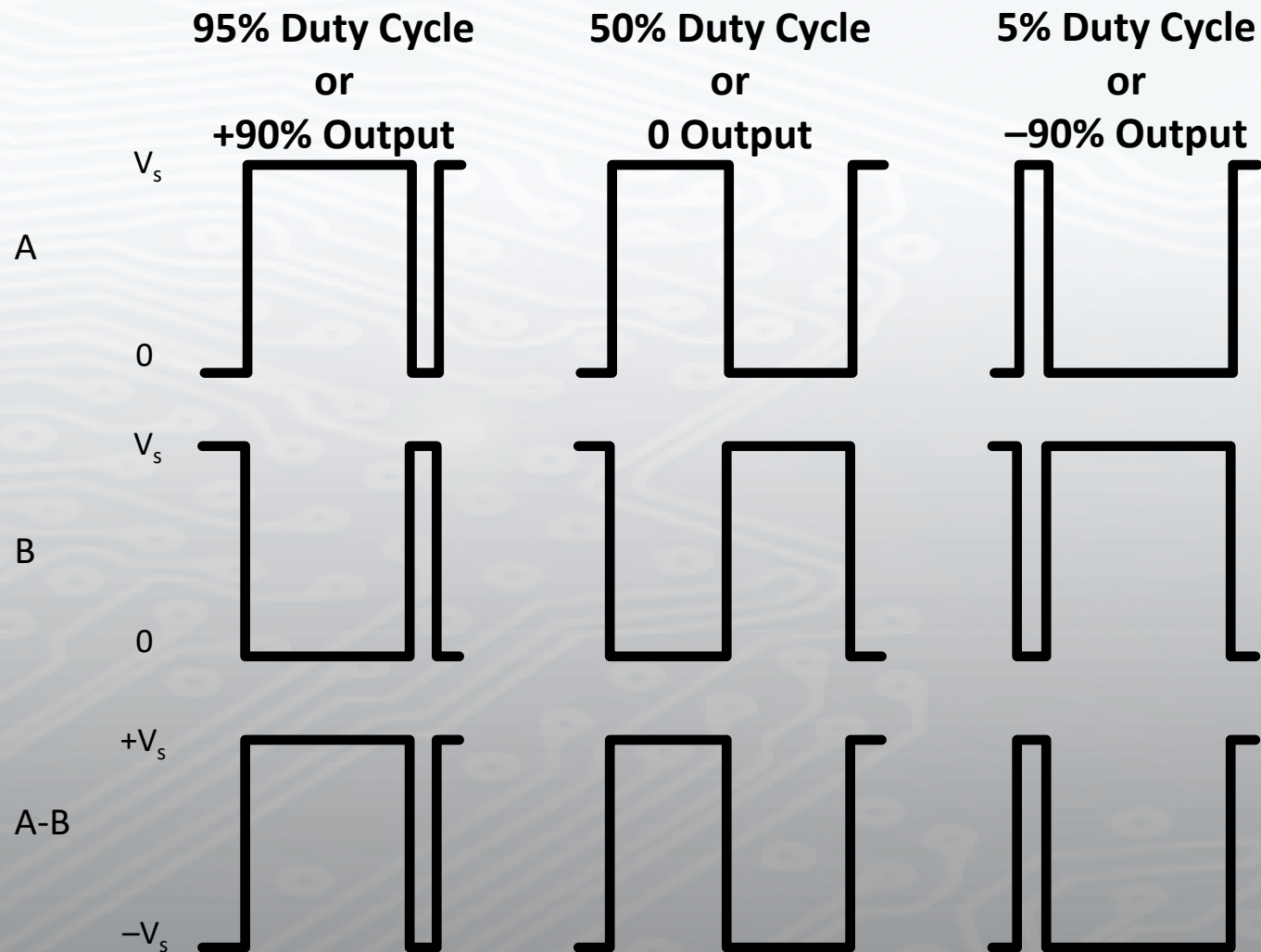
PWM Power Delivery



H-Bridge Providing Bipolar Output from a Single Supply



H-Bridge Waveforms

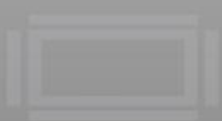
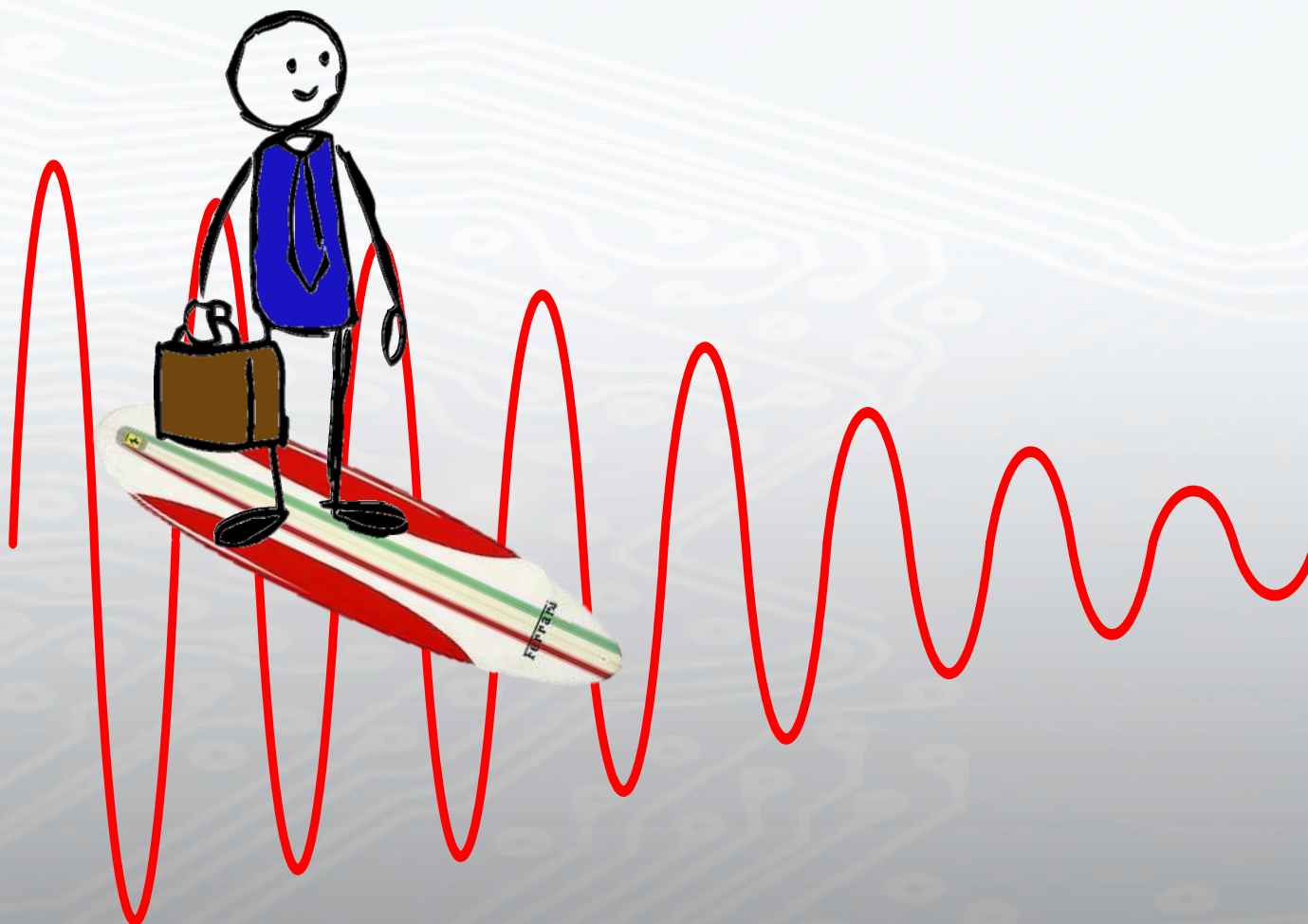


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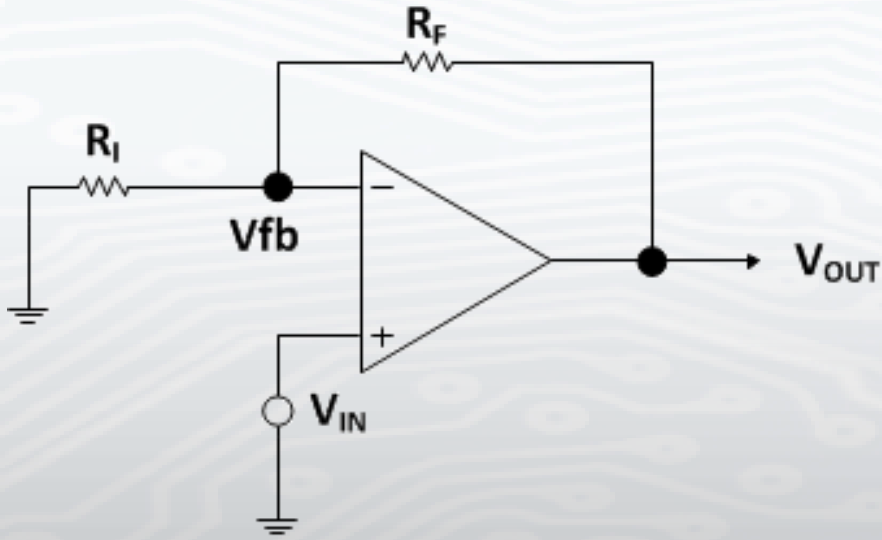
STABILITY & COMPENSATION



Loop Stability

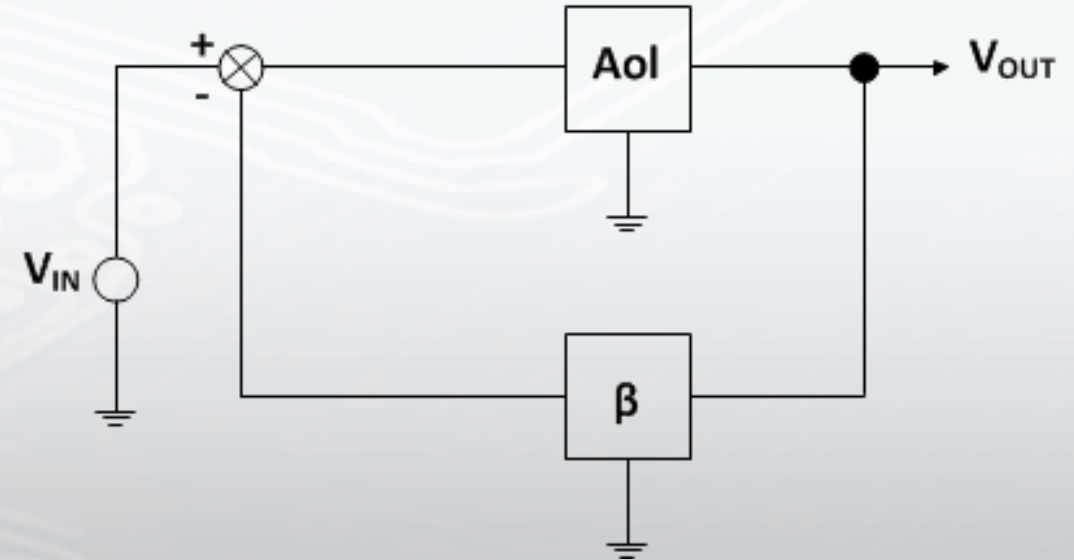


Beta (β) – Feedback Factor



$$\beta = \frac{R_i}{R_i + R_f}$$

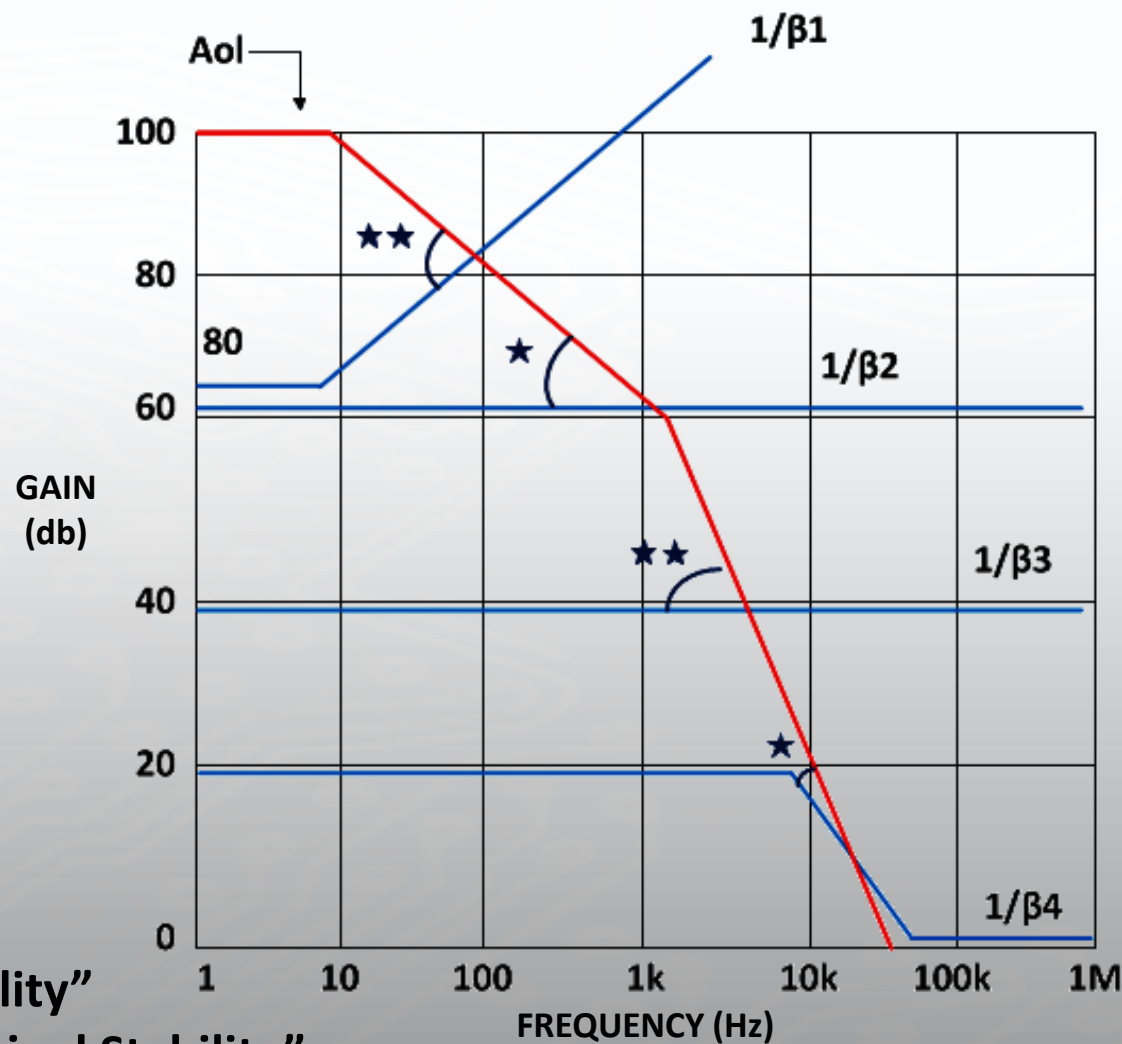
$$V_{fb} = \beta V_{out}$$



$$\frac{V_{out}}{V_{in}} = \frac{A_{ol}}{1 + A_{ol}\beta} = \frac{1}{\beta}$$

(For $A_{ol} \beta \gg 1$)

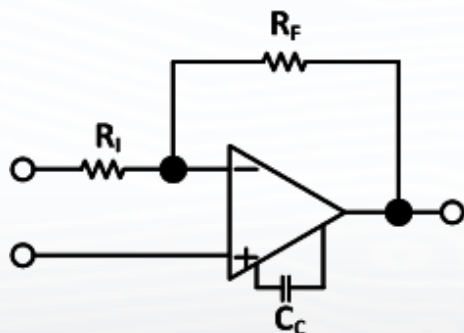
Rate of Closure & Stability



*20db/ decade Rate of Closure → “Stability”

**40db/ decade Rate of Closure → “Marginal Stability”

External Phase Compensation

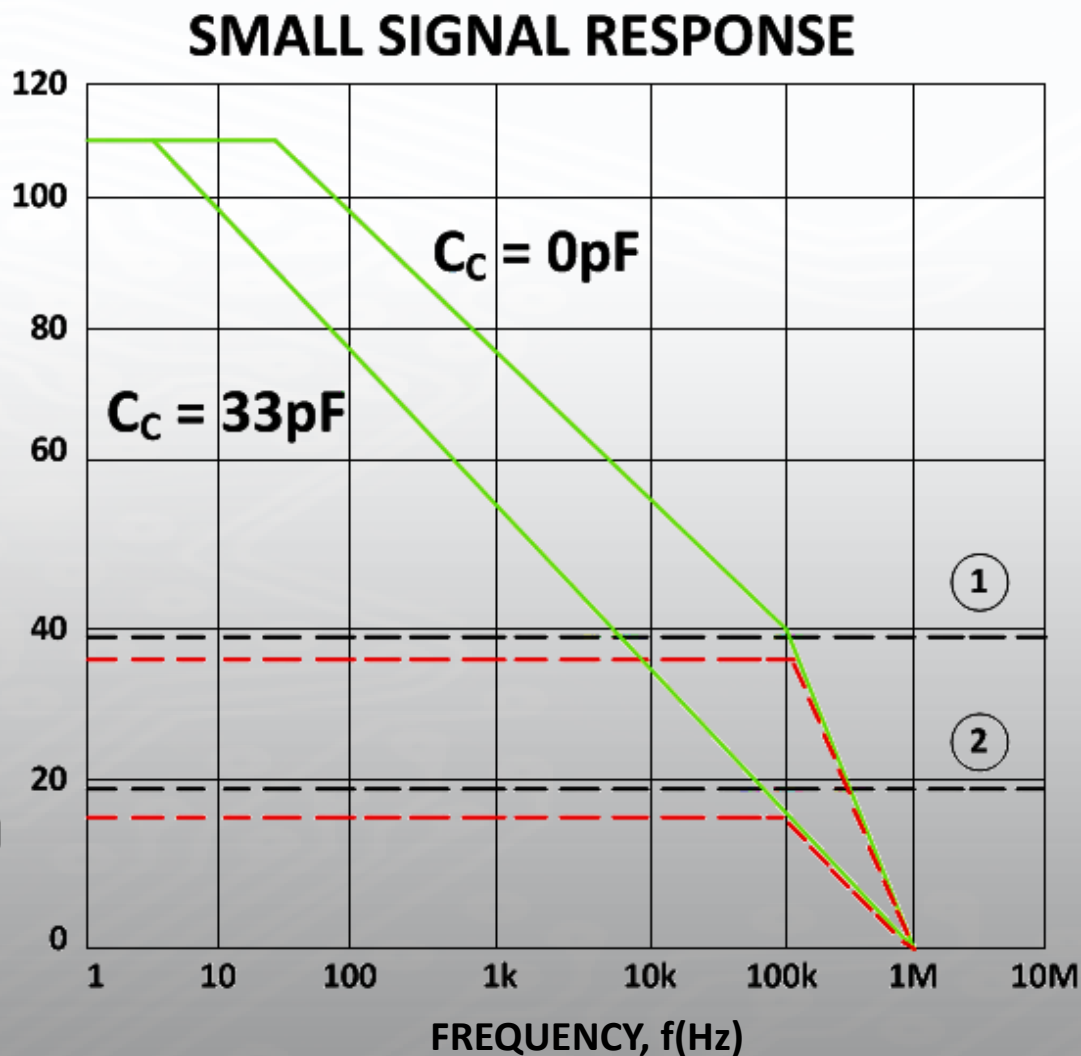


OPEN
LOOP
GAIN
 $A_{oL}(\text{dB})$

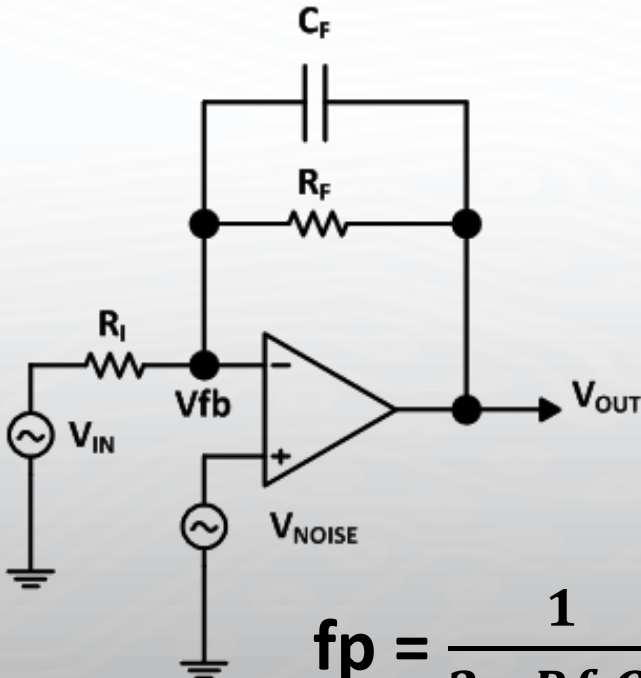
① Stable for any C_C

② Unstable for $C_C = 0\text{pF}$

— A_{oL}
- - - $1/\beta$
- - - AV_{cl}



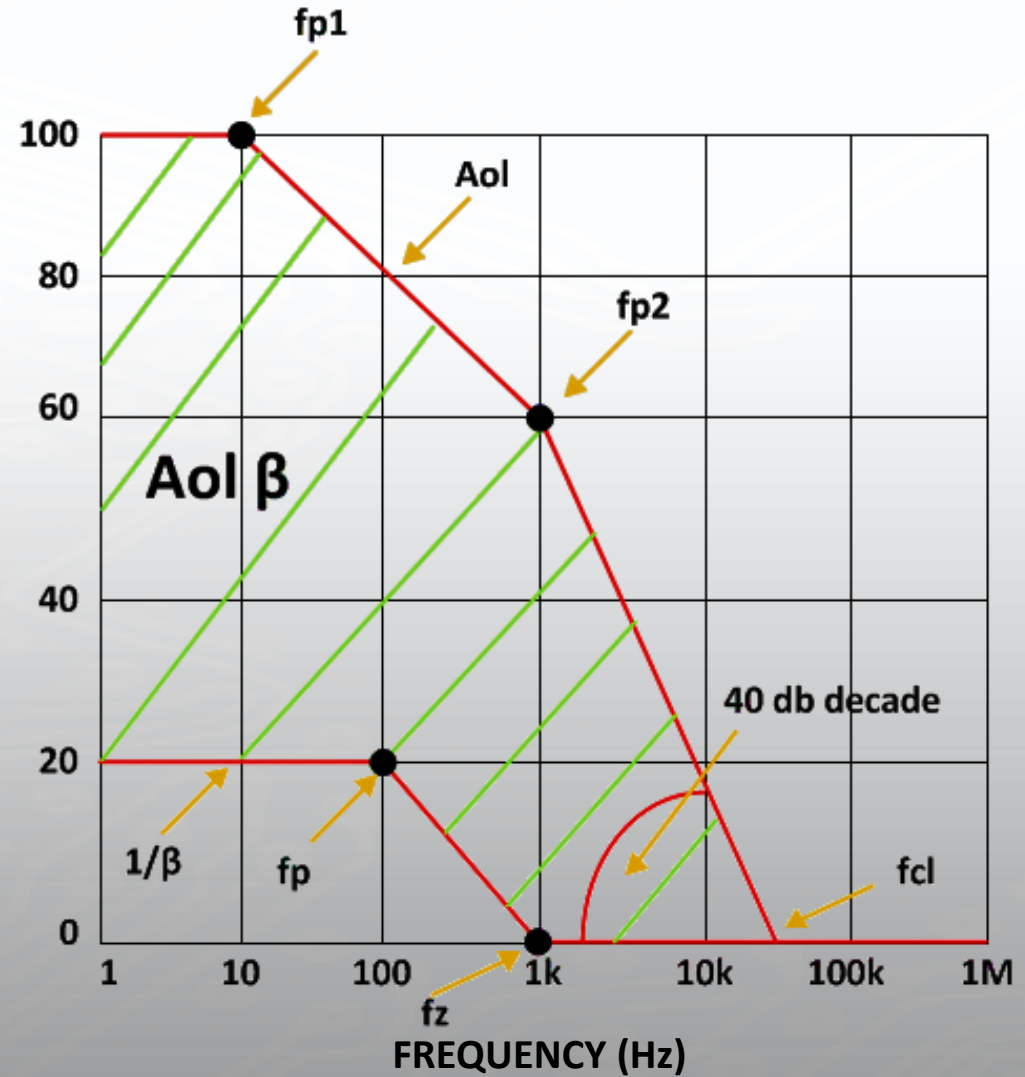
Stability – Rate of Closure



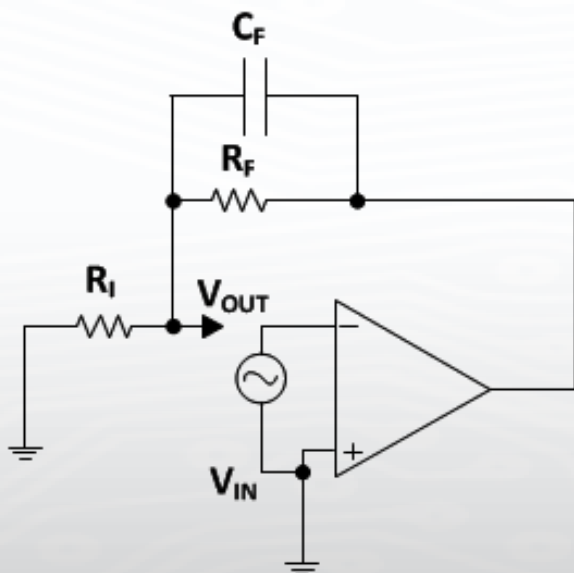
$$f_p = \frac{1}{2\pi R_f C_f}$$

$$f_z = \frac{R_i + R_f}{2\pi R_i R_f C_f}$$

GAIN (db)

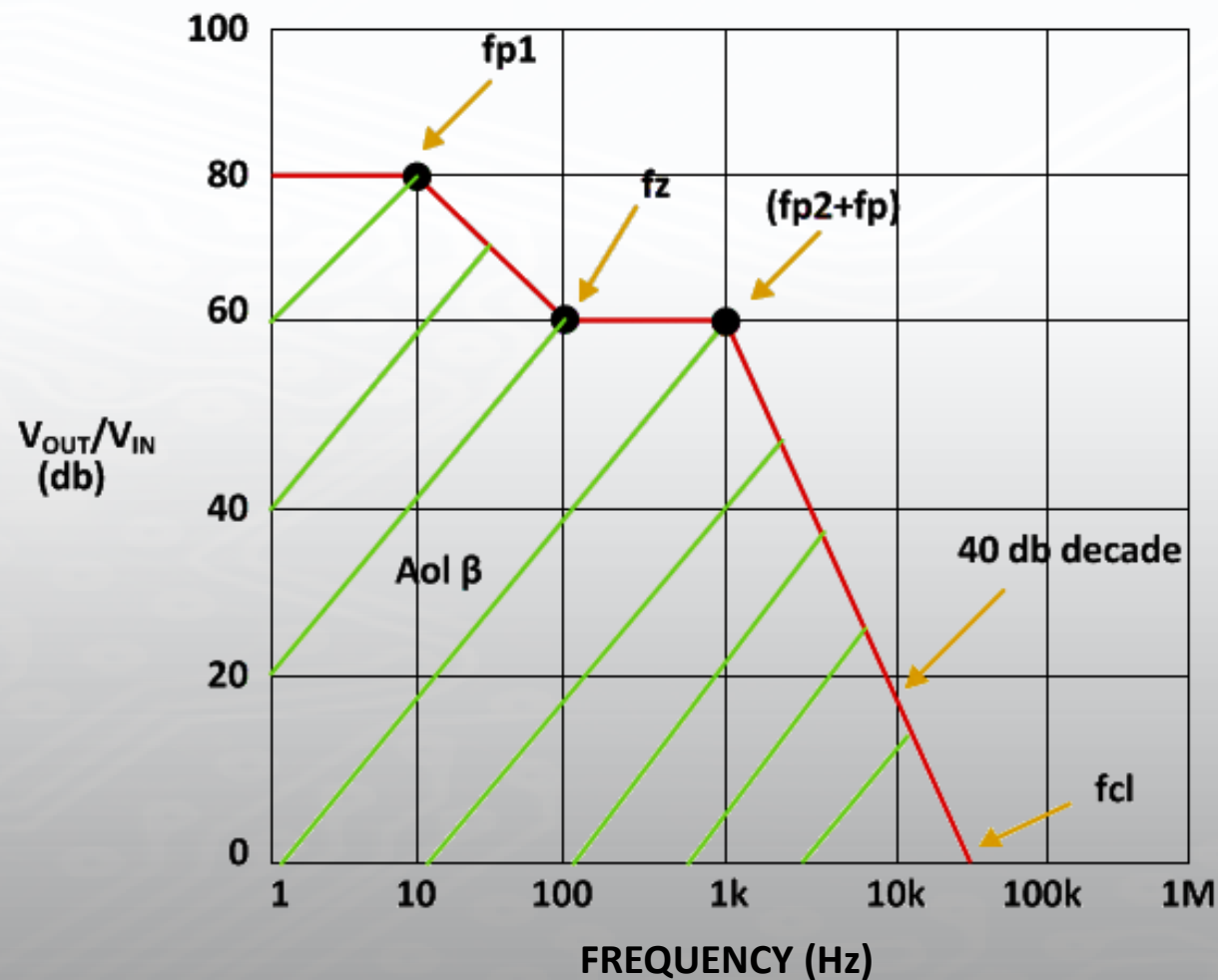


Stability – Open Loop

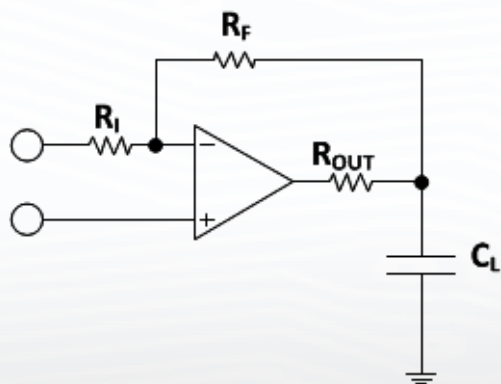


$$f_z = \frac{1}{2\pi R_f C_f}$$

$$f_p = \frac{R_i + R_f}{2\pi R_i R_f C_f}$$



Capacitive Loading



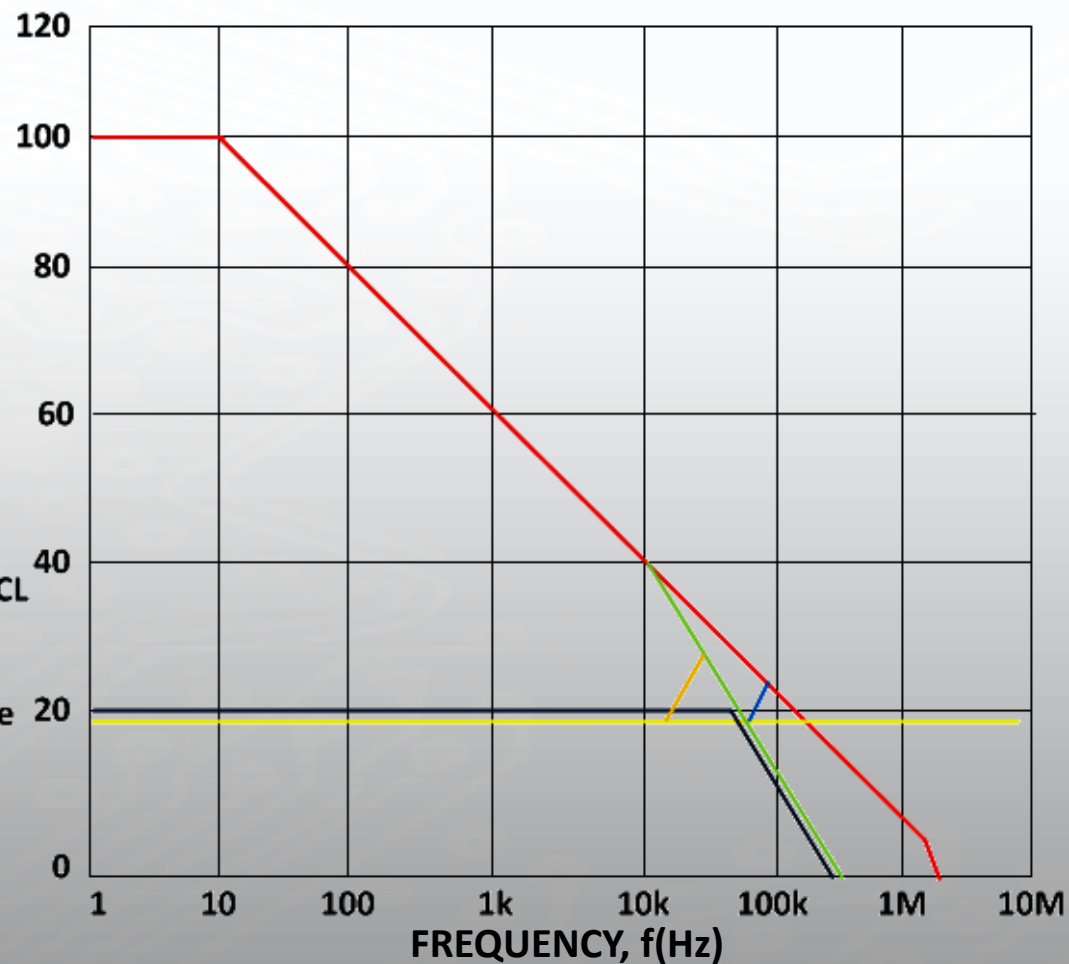
Unity Gain
Stable Amplifier

Unstable
40db/decade
with CL

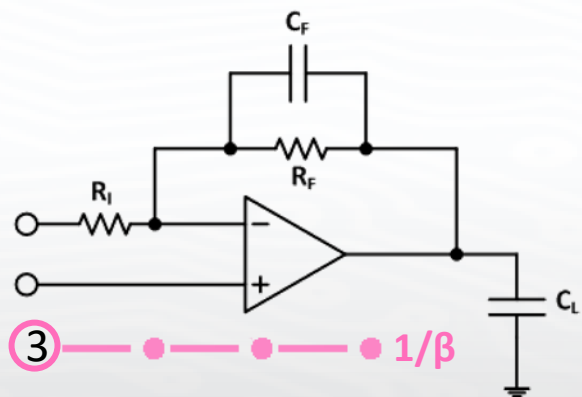
OPEN
LOOP
GAIN
Aol (db)

- Aol
- Aol w/ CL
- 1/β
- Acl
- Stable
- Unstable

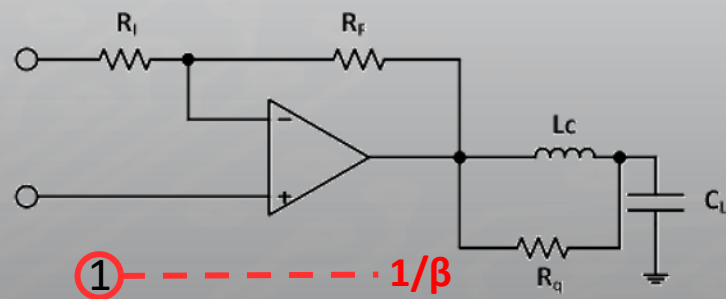
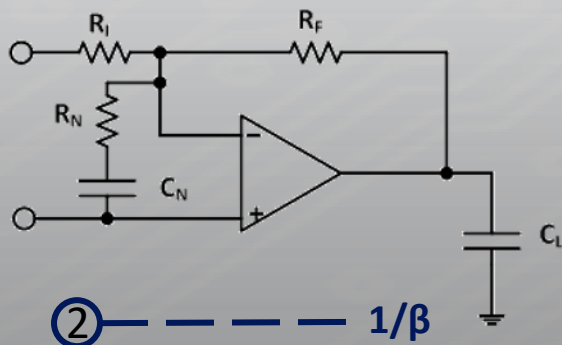
SMALL SIGNAL RESPONSE



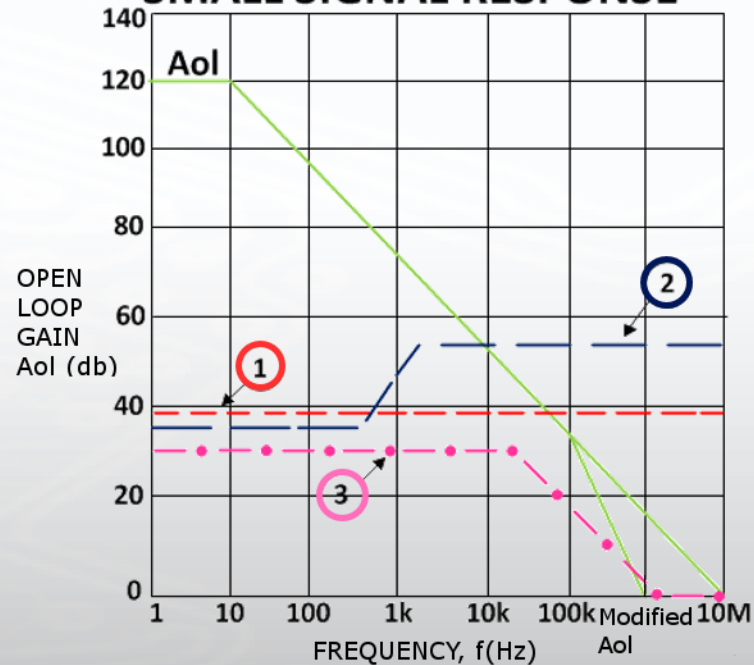
Capacitive Load Compensation



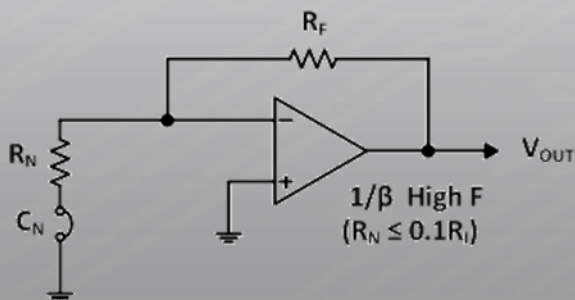
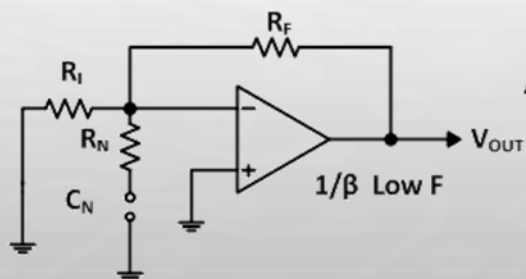
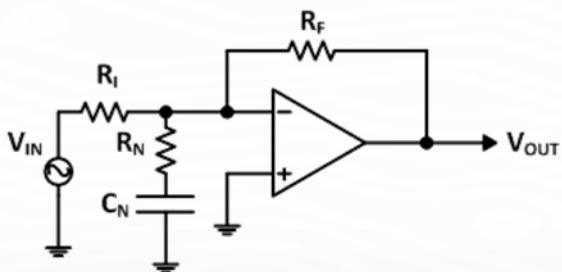
REQUIRES UNITY
GAIN STABLE AMPLIFIER



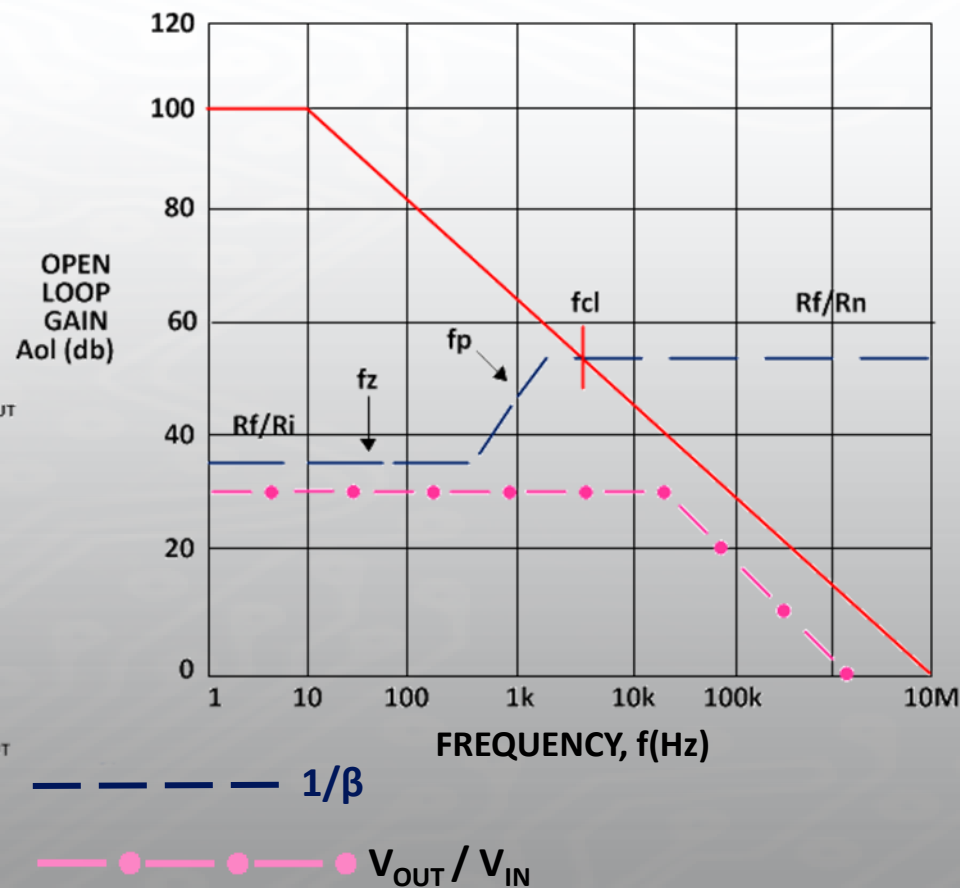
SMALL SIGNAL RESPONSE



Noise-Gain Compensation



SMALL SIGNAL RESPONSE



PowerDesign Cload

Inverting Mode

☒ Inverting
 ☐ Non-Inverting

Output Type

☒ Voltage Gain
 ☐ Current Gain

☐ Composite
 ☐ Power Booster

PA07

☒ Capacitive Load - Voltage Gain
 ☐ Inductive Load - Current Gain

C_LOAD

4u

F

R_ISO

short

Ω

R_IN

2k

Ω

R_FB

20k

Ω

C_FB

open

F

R_NG

open

Ω

C_NG

open

F

R_CL

0.4

Ω

C_COMP

0

pF

View Schematic

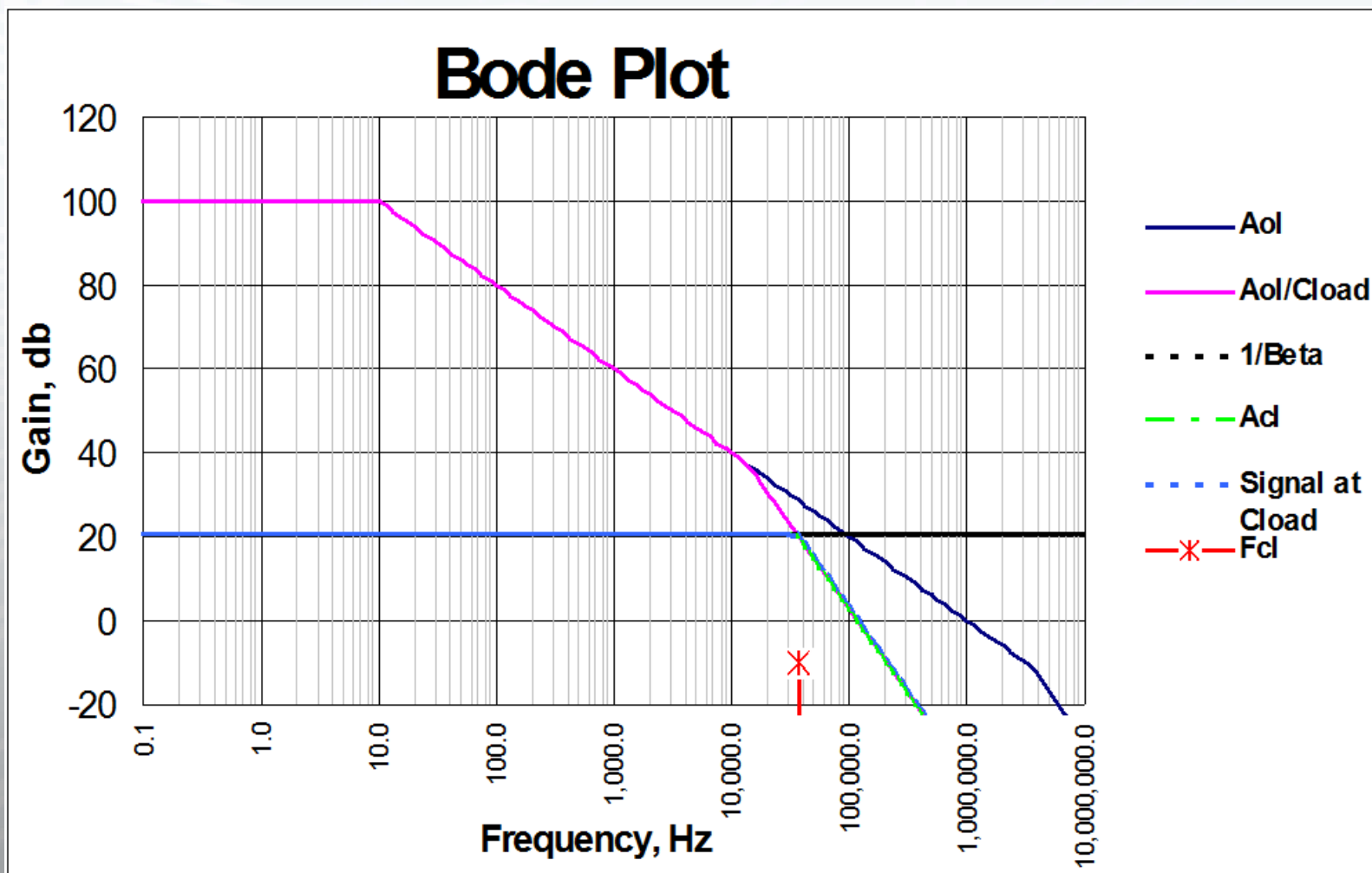
☐ Hide Legends

View Phase Shift

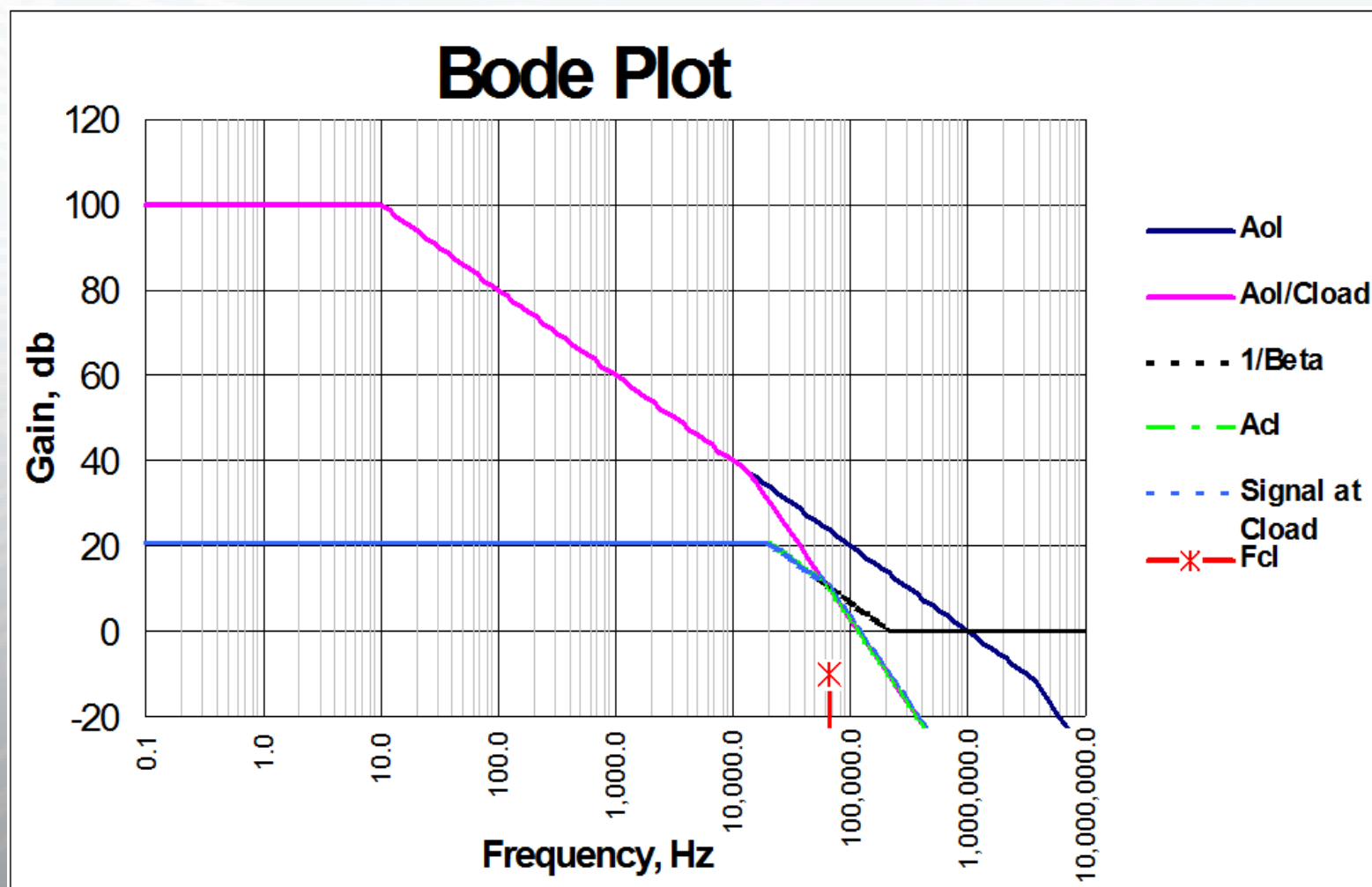
Analysis Results

Maximum Slew Rate	5 V/μs
Closure Frequency	36.5 kHz
Maximum Bandwidth	8.66 kHz
Phase Margin	25.9°
Closure Rate	40 dB/dec

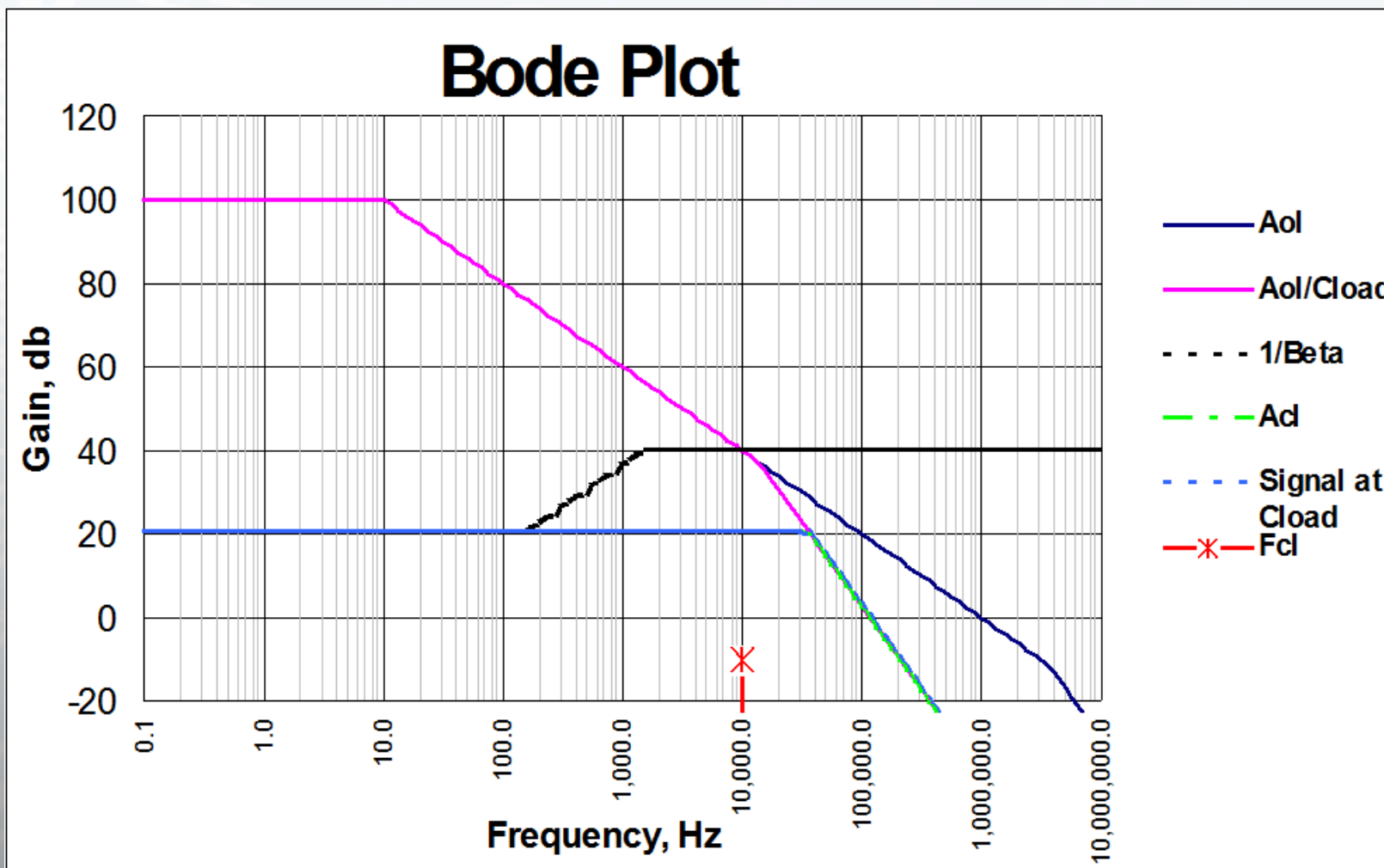
UNSTABLE



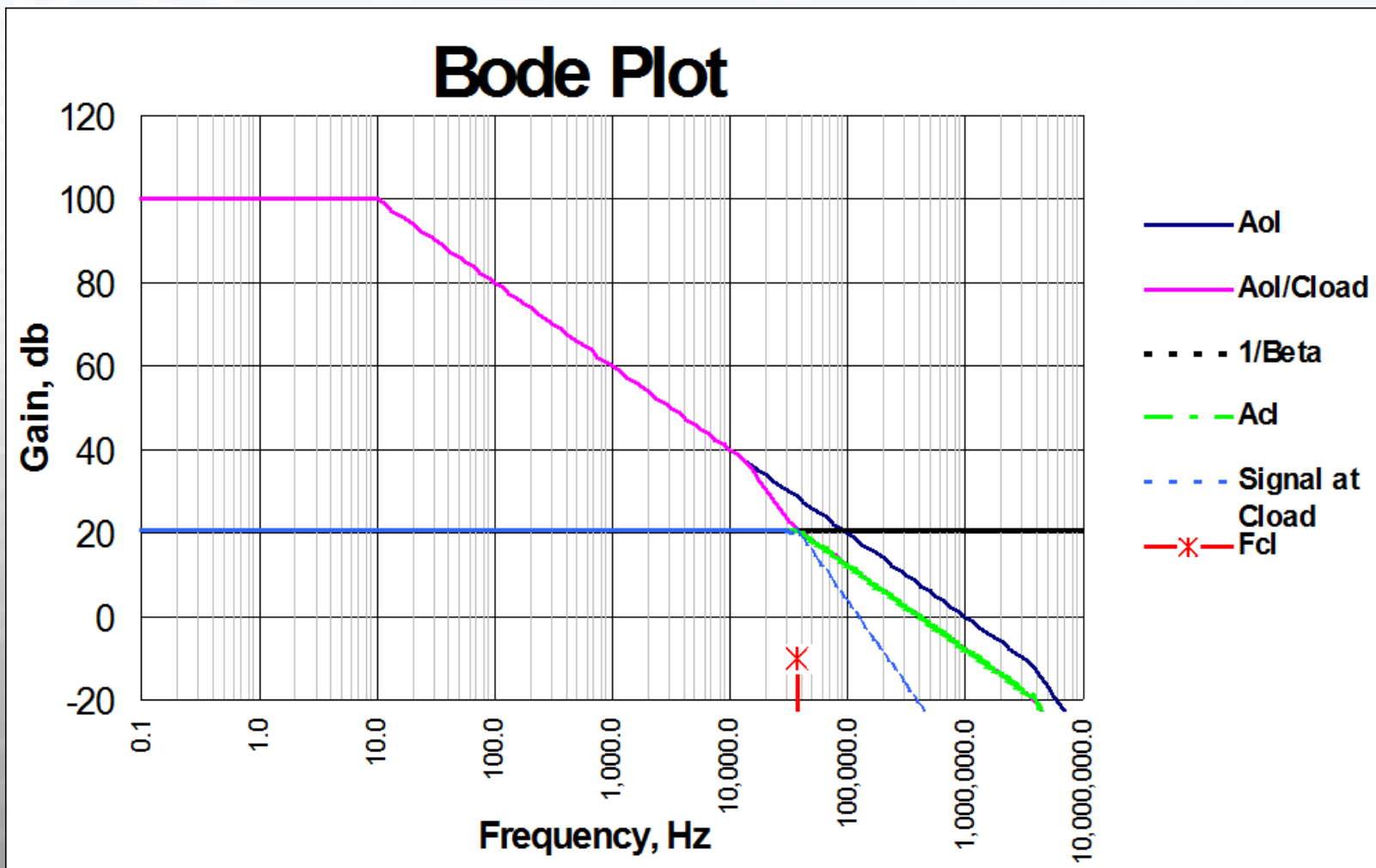
PA07/Clod Cf Solution



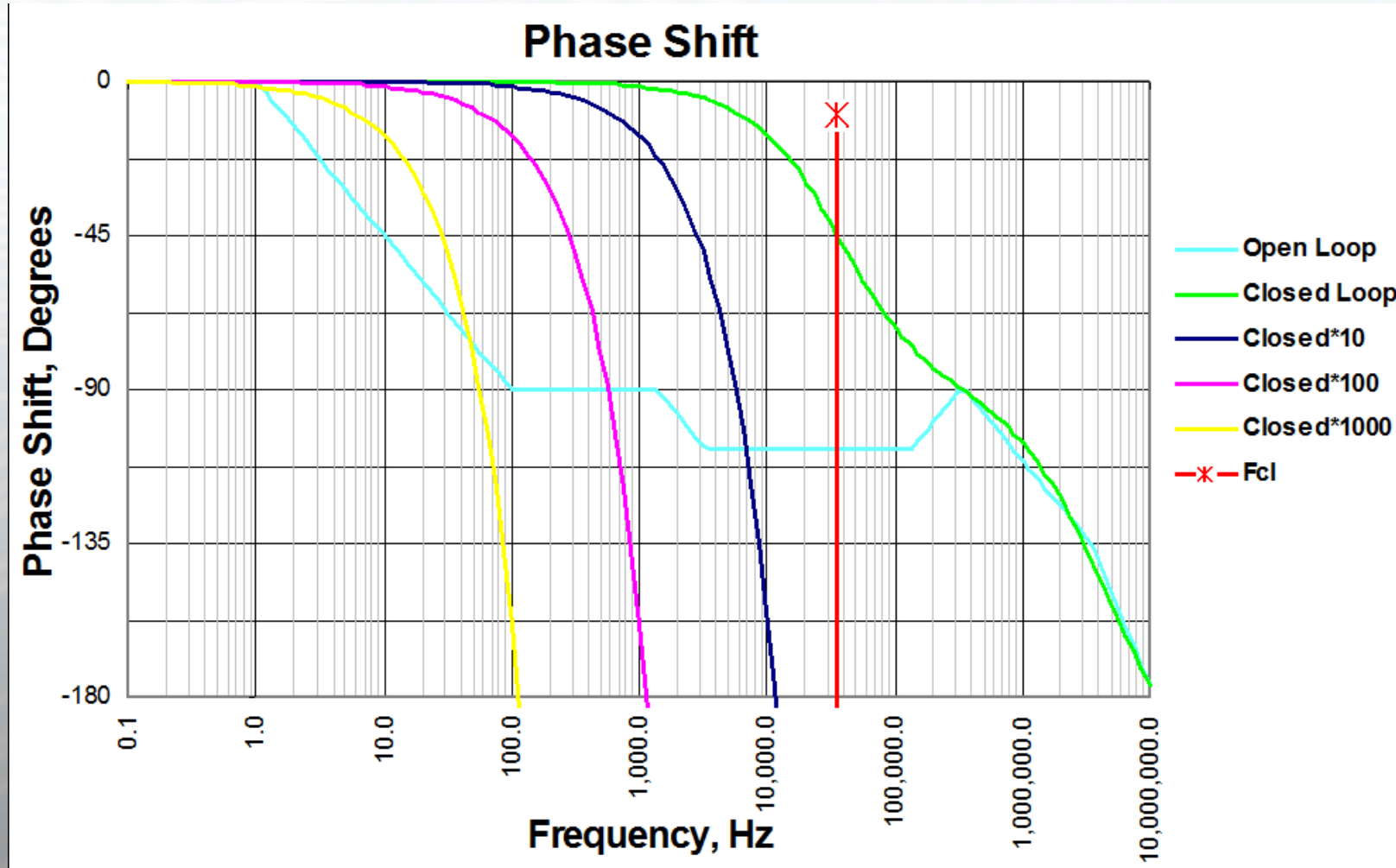
PA07/ Cload Noise Gain Solution



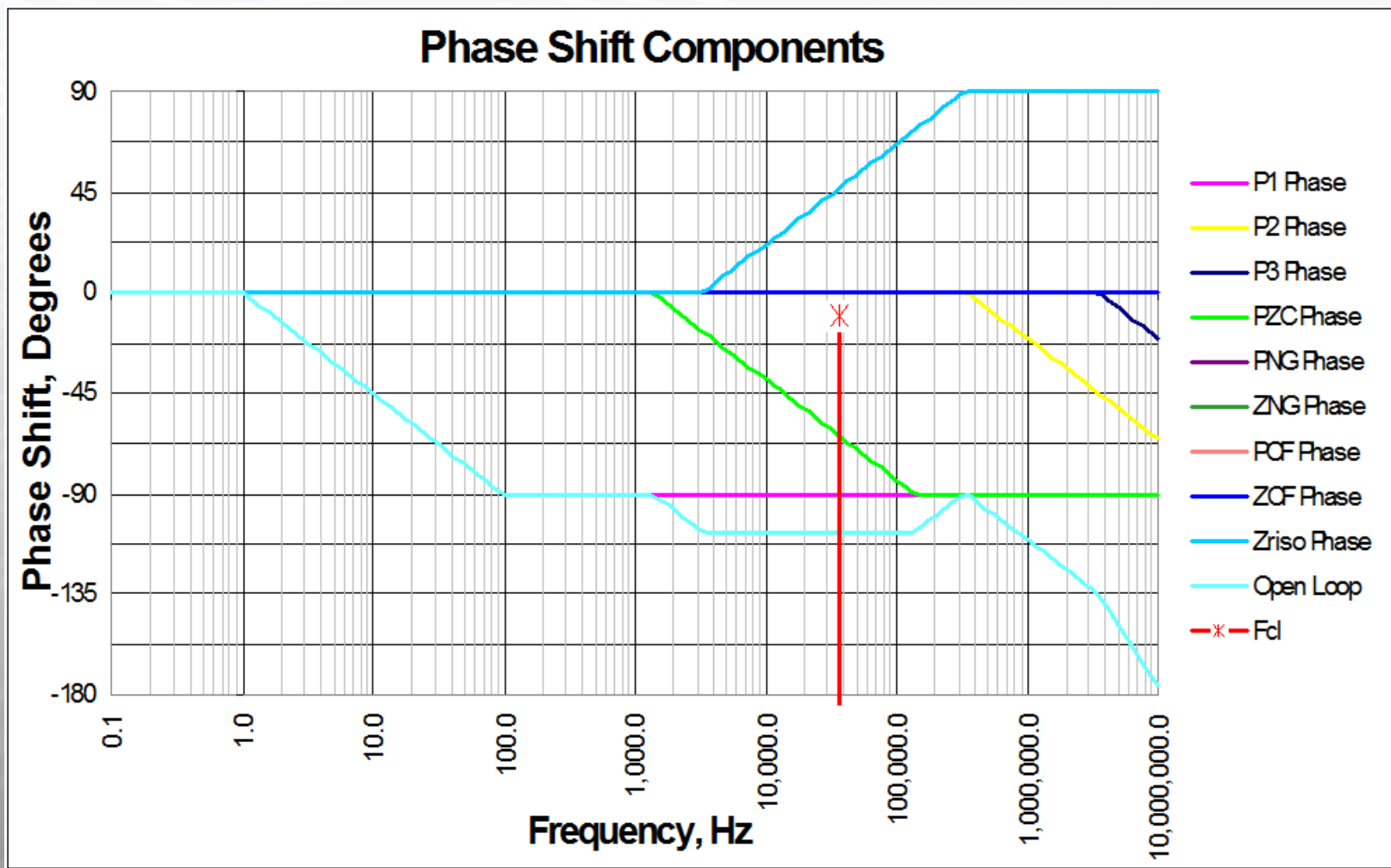
PA07/Cload Riso Solution



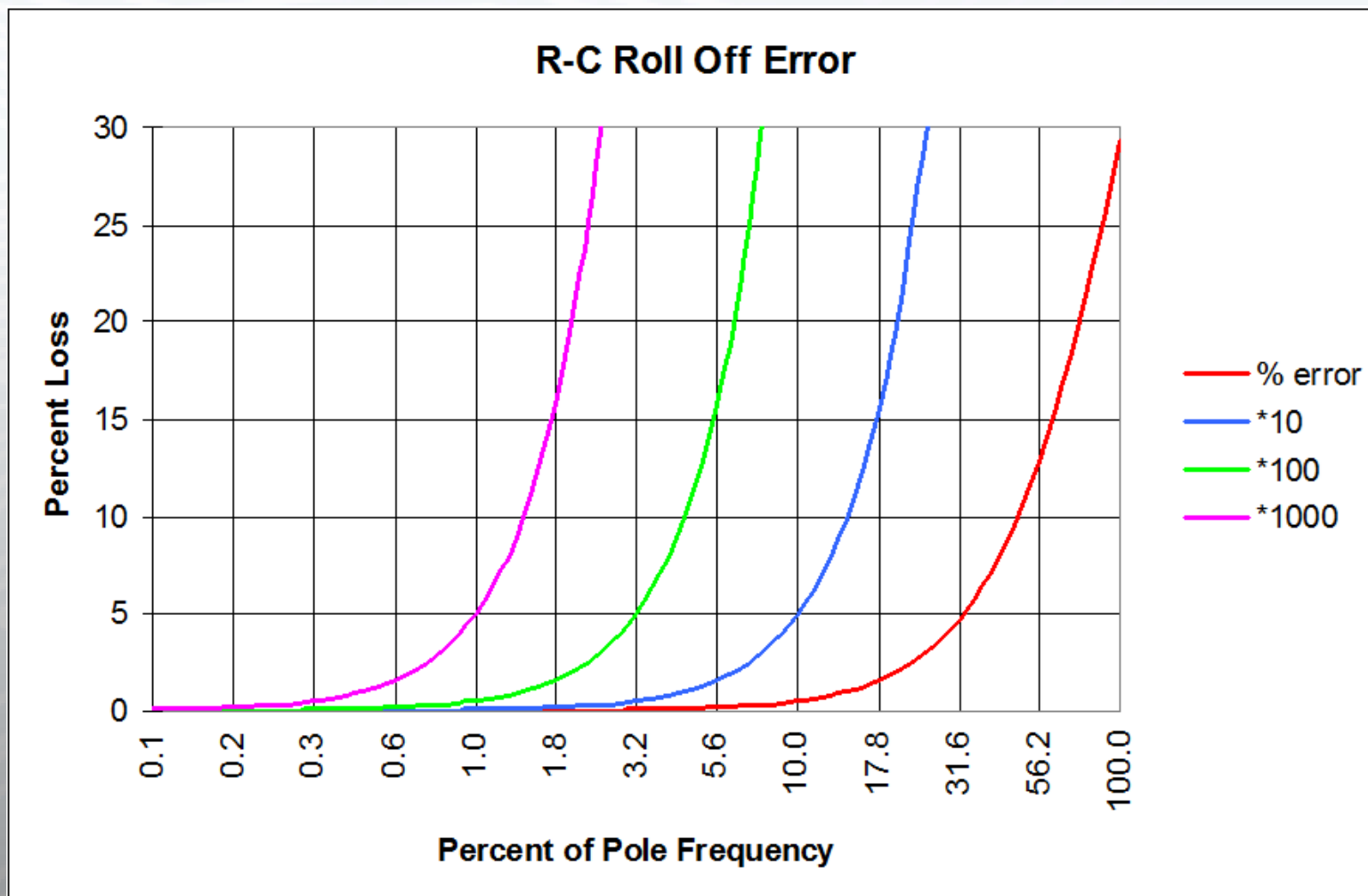
PA07/Clod Riso Phase



PA07/Cload Riso Phase Components

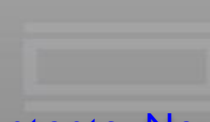
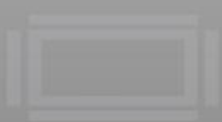


Errors of Straight Lines



Stability and Compensation

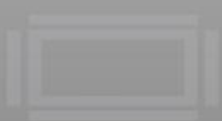
- Ground Loops
- Supply Loops
- Local Internal Loops
- Coupling: Internal and External
- Aol Loop Stability



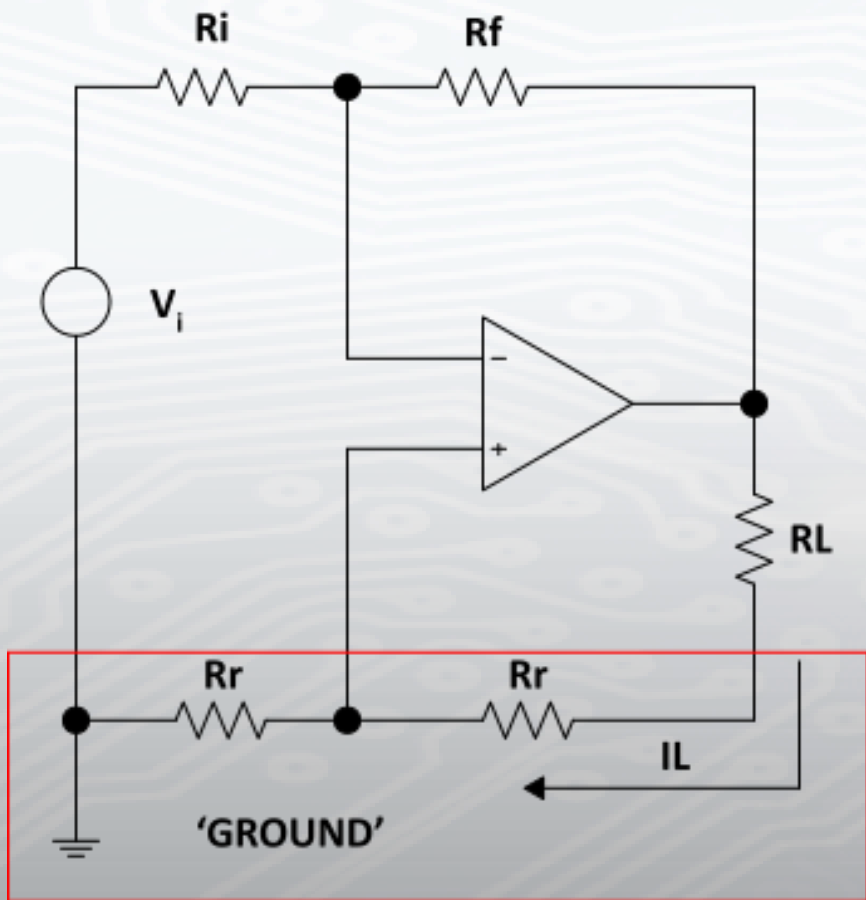
Eliminate Coupling

Internal and External

- ✓ Ground the Case
- ✓ Reduce Impedances
- ✓ Eliminate I_b Compensation Resistor on +IN
- ✓ Don't Run Output Traces Near Input Traces
- ✓ Run I_{out} Traces Adjacent to I_{out} Return Traces

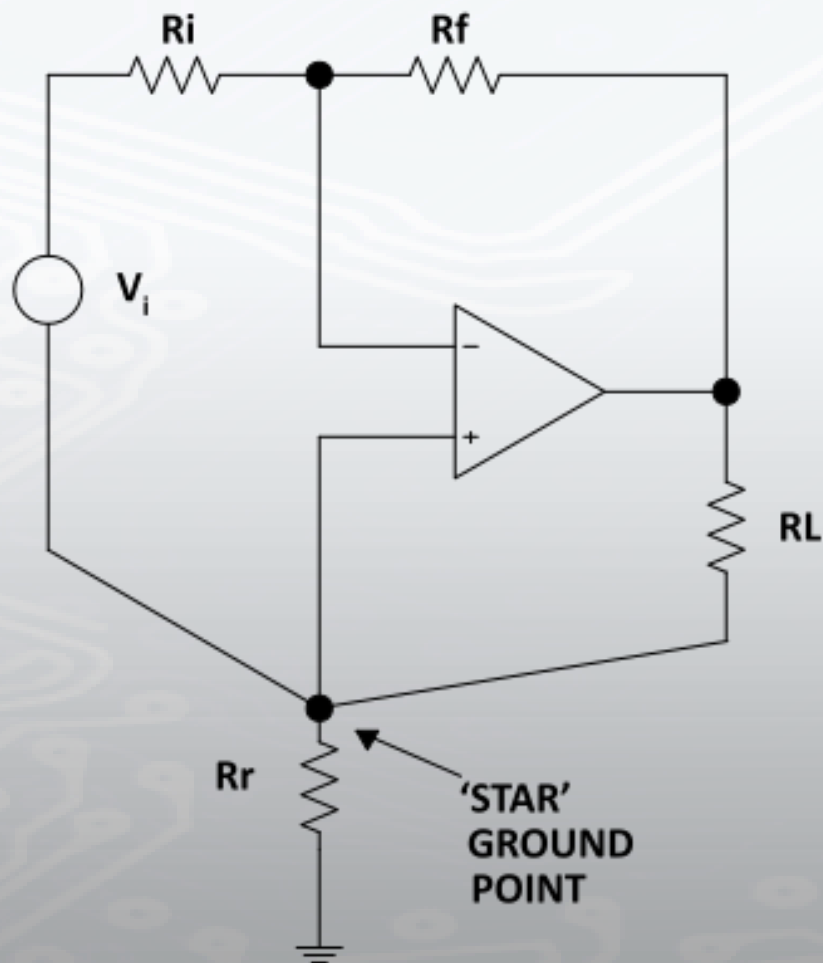


Ground Loops



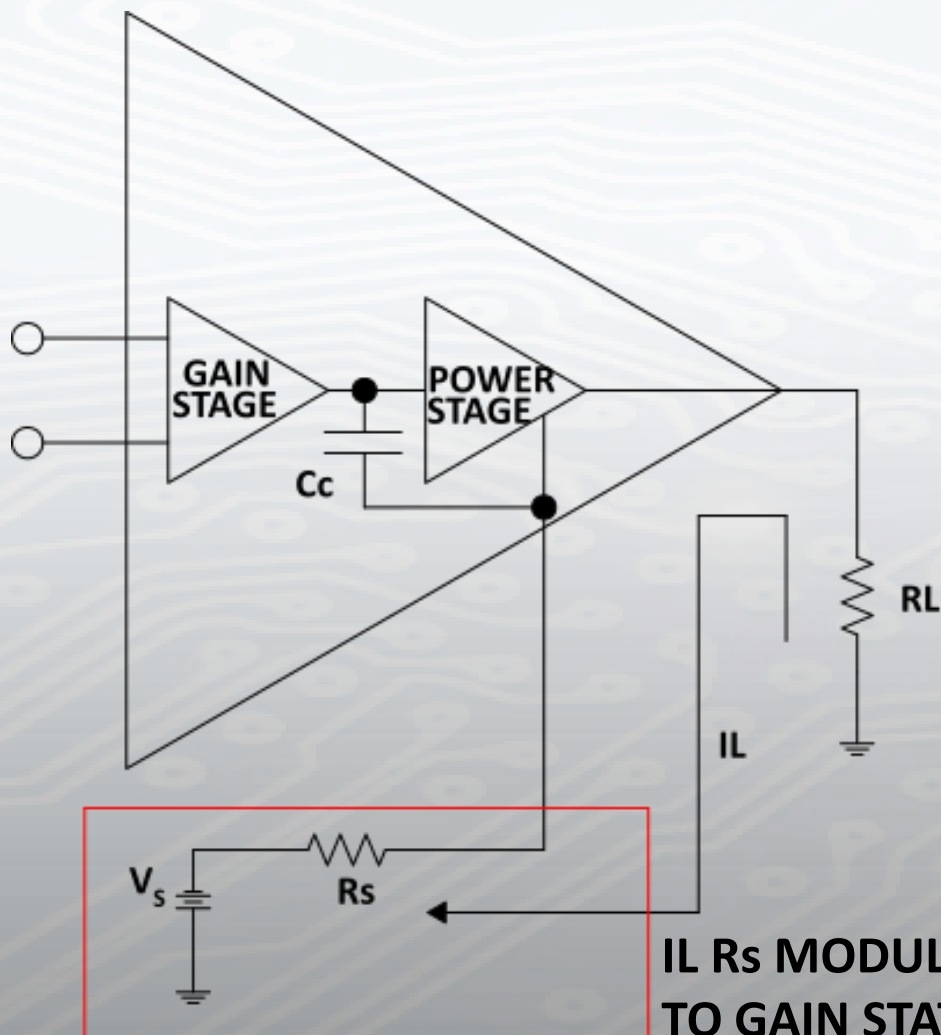
PROBLEM

$$f(\text{osc}) \approx f(\text{unity})$$

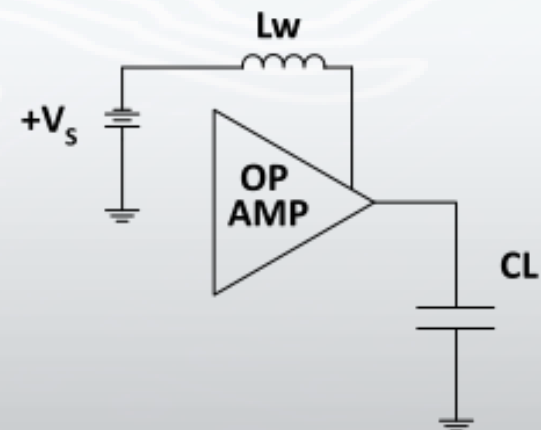


SOLUTION

Supply Line Modulation

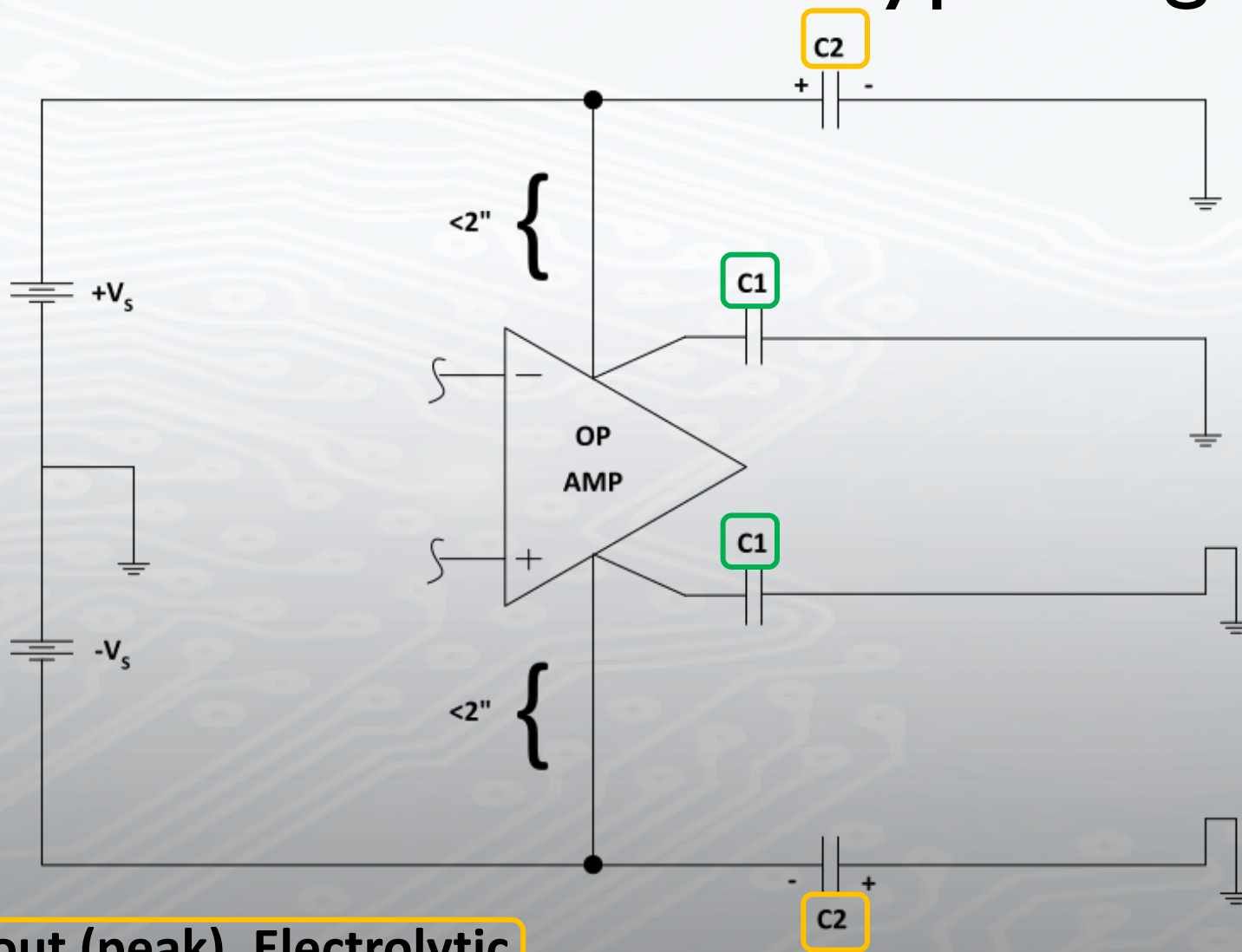


**IL Rs MODULATION FEEDS BACK
TO GAIN STATE**



**Lw & CL FORM
"TANK"**

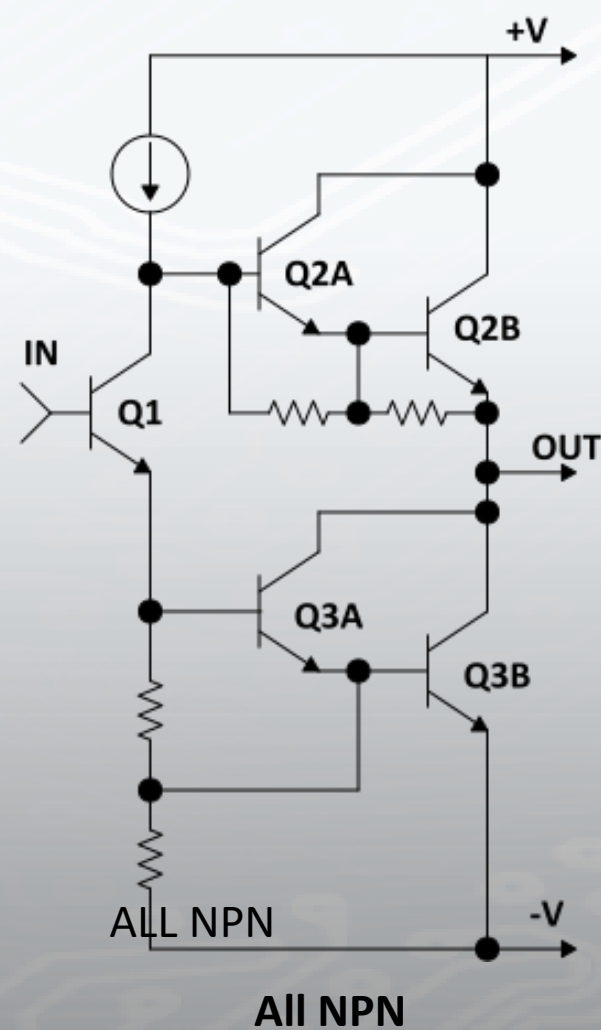
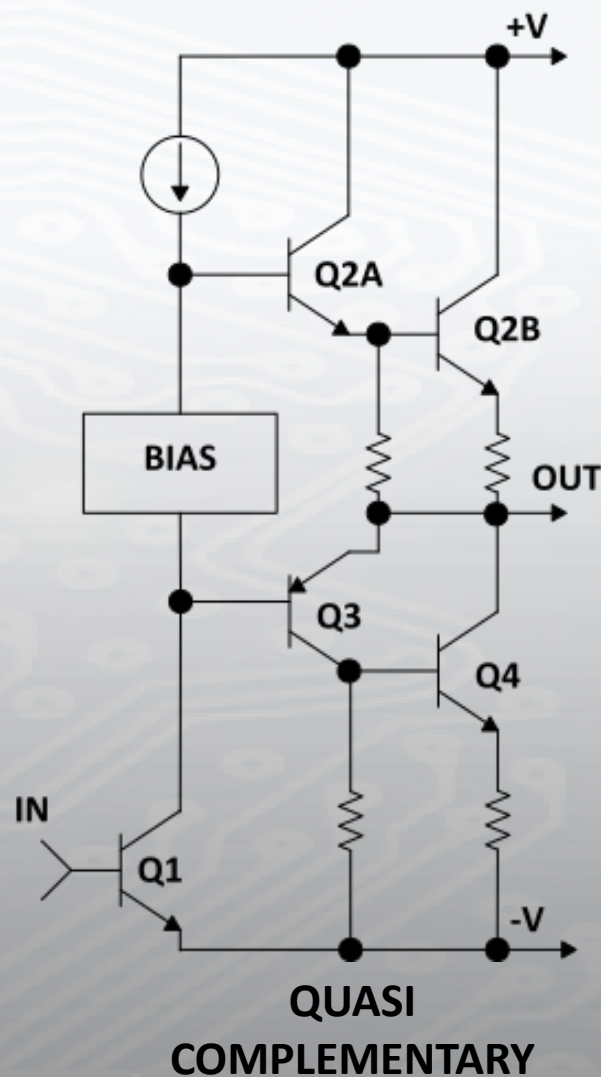
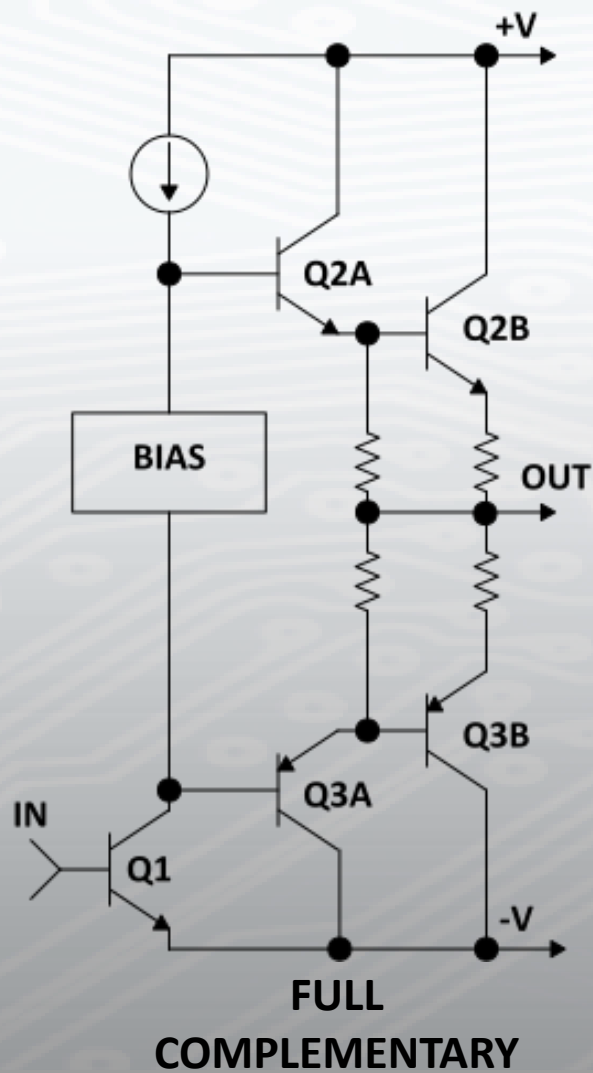
Bypassing Supply Lines



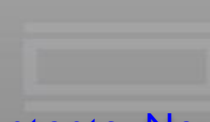
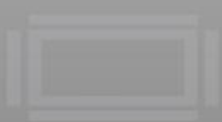
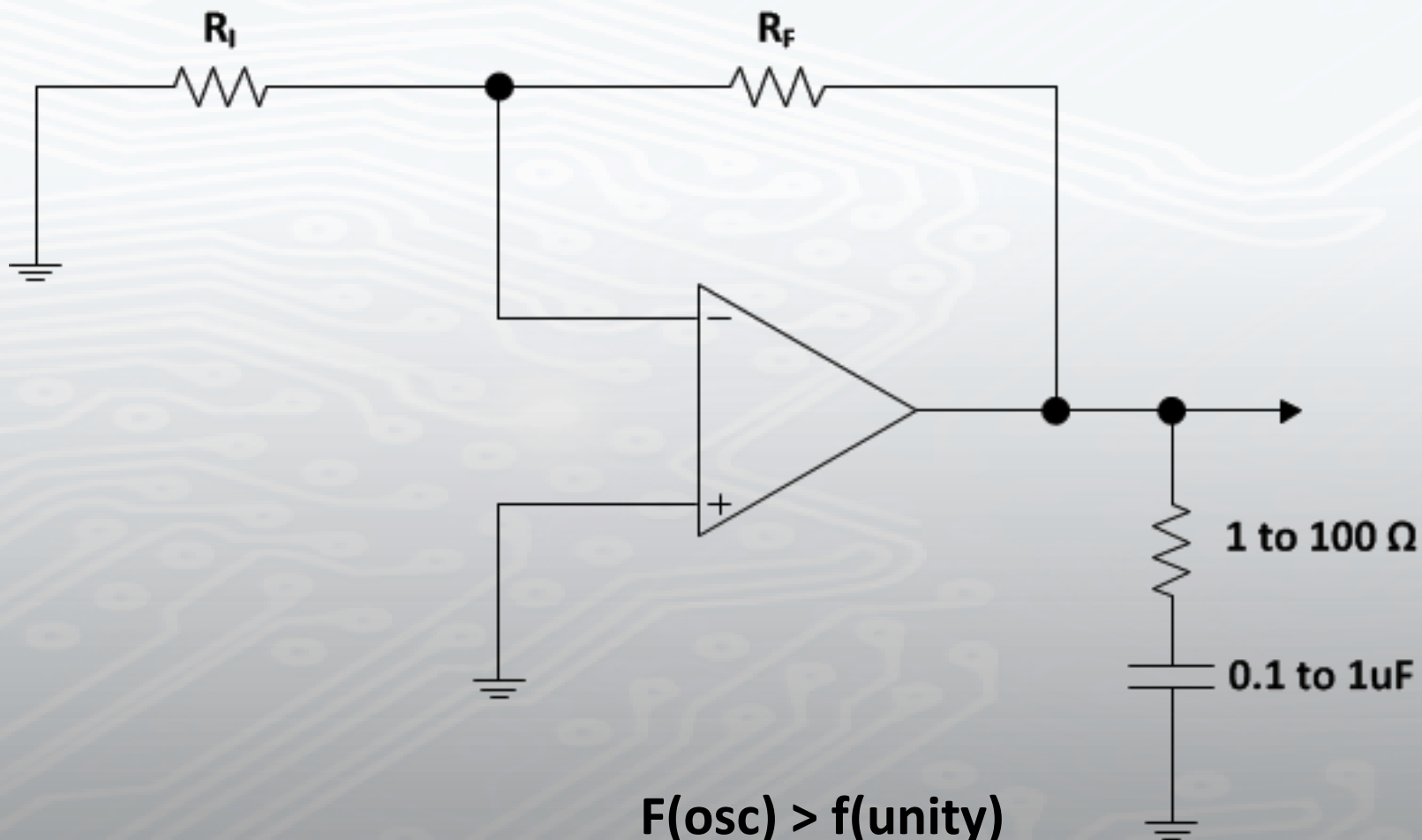
$C1 = 0.1 \text{ to } 1\mu\text{F}$

$C2 = 10\mu\text{F}/\text{Amp out (peak), Electrolytic}$

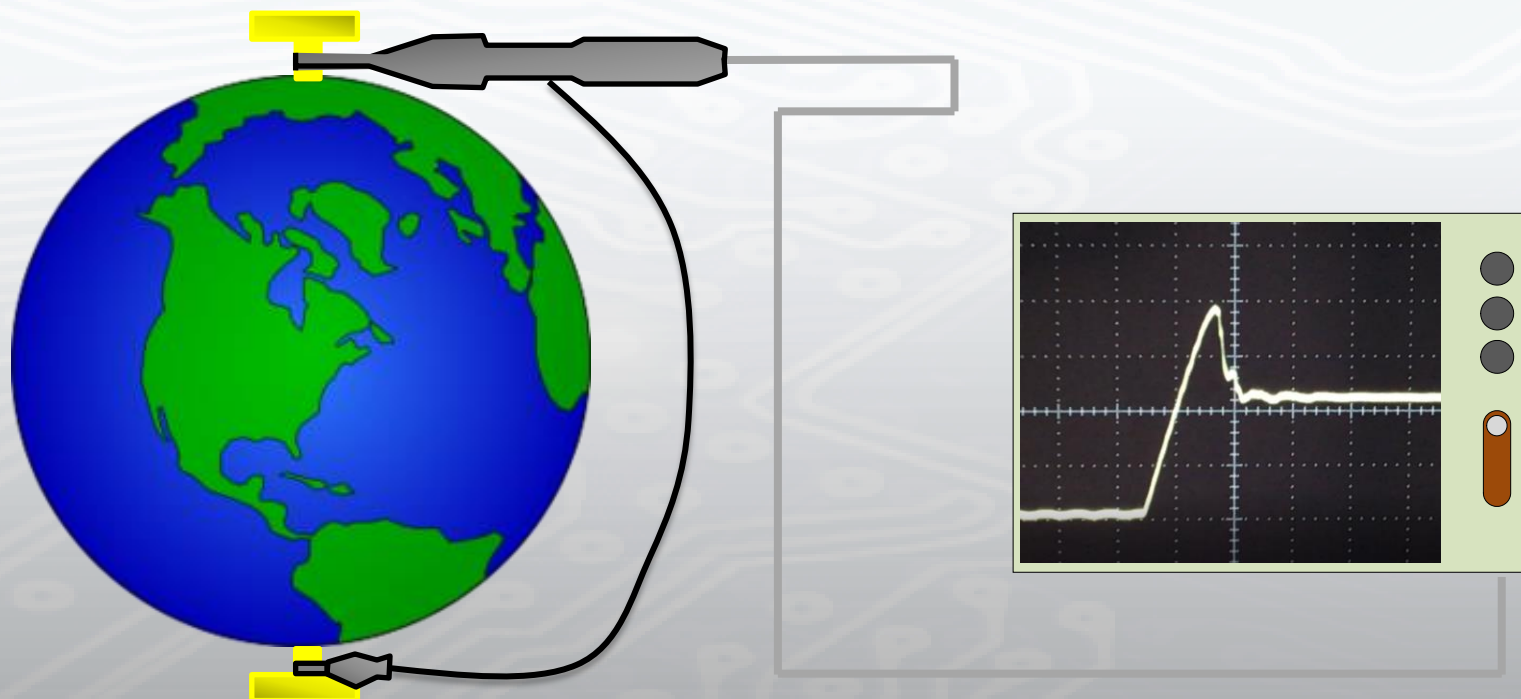
Bipolar Output Stages



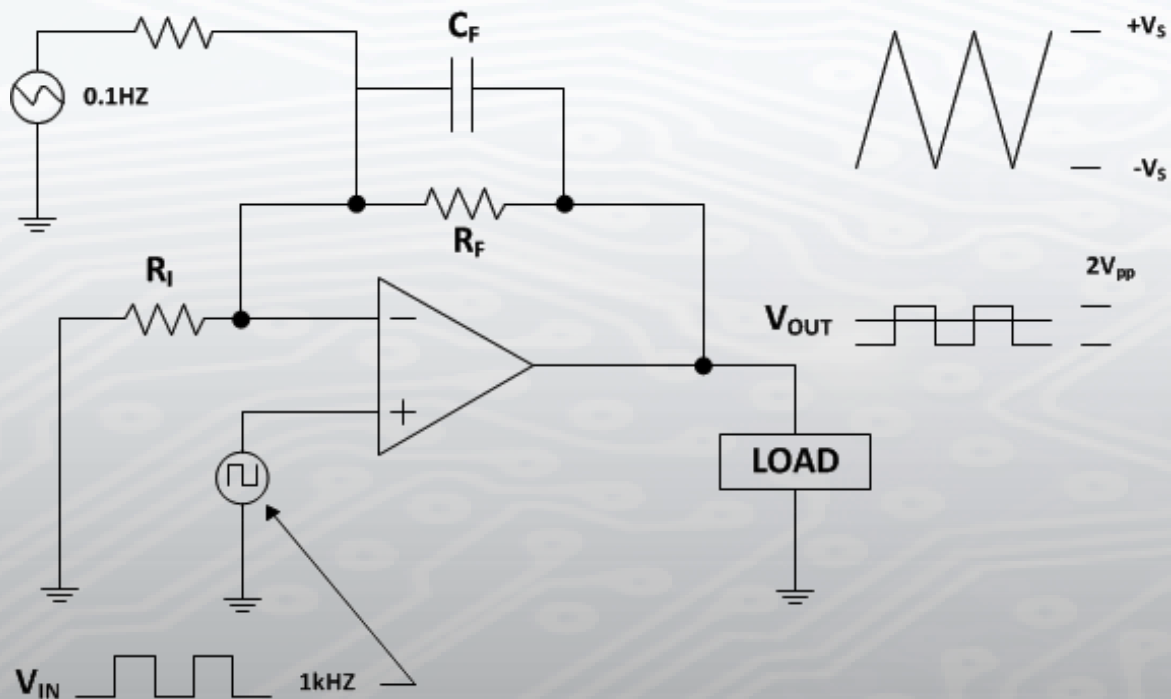
Fixing Output Stage Oscillations



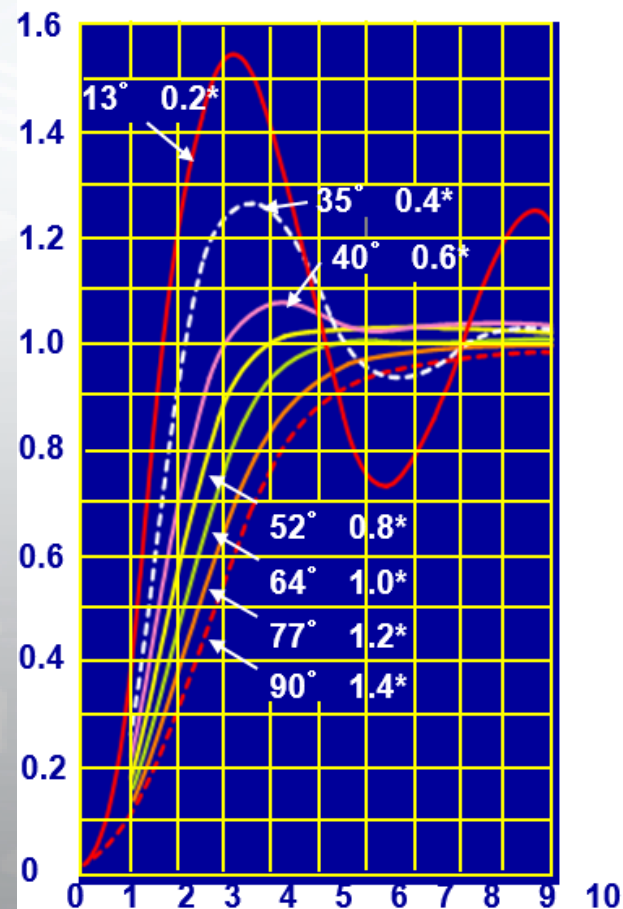
Real World Stability Tests



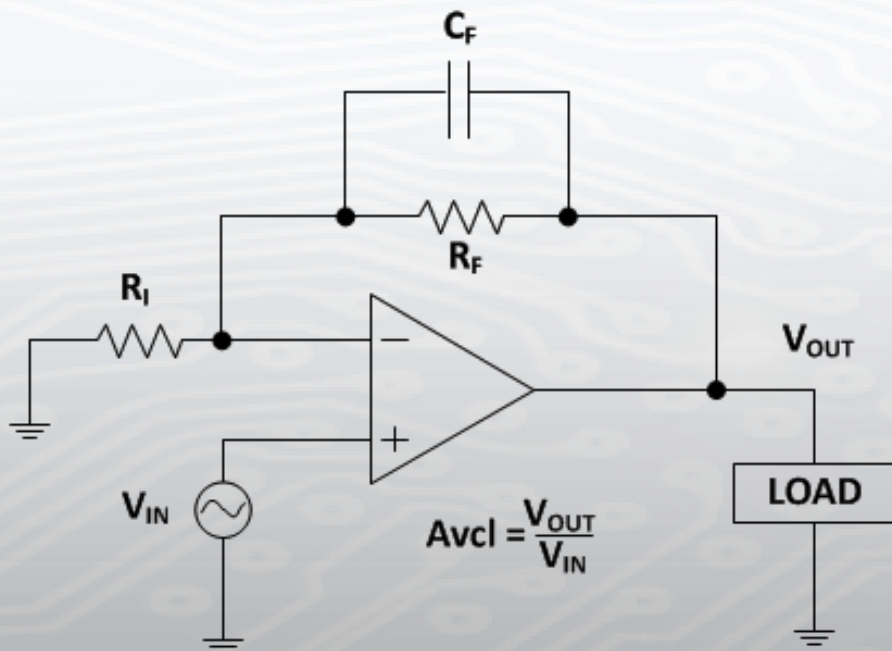
Square Wave Test



SQUARE WAVE TEST

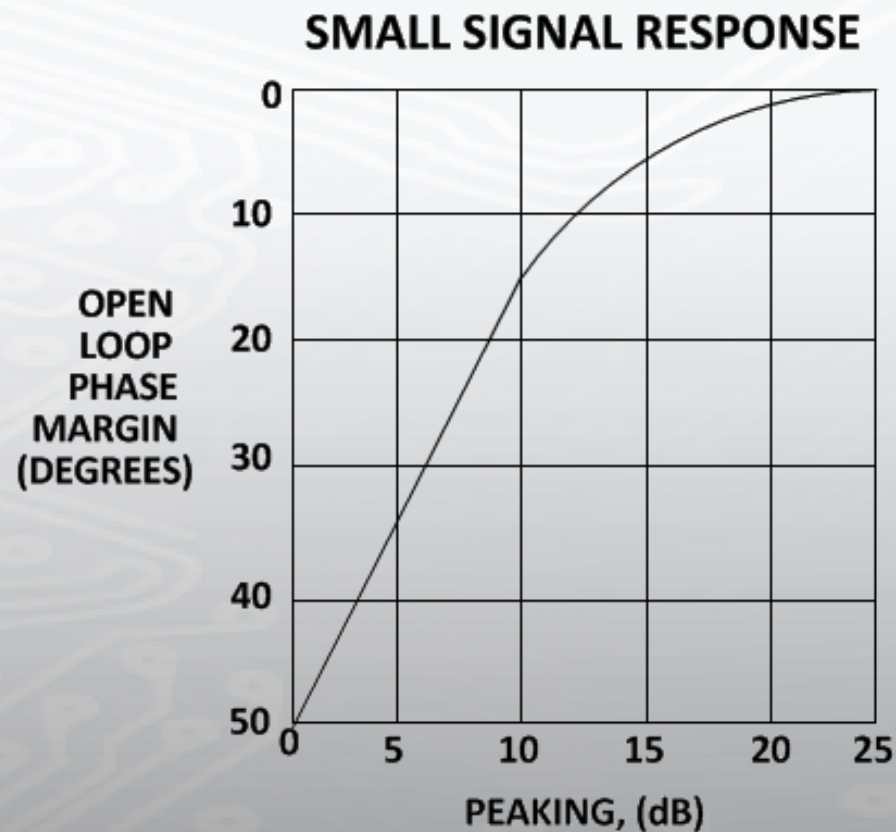


AVcl Peaking Test



PEAKING-MEASURED CLOSED LOOP

$Av_{cl}(\text{dB})$ Peaking



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CONCLUSION





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5980 N Shannon Rd
Tucson, AZ 85741, USA
Phone +1 (520) 690-8600
Email: info@apexanalog.com
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Tucson, AZ 85741, USA
Phone +1 (520) 690-8600
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